Non-Interactive Publicly-Verifiable Delegation of Committed Programs

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Abstract. In this work, we present the first construction of a fully non-interactive publicly-verifiable delegation scheme for committed programs. More specifically, we consider a setting where Alice is a trusted author who delegates to an untrusted worker the task of hosting a program P, represented as a Boolean circuit. Alice also commits to a succinct value based on P. Any arbitrary user/verifier without knowledge of P should be convinced that they are receiving from the worker an actual computation of Alice's program on a given input x.

Before our work, the only object known to imply this challenging form of delegation was a SNARG/SNARK for \mathcal{NP} . This is because from the point of view of the user/verifier, the program P is an unknown witness to the computation. However, constructing a SNARG for \mathcal{NP} from standard assumptions remains a major open problem.

In our work, we show how to achieve delegation in this challenging context assuming only the hardness of the Learning With Errors (LWE) assumption, bypassing the apparent need for a SNARG for \mathcal{NP} .

1 Introduction

We consider a scenario where a trusted software author Alice wishes to make it possible for a set of users to make use of her program P, which we treat as a (non-uniform) Boolean circuit. In particular, this program P may have embedded within it a large proprietary database that Alice's program makes use of. However, Alice neither wants to release her program P nor does she want to host and execute the program herself. Instead she wishes to delegate this computation to an untrusted Worker, and the User/Verifier wants to be certain that they are receiving an output obtained via a computation of Alice's actual program P. As illustrated in Figure 1, the way this works is:

- 1. Alice sends the program P along with some computed state to the Worker, and Alice also publishes a succinct hash H_P of her program, which the User/Verifier obtains. This step is done once and for all.
- 2. An Input Provider chooses an input x, which is sent to both the Worker and the User/Verifier. Note that the input provider could be some public source of information like a news channel of bulletin board, and need not involve the User/Verifier.

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3. Finally, the Worker computes the output y = P(x) along with a succinct proof Π , and sends both of these to the User/Verifier. Steps 2 and 3 may be repeated polynomially many times.

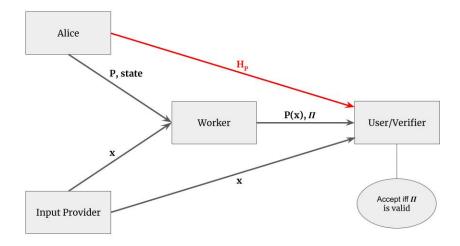


Fig. 1: The Delegation Setup

As illustrated in Figure 1, this process involves no back–and–forth communication. The communication is entirely unidirectional – which we call non-interactive – from left to right. Furthermore, we say that this scenario is succinct if all communication to the User/Verifier, and the runtime of the User/Verifier, is $\operatorname{poly}(\log |P|, \lambda, |x|)$, where λ is a security parameter.

Remark 1. Note that on one hand, the Worker is trusted with the program P by Alice, whereas, it is not trusted by the verifier. This asymmetry of trust is inherent in our setup and is well motivated. In a typical real world situation, the verifier is typically a user on the internet who takes part in a one off interaction with a cloud service for some computation. The need to prove honesty in this situation is significant. On the other hand, Alice might be able to have an agreement with the cloud service before handing over her program, which would make it hard for their Worker to breach trust without consequences.

Comparison to Prior Work. What we have just described is one of the most challenging variants of the classical problem of publicly verifiable delegation which has been the subject of intense work for decades, for many relaxed variations of the model that we describe above.

Specifically, delegation schemes without public verification based on standard assumptions for deterministic and non-deterministic computations have been de-

signed [40,12,38,37,11,22,39,25,1,6,7,24]. Restricting verification to a designated verifier implies that the worker needs to produce a fresh proof unique for each particular verifier for any computation, which is certainly not ideal. Another line of work [15] achieves public verification but does not achieve public delegation. In other words, the input provider needs to run a pre-processing algorithm corresponding to the program P before being able to delegate. Another model which has been extensively explored is when the User/Verifier is allowed to have interaction with the Worker, i.e., interactive delegation. Influenced by the first work on interactive efficient arguments by Kilian [27], there have been several works from standard assumptions [24,33,34,5] and some even unconditional soundness[17,36]. These are however not applicable in our setting where only one-way communication is permitted between the parties, as can be seen in the acyclic graph in Figure 1.

With regard to non-interactive publicly verifiable delegation, Starting from the seminal work on computationally sound proofs by Micali [31] in the random oracle model, there have been several constructions on publicly verifiable non-interactive delegation schemes [2,3,13,4,16,18,28,32] based on the Random Oracle Model or non-standard knowledge assumptions. From more standard assumptions, there have been several works recently [1,6,7,23]. An illustrative example is the recent work of [23] that proposed the first publicly verifiable non-interactive delegation scheme from a falsifiable decisional assumption on groups with bilinear pairings. However, in contrast with the setting we describe above, they can only achieve succinct delegation when the Verifier knows the program P. In our setting of Boolean circuits, this trivializes the delegation problem, since reading P's description takes as long as evaluating P. Indeed, the case that we consider — where Alice's program is large — is extremely well motivated: the program P could be an ML model with billions of painstakingly learned parameters.

The SNARGs for \mathcal{NP} barrier. Why has constructing a protocol that caters to the fully non-interactive setting which we have defined been so elusive? Note that in our problem, the User/Verifier and Input Provider do not know the program P. Hence, from User/Verifier's perspective, P is an \mathcal{NP} witness. Thus, it certainly seems that finding a solution is intricately related to a major goal in the area of non interactive succinct proof systems, i.e., SNARGs for \mathcal{NP} . Unfortunately, the only known constructions of SNARGs for \mathcal{NP} base their soundness on the $Random\ Oracle\ Model$ or non-standard $knowledge\ assumptions$. Finding a solution solely relying on standard assumptions has been an open problem for over a decade. In fact, the closest that we have come is the very recent work achieving SNARGs for \mathcal{P} [10] (see also [26]).

The major technical contribution in our work is to enable *Non-Interactive Publicly Verifiable Succinct Delegation for Committed Programs* without having to use SNARGs for \mathcal{NP} .

Our Contribution: We present the first complete solution to achieving succinct non interactive publicly verifiable delegation for committed programs. Indeed, furthermore, we can also achieve zero-knowledge guarantees as well. Our only computational assumption is the hardness of the Learning with Errors (LWE) problem. Somewhat surprisingly, we show that SNARGs for \mathcal{NP} are not required to solve this problem, even though the statement being proved looks like an \mathcal{NP} statement to the Verifier!

Instead, we show that many ideas from SNARGs for \mathcal{P} [10] can in fact be applied here. Although P is unknown to the User/Verifier, we show that it suffices for Alice to communicate a tiny amount of information of size $\operatorname{poly}(\log |P|)$ about the program P (referred to as H_P) as shown in Figure 1. Because Alice is the author of P, this H_P can be trusted as correctly generated. We stress that Alice does not need to know x to compute H_P , hence this achieves public delegation and public verification in the completely non-interactive model described above. This leads to our main theorem,

Theorem 1. Assuming the hardness of the LWE problem, Figure 2 gives a construction for publicly verifiable non-interactive succinct delegation for committed programs with CRS size, proof size and verifier time $poly(\lambda, \log |P|, |x|)$ and prover run time being $poly(\lambda, |P|)$.

Finally, in order to get zero-knowledge, it suffices for Alice to commit to H_P rather than sending it out in the open. We then present a generic transformation to convert any delegation protocol of this form to attain zero-knowledge.

Theorem 2. Assuming the hardness of the LWE problem and existence of a succinct delegation scheme, Figure 5 gives a construction for publicly verifiable succinct delegation scheme with zero knowledge such that CRS size, proof size and verifier time are $poly(\lambda, log | P|)$ and prover run time is $poly(\lambda, |P|)$.

Finally, we also show how to achieve zero knowledge versions of our delegation scheme, meeting the same strong succinctness and efficiency goals, and under the same assumption (LWE).

We present a more detailed explanation in the Technical Overview.

2 Technical Overview

Our Delegation Scenario Let us briefly recall the setup of our delegation scenario. There are 4 parties, namely, (1) Alice-the program author ProgAuth who sends a program P and some computed state state to a Worker, (2) an Input Provider I that outputs some value x, (3) Worker W that takes as input (P, state, x) and outputs P(x) and a proof Π , and (4) User/Verifier V gets as inputs $(x, P(x), \Pi)$ and outputs 1 if and only if Π was a valid proof. Assume that all the parties get the security parameter λ as an input. An additional requirement is that $|\Pi|$ and runtime of V is $\mathsf{poly}(\lambda, \log |P|, |x|)$, and W runs in time $\mathsf{poly}(\lambda, |x|, |P|)$. Thus, any non-interactive publicly verifiable succinct delegation scheme can be viewed as a collection of 4 algorithms: $\mathsf{sDel} = (\mathsf{ProgAuth}, W, I, V)$ with the input output behaviour and efficiency guarantees as specified. Note that this is indeed a \mathcal{P} computation for the Worker but the primary challenge is that the verifier does

not have knowledge of the "witness" P, hence this is an \mathcal{NP} computation from the verifier's point of view. In this work, we observe that it is indeed feasible to achieve our delegation scenario for all circuits without having to go through SNARGs for \mathcal{NP} . Our technique is based on the recent work of Choudhuri et. al. [10] on SNARGs for \mathcal{P} . We begin by giving a brief overview of their approach and elaborate the challenges of directly incorporating their methodology for our setting.

Challenges of implementing [10] Roughly, the work of [10] uses Batch Arguments for \mathcal{NP} (BARGs), which they build from LWE. BARGs allow an efficient prover to compute a non-interactive and publicly verifiable "batch proof" of many \mathcal{NP} instances, with size $\mathsf{poly}(|w|\log T)$ for T-many \mathcal{NP} statements with each witness of size |w|. They begin by looking at P as a Turing machine and the steps of P's computation are interpreted as an $Index\ Circuit\ C_{\mathsf{index}}$. Say, P terminates in T steps. Formally, they construct a BARG for the $Index\ Language\ \mathcal{L}^{\mathsf{index}}$, where

$$\mathcal{L}^{\text{index}} = \{(C_{\text{index}}, i) | \exists w_i, \text{ such that } C(i, w_i) = 1\},$$

where $i \in [T]$ is an index. Let s_0, s_1, \ldots, s_T denote the encoding of internal states of P along with its tape information, and let Step be its step function such that $Step(s_{i-1}) = s_i$ The witness for the i^{th} intermediate computation is then defined as $w_i = (s_{i-1}, s_i)$. The index circuit is built such that $(C_{\text{index}}, i) \in \mathcal{L}^{\text{index}}$ essentially implies that the Turing machine step function was correctly computed on s_{i-1} to yield s_i . Note that this alone does not suffice as a proof because the BARG only confirms that (s_{i-1}, s_i) and (s'_i, s_{i+1}) are valid witnesses. If $s_{i-1}, s_i, s'_i, s_{i+1}$ are generated by the step function of the same Turing machine P, they must be consistent with each other, i.e., $s_i = s'_i$. However, this is not guaranteed by a BARG.

To resolve this issue, the prover also sends a Somewhere Extractable Hash (SE) to the witnesses $(s_0, \{s_{i-1}, s_i\}_{i \in [T]})$. The extraction property of this hash allows the verifier to check if the witness of two consecutive BARG instances are indeed consistent with each other. At this stage, we would like to remind the reader of their efficiency goals where crucially, they desire proof size and verification time to be $poly(\lambda, \log T)$. However, note that $|C_{index}|$ grows linearly with $|s_i|$ and the known constructions [20] of SE hashes can only produce hashes with size $poly(|s_i|)$. This means that total communication and verifier run time will be at least $poly(|s_i|)$. This is certainly no good if the Turing machine has massive states. To overcome this final barrier, they make use of Hash Trees which compress the states s_i to a short hash h_i such that $|h_i| = poly(\lambda)$. Such trees [30] also have a soundness property where a Prover must produce a succinct proof Π_i that the hash tree was indeed implemented correctly at the i^{th} step of the Turing machine computation. Once the succinctness guarantee is ensured, the prover then produces SE hashes corresponding to $(h_0, \Pi_0, \{h_{i-1}, \Pi_{i-1}, h_i, \Pi_i\}_{i \in [T]})$ along with the openings to these hashes. To summarise, the proof consists of two parts, (1) The BARG proof, and (2) A somewhere extractable hash of the witnesses. Relying on the soundness of BARG, extraction correctness property of SE hash and soundness of the Hash Tree, a User/Verifier can check if each of these T intermediate steps are indeed the correct states for P, i.e., the computation was done honestly.

However, this approach only works if User/Verifier can confirm that the inputs used for the computation by the Worker, i.e. (P, x) are indeed the correct starting values as provided by the Program Author and Input Provider. This works fine for [10] because in their setting, the User/Verifier actually knows (P, x). Unfortunately, this is not at all true in our scenario. Thus, the techniques of Choudhuri et al. [10] cannot be implemented directly as the soundness of the BARG proof cannot provide any guarantees if there is no way for to check that the initial inputs used by the Worker are correct.

Our Idea. We start with an alternate way of interpreting the computation of P on input x as the following: Consider a Circuit-Universal Turing Machine \mathcal{TM} which takes as input P, x, y and accepts (P, x, y) in $T = \tilde{O}(|P|)$ steps if P(x) = y. We can assume without loss of generality that $P \in \{0, 1\}^m$, $x \in \{0, 1\}^n$ and $y \in \{0, 1\}$, where $m, n \leq 2^{\lambda}$. Keeping this in mind, we introduce the notion of Semi-Trusted SNARGs for \mathcal{NP} . This new kind of SNARG is one that will work for general \mathcal{NP} computations, but only with a little bit of extra help from a trusted party that knows the witness – which in our delegation scenario is Alice, who knows the witness P!

A Semi-Trusted SNARG is a tuple of algorithms: stSNARG = (Setup, TrustHash, P, V), where (1) Setup is a randomised algorithm that takes as input the security parameter and outputs a Common Random String (CRS). (2) a trusted deterministic TrustHash takes as input the (CRS, P) and outputs a digest H_P , (3) a deterministic prover P which takes as input CRS and (P, x, y), and outputs a proof Π , and (4) a deterministic verifier V which gets CRS, (H_P, x, y, Π) as input and outputs 1 iff Π is valid. It must be that $|\Pi|$ and run time of V is $\operatorname{poly}(\lambda, \log T)$, and P runs in time $\operatorname{poly}(\lambda, |x|, |P|, T)$. A simple reduction shows that in the CRS model (or alternatively in a model where Alice chooses the CRS), existence of stSNARG implies the existence of sDel. We show this formally in Lemma 11. Hence, from here onwards, our goal is to construct a Semi-Trusted SNARG for \mathcal{NP} .

We briefly provide an informal explanation of our construction.

Like [10], every intermediate state of the Universal Turing Machine is encoded into a succinct hash (call it h_0, \ldots, h_T) accompanied with succinct proofs $\{\Pi_i\}_{i\in[T]}$. The prover computes two independent copies of Somewhere Extractable (SE) hashes (c_1, c_2) of the encoding $\{h_0, \{(h_1, \Pi_1), \ldots, (h_T, \Pi_T)\}\}$ along with their corresponding openings. Here $h_0 = (\mathsf{st}_0, H_P, H_x, H_{work})$, where st_0 is that hash of $\mathcal{T}\mathcal{M}$'s starting state which is publicly known, H_x denote the hash of x, and H_{work} is the hash of $\mathcal{T}\mathcal{M}$'s blank work tape. The use of two independent SE hashes are pivotal for soundness which we elaborate later.

We point out that TrustHash computes H_P using the same hash tree which is used for hashing the Turing machine states by the Prover. This is crucial to ensure soundness of the protocol. We show in Figure 3 that once the public hash is fixed by TrustHash, one can hard code $(y, c_1, c_2, T, H_P, H_x)$ to the index circuit

 $C_{\rm index}$ for BARG. At this point, we can now follow the approach from [10]. V can rely upon the binding property/collision resistance of the hash to ensure that the prover has used P and x which were provided by Alice and the input provider respectively. The main observation here is that once a trusted party fixed a hash of the program P and V is convinced that computation was commenced with the correct inputs, the soundness of BARG, extraction correctness of the SE hash and soundness of hash tree ensures that the semi-trusted SNARG construction is sound.

While our proof of soundness closely follows the blueprint of [10], we choose to present our proof in a different, and arguably simpler, way. In [10], No-Signaling Somewhere Extractable(NSSE) hashes are used extensively. In our proof, we choose to omit explicit use of this notion, and instead we make direct use of two independent SE hashes as mentioned above. A simple hybrid argument then gives a straightforward proof for soundness. This shows that the "anchor and step" use of SE hashes, which dates to the introduction of somewhere-binding hashes [20] in 2015, is directly sufficient for this proof of soundness.

Zero-Knowledge We have only discussed soundness guarantees thus far. However, in our delegation scenario, it might also be extremely important to ensure that no information about P leaked to V during the delegation process. Hence it is important to add zero-knowledge guarantees to our protocol. We finally give a generic transformation to modify a semi-trusted SNARG to add zero knowledge guarantees. In order to do so we make use of a statistically binding extractable commitment scheme and a NIZK 1 , and roughly make the following modifications:

- We add an additional commitment to 0 in the CRS which is never used in the proof but helps in proving zero knowledge.
- The public hash output by TrustHash is a binding commitment C_P of H_P . It then sends (P, H_P) to the worker W only.
- The SE hashes c_1, c_2 are also committed as a part of the proof and not published in the open.
- The prover wraps the BARG Π with a NIZK which proves that that the BARG verification circuit indeed accepts the BARG proof.
- The Verifier then checks if the NIZK proof is valid.

The binding and hiding property of the commitment, and witness indistinguishability of NIZK guarantees zero knowledge.

3 Preliminaries

We use some standard tools as building blocks to perform the Succinct Delegation.

¹Multi-thoerem NIZK from LWE is possible by combining [35] and [14]. Note that the weaker notion NIWI would also suffice to achieve zero knowledge in our setting.

- Somewhere Extractable Hash [20,10,9]:
 SE = (SE.Gen, SE.TGen, SE.Hash, SE.Open, SE.Verify, SE.Ext)
- Non Interactive Batch Arguments (BARG) for Index Language[10]:
 BARG = (BARG.Gen, BARG.TGen, BARG.Prove, BARG.Verify)
- Hash Tree[23,30]:
 - $\mathsf{HT} = \mathsf{HT}.\mathsf{Gen}, \mathsf{HT}.\mathsf{Hash}, \mathsf{HT}.\mathsf{Read}, \mathsf{HT}.\mathsf{Write}, \mathsf{HT}.\mathsf{VerRead}, \mathsf{HT}.\mathsf{VerWrite}$
- Non Interactive Zero Knowledge Argument[35,8,19]:
 (NIZK = NIZK.Gen, NIZK.Prove, NIZK.V)
- Statistically Binding Extractable Commitment[29]:
 Com_{bind} = (Com.Gen, Com.TGen, Com.C, Com.Ext)

We use all the primitives in a standard way as prior works. The hash tree can be constructed from any collision resistant hash function. The others are known to be instantiated from LWE. Formal definitions and properties of the primitives can be found in the Supplementary Material.

4 Publicly Verifiable Non Interactive Succinct Delegation

We formally define the notion of Publicly Verifiable Non Interactive Succinct Delegation (sDel) which is similar to the definition proposed in prior works [21]. Such a delegation scheme in the CRS model involves the following PPT algorithms, (1)Software/Program Author ProgAuth (3)Cloud Worker W, and (3) Verifier VAn sDel comprises of the following polynomial time algorithms:

- sDel.Setup(1^{λ}): A randomized setup algorithm which on input security parameter λ and outputs crs.
- sDel.ProgAuth(1 $^{\lambda}$, crs): A program author which takes as input λ , outputs a (not public) program $P \in \{0,1\}^m$, $m \leq 2^{\lambda} \in \mathbb{N}$, state and a public digest H_P .
- sDel. $W(\text{crs}, P, \text{state}, H_P, x)$: A deterministic cloud worker which on input crs, program P, input $x \in \{0, 1\}^n, n \leq 2^{\lambda} \in \mathbb{N}$ outputs a value y and proof Π .
- sDel. $V(crs, x, y, H_P, \Pi)$: A deterministic verifier which on input crs, digest H_P, x, y, Π either accepts or rejects.

A publicly verifiable succinct delegation scheme ($\mathsf{sDel}.\mathsf{Setup}, \mathsf{sDel}.\mathsf{ProgAuth}, \mathsf{sDel}.W, \mathsf{sDel}.V$) satisfies the following properties:

- Completeness. For every PPT program generating algorithm sDel.ProgAuth, every $\lambda, n, m \in \mathbb{N}$, and for all $x \in \{0, 1\}^n$ such that $n, m < 2^{\lambda}$, we have

$$\begin{split} \Pr[\mathsf{sDel}.V(\mathsf{crs},x,y,H_P,\Pi) &= 1 \land P(x) = y \big| \mathsf{crs} \leftarrow \mathsf{sDel}.\mathsf{Setup}(1^\lambda), \\ & ((P,\mathsf{state}),H_P) \leftarrow \mathsf{sDel}.\mathsf{ProgAuth}(1^\lambda,\mathsf{crs}), \\ & (y,\Pi) \leftarrow \mathsf{sDel}.W(\mathsf{crs},P,\mathsf{state},H_P,x)] = 1. \end{split}$$

- **Efficiency.** sDel.Setup runs in time $poly(\lambda)$, sDel.W runs in time $poly(\lambda, |P|, |x|)$ and outputs a proofs of length $poly(\lambda, \log |P|, |x|)$, and sDel.V runs in time $poly(\lambda, \log |P|, |x|)$.

- Soundness. For every PPT adversary $\mathcal{A} := (\mathcal{A}_1, \mathcal{A}_2)$, every PPT program generating algorithm sDel.ProgAuth, and the tuple $n = n(\lambda), m = m(\lambda)$, there exists a negligible function $\mathsf{negl}(\lambda)$ such that for every $\lambda \in \mathbb{N}$,

$$\begin{split} \Pr[\mathsf{sDel}.V(\mathsf{crs},x,y,H_P,\Pi) &= 1 \land P(x) \neq y \big|, \mathsf{crs} \leftarrow \mathsf{sDel}.\mathsf{Setup}(1^\lambda), \\ &\qquad \qquad ((P,\mathsf{state}),H_P) \leftarrow \mathsf{sDel}.\mathsf{ProgAuth}(1^\lambda,\mathsf{crs}), \\ &\qquad \qquad (x,\mathsf{aux}) \leftarrow \mathcal{A}_1(1^\lambda,\mathsf{crs}), (y,\Pi) \leftarrow \mathcal{A}_2(\mathsf{crs},P,\mathsf{state},H_P,x,\mathsf{aux})] \\ &\qquad \qquad \leq \mathsf{negl}(\lambda). \end{split}$$

To construct sDel, we introduce a notion of *Semi-Trusted Succinct Non-Interactive Arguments* stSNARG which we formally introduce and construct in Section 5. After that, we prove the following lemma (cf. Lemma 11) which shows how to construct sDel using stSNARG as a building block.

Lemma 1. Assuming $T = \mathsf{poly}(m,n)$, $T,m,n \leq 2^{\lambda}$, the stSNARG protocol in Figure 2 implies the unconditional existence of a publicly verifiable non interactive succinct delegation scheme sDel as defined above.

4.1 sDel with Zero-Knowledge

A publicly verifiable non interactive succinct delegation scheme with zero knowledge zk - sDel is defined by the following efficient algorithms:

- $\mathsf{zk} \mathsf{sDel}.\mathsf{Setup}(1^{\lambda})$: A randomized setup algorithm which on input security parameter λ and outputs crs .
- zk $\mathsf{sDel}.\mathsf{ProgAuth}(1^\lambda,\mathsf{crs})$: A program author which takes as input λ , generates a program $P \in \{0,1\}^m$, $m \leq 2^\lambda \in \mathbb{N}$. Additionally, it computes a digest H_P and creates a statistically binding and extractable commitment C_P of H_P under randomness r. Finally it sends a private output (P,state) and public output C_P . Here state contains the randomness r and H_P encoded in it along with any other state information.
- $\mathsf{zk} \mathsf{sDel}.W(\mathsf{crs}, P, \mathsf{state}, C_P, x)$: A deterministic cloud worker which on input crs , program P, commitment C_P , $x \in \{0,1\}^n, n \leq 2^{\lambda} \in \mathbb{N}$ outputs a value y and proof Π .
- $\mathsf{zk} \mathsf{sDel}.V(\mathsf{crs}, x, y, C_P, \Pi)$: A deterministic verifier which on input $(\mathsf{crs}, C_P, x, y, \Pi)$ either accepts or rejects.

Apart from the Completeness, Efficiency and Soundness guarantees mentioned above, a publicly verifiable succinct delegation scheme (zk - sDel.Setup, zk - sDel.ProgAuth, zk - sDel.W, zk - sDel.V) satisfies the fol-

(zk - sDel.Setup, zk - sDel.ProgAuth, zk - sDel.W, zk - sDel.V) satisfies the following additional property:

Non Interactive Zero Knowledge. For all $\lambda, n, m \in \mathbb{N}$ such that $n, m \leq 2^{\lambda}, \forall, x \in \{0,1\}^n$ and $y \in \{0,1\}$, there exists a PPT simulator $\mathsf{Sim} := (\mathsf{Sim}_1, \mathsf{Sim}_2, \mathsf{Sim}_3)$ such that the distributions of

$$(\mathsf{crs}, x, y, C_P, \Pi) \big| (\mathsf{crs}, \mathsf{aux}) \leftarrow \mathsf{Sim}_1(1^{\lambda}), (C_P, \mathsf{aux}') \leftarrow \mathsf{Sim}_2(\mathsf{crs}, \mathsf{aux}), \\ (y, \Pi) \leftarrow \mathsf{Sim}_3(\mathsf{aux}', \mathsf{crs}, x, C_P)$$

and

$$(\mathsf{crs}, x, y, C_P, \Pi) \big| \mathsf{crs} \leftarrow \mathsf{zk} - \mathsf{sDel}.\mathsf{Setup}(1^\lambda), ((P, \mathsf{state}), C_P) \leftarrow \mathsf{zk} - \mathsf{sDel}.\mathsf{ProgAuth}(1^\lambda, \mathsf{crs}), \\ (y \coloneqq P(x), \Pi) \leftarrow \mathsf{zk} - \mathsf{sDel}.W(\mathsf{crs}, P, \mathsf{state}, x, C_P)$$

are indistinguishable.

In Section 6, we present a generic construction of a semi trusted non-interactive succinct arguments with zero-knowledge (ZKstSNARG) from stSNARG. Analogous to the previous lemma, we get the following corollary(cf. Corollary 2) from Lemma 11

Corollary 1. Assuming $T = \mathsf{poly}(m,n)$, $T,m,n \leq 2^{\lambda}$, the ZKstSNARG protocol in Figure 5 implies the unconditional existence of a publicly verifiable non interactive succinct delegation scheme with zero knowledge.

5 Semi-Trusted Succinct Non-Interactive Argument (stSNARG)

We introduce a notion of "Semi-Trusted" SNARGs which is similar to the general definition of SNARGs with an addition "trusted" polynomial time algorithm that outputs a hash for the witness. Further, we provide an explicit construction of an stSNARG for all of NP. Note that any SNARG for arbitrary NP language \mathcal{L} can be reformulated as a Turing Machine which takes in as input an instance x along with witness x and accepts x, x in x steps if $x \in \mathcal{L}$ [10]. In this work, we modify the definition of [10] by using a Universal Turing Machine \mathcal{TM} which takes as input an instance (x, y), a witness which is a program x and accepts x, x in x steps if x in x steps if x in x steps if x and accepts this notion as follows:

Let \mathcal{TM} be a Universal Turing Machine which takes as input a program $P \in \{0,1\}^m$ for some $m < 2^{\lambda}$, and $x \in \{0,1\}^n$ for some $n < 2^{\lambda}$ and $y \in \{0,1\}$ which serve as an input and output for P respectively. \mathcal{TM} accepts (P,x,y) in T steps if P(x) = y. A prover produces a proof II to convince a verifier that \mathcal{TM} accepts P, x, y in T. A publicly verifiable semi-trusted SNARG (stSNARG) for \mathcal{TM} has the following polynomial time algorithms:

- stSNARG.Setup(1^{λ} , 1^{T}): A randomized setup algorithm which on input security parameter λ , and number of Turing Machine steps T, outputs crs.
- stSNARG.TrustHash(crs, P): A deterministic and honest algorithm which on input crs and a program $P \in \{0,1\}^m$ for some $m < 2^{\lambda}$, outputs a succinct and public digest H_P of P corresponding to crs.
- stSNARG.P(crs, P, x, y, H_P): A deterministic prover algorithm which on input the crs, $P \in \{0,1\}^m$ for some $m < 2^{\lambda}$, $x \in \{0,1\}^n$ for some $n < 2^{\lambda}$, $y \in \{0,1\}$ and the digest H_P outputs a proof Π .

- stSNARG.V(crs, x, y, H_P, Π): A deterministic verification algorithm which on input crs, x, y, digest H_P and proof Π , either accepts(output 1) or rejects(output 0) it.

A Universal Turing Machine \mathcal{TM} on input (P, x, y) outputs 1 if it accepts (P, x, y) within T steps. We define the NP language $\mathcal{L}_{\mathcal{TM}}$ as,

$$\mathcal{L}_{\mathcal{TM}} := \{(P, x, y, T, H_P, \mathsf{crs}) | \mathcal{TM}(P, x, y) = 1 \land \mathsf{stSNARG}.\mathsf{TrustHash}(\mathsf{crs}, P) = H_P \}.$$

Note that here P is not considered a part of the witness although it is unknown to the verifier because a typical NP statement puts a there exists constraint on the witness. In that case, the statement becomes trivial because there will always exist a program P which on input x ignores the input and outputs y. We need to ensure that P is the program output by the program author independent of x. Moreover, this is indeed a P statement for the prover.

A publicly verifiable stSNARG scheme stSNARG = (stSNARG.Setup, stSNARG.TrustHash, stSNARG.P, stSNARG.V) satisfies the following properties:

- Completeness. For every $\lambda, T, n, m \in \mathbb{N}$ such that $T, n, m < 2^{\lambda}$, program $P \in \{0,1\}^m$, input $x \in \{0,1\}^n$ and output $y \in \{0,1\}$ such that $(P, x, y, T, H_P, \operatorname{crs}) \in \mathcal{L}_{TM}$, we have

$$\Pr[\mathsf{stSNARG.V}(\mathsf{crs}, x, y, H_P, \Pi) = 1 \big| \mathsf{crs} \leftarrow \mathsf{stSNARG.Setup}(1^\lambda, 1^T), \\ H_P \leftarrow \mathsf{stSNARG.TrustHash}(\mathsf{crs}, P), \Pi \leftarrow \mathsf{stSNARG.P}(\mathsf{crs}, P, x, y, H_P)] = 1.$$

- **Efficiency.** stSNARG.Setup runs in time poly(λ, T), stSNARG.TrustHash runs in time poly($\lambda, |P|, T$), stSNARG.P runs in time poly($\lambda, |x|, |P|, T$) and outputs a proofs of length poly($\lambda, \log T$), and stSNARG.V runs in time poly($\lambda, \log T$)
- **Soundness.** For every PPT adversary $\mathcal{A} := (\mathcal{A}_1, \mathcal{A}_2)$ and the tuple $T = T(\lambda), n = n(\lambda), m = m(\lambda)$, there exists a negligible function $\mathsf{negl}(\lambda)$ such that for every $\lambda \in \mathbb{N}$,

$$\begin{split} \Pr[\mathsf{stSNARG.V}(\mathsf{crs}, x, y, H_P, \Pi) &= 1 \land (P, x, y, T, H_P, \mathsf{crs}) \notin \mathcal{L}_{\mathcal{TM}} \Big|, \\ \mathsf{crs} \leftarrow \mathsf{stSNARG.Setup}(1^\lambda, 1^T), (P, \mathsf{aux}) \leftarrow \mathcal{A}_1(1^\lambda, \mathsf{crs}), \\ H_P \leftarrow \mathsf{stSNARG.TrustHash}(\mathsf{crs}, P), (x, y, \Pi) \leftarrow \mathcal{A}_2(\mathsf{crs}, P, H_P, \mathsf{aux})] \leq \mathsf{negl}(\lambda). \end{split}$$

```
Protocol 1 (Semi-Trusted SNARG)
   - stSNARG.Setup(1^{\lambda}, 1^T):
            \bullet \; \operatorname{SE}.K_{\operatorname{even}} \leftarrow \operatorname{SE}.\operatorname{Gen}(1^{\lambda},1^{M_{\lambda,T}},1^{L_{\lambda}})^{a}
             • SE.K_{odd} \leftarrow SE.Gen(1^{\lambda}, 1^{M}, 1^{L})
            \bullet \ \ \mathsf{BARG.crs} \leftarrow \mathsf{BARG.Gen}(1^{\lambda}, 1^{\acute{T}+1}, 1^{|C_{\mathsf{index}}|})
            • dk \leftarrow HT.Gen(1^{\lambda})
             • return \text{ crs} := (SE.K_{even}, SE.K_{odd}, BARG.crs, dk).
   - stSNARG.TrustHash(crs, P)
             • (\mathsf{tree}_0^2, \mathsf{rt}_0^2) \leftarrow \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, P), H_P \leftarrow \mathsf{rt}_0^2

    return H<sub>P</sub>.

   - stSNARG.P(crs, P, x, y, H_P):
            • \square := empty \ string
            • (\mathsf{tree}_0^1, \mathsf{rt}_0^1) \leftarrow \mathsf{HT.Hash}(\mathsf{dk}, x), (\mathsf{tree}_0^2, \mathsf{rt}_0^2)
                                                                                                                                          \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},P),
                  (\mathsf{tree}_0^3, \mathsf{rt}_0^3) \leftarrow \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, \square)
             ullet initialize s with the start state of \mathcal{TM}
             • st_0 := (0, 0, 0, s)
             • h_0 := (st_0, rt_0^1, rt_0^2, rt_0^3)
             • for every i = 1 to T,
                           \mathsf{rt}_i^1 \leftarrow \mathsf{rt}_{i-1}^1, \mathsf{rt}_i^2 \leftarrow \mathsf{rt}_{i-1}^2
                           (l_i^1, l_i^2, l_i^3) \leftarrow \mathsf{StepR}(\mathsf{st}_{i-1})
                          \left\{ (b_i^j, \Pi_i^j) \leftarrow \mathsf{HT.Read}(\mathsf{tree}_{i-1}^j, l_i^j) \right\}_{j \in [3]}
                           (b_i', l_i', \mathsf{st}_i) \leftarrow \mathsf{StepW}(\mathsf{st}_{i-1}, b_i^1, b_i^2, b_i^3)
                           (\mathsf{tree}_i^3, \mathsf{rt}_i^3, \Pi_i') \leftarrow \mathsf{HT.Write}(\mathsf{tree}_{i-1}^3, l_i'^3, b_i'^3)
                           \mathsf{h}_i \leftarrow (st_i, \mathsf{rt}_i^1, \mathsf{rt}_i^2, \mathsf{rt}_i^3)
            • A := \left(\mathsf{h}_0, \left(\mathsf{h}_1, \{b_1^j, \Pi_1^j\}_{j \in [3]}, \Pi_1'\right), \dots, \left(\mathsf{h}_T, \{b_T^j, \Pi_T^j\}_{j \in [3]}, \Pi_T'\right)\right)
             • c_{\mathsf{even}} \leftarrow \mathsf{SE}.\mathsf{Hash}\left(\mathsf{SE}.K_{\mathsf{even}},A\right) \ and \ c_{\mathsf{odd}} \leftarrow \mathsf{SE}.\mathsf{Hash}\left(\mathsf{SE}.K_{\mathsf{odd}},A\right)
             • c := (c_{\text{even}}, c_{\text{odd}})
             • I_x \leftarrow \{[i_1, i_2] | A[i_1, i_2] = x\}
             • \rho_{h_0} \leftarrow SE.Open(SE.K_{even}, A, I_{h_0})^b
                 for every i \leq \lfloor \lfloor T/2 \rfloor \rfloor,
                          for \ B \in \{\mathsf{h}_{2i}, \{b_{2i}^j, \varPi_{2i}^j\}_{j \in [3]}, \varPi_{2i}'\}, \ \rho_B \leftarrow \mathsf{SE.Open}(\mathsf{SE}.K_{\mathsf{even}}, A, I_B)
             • for every i \leq \lfloor |T/2 \rfloor \rfloor,
                           for
                                    B
                                                                      \{\mathsf{h}_{2i+1}, \{b_{2i+1}^j, \Pi_{2i+1}^j\}_{j \in [3]}, \Pi_{2i+1}'\}, \quad \rho_B
                           SE.Open(SE.K_{odd}, A, I_B)
             • Let C<sub>index</sub> be as defined in Figure 3
             \bullet \ \ \varPi \coloneqq \mathsf{BARG.P} \Big(\mathsf{crs}, C_{\mathsf{index}}, \mathsf{h}_0, \{\mathsf{h}_{i-1}, \mathsf{h}_i, \{b_i^j, \varPi_i^j\}_{j \in [3]}, \varPi_i', \rho_{\mathsf{h}_{i-1}}, \rho_{\mathsf{h}_i},
                  \{\rho_{b_i^j}, \rho_{\Pi_i^j}\}_{j \in [3]}, \rho_{\Pi_i'}\}_{i \in [T]}
             • return (c,\Pi)^c.
   - stSNARG.V(crs, (x, y), H_P, (c, \Pi)):
            • Compute Cindex
             • return 1 if and only if BARG.V(BARG.crs, C_{index}, \Pi) = 1.
```

 $[^]aM_{\lambda,T}=O(T\operatorname{\mathsf{poly}}(\lambda))$ and $L_\lambda=O(\operatorname{\mathsf{poly}}(\lambda))$ are arbitrary and efficiently computable values which can be fixed in advance and hardcoded to the Setup algorithm during instantiation.

 $[^]b$ Note that for simplification, we abuse notation here by specifying opening to more than a single bit.

^cWe often abuse notation and use (c, Π) to denote a proof. This can be done without loss of generalization by defining a new proof $\Pi' = (c || \Pi)$.

```
Circuit 1 (Circuit Cindex)
   - \textit{Hard-coded:}\ y, c, \mathsf{start}, \phi, \mathsf{SE}.K_{\mathsf{even}}, \mathsf{SE}.K_{\mathsf{odd}}, T, H_P, H_x \coloneqq \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, x)
   - Input:
                  (i, (\mathsf{h}_i \coloneqq (\mathsf{st}_i, \mathsf{rt}_i^1, \mathsf{rt}_i^2, \mathsf{rt}_i^3), \rho_{\mathsf{h}_i})), if i = 0
                  \left(i,(\{\mathsf{h}_{i-1},\mathsf{h}_{i},\{b_{i}^{j},\boldsymbol{\Pi}_{i}^{j}\}_{j\in[3]},\boldsymbol{\Pi}_{i}',\rho_{\mathsf{h}_{i-1}},\rho_{\mathsf{h}_{i}},\{\rho_{b_{i}^{j}},\rho_{\boldsymbol{\Pi}_{i}^{j}}\}_{j\in[3]},\rho_{\boldsymbol{\Pi}_{i}'}\})\right),\ \forall i\in[T]
   - Output: return 1 if and only if
             • if i = 0
                     a. st_0 = start
                     b. H_x = \mathsf{rt}_0^1
                     c. H_P = rt_0^2
                     d. HT.Hash(dk, \square) has rt<sub>0</sub><sup>3</sup> as root
                 else
                      * if i is even:
                              a. SE.Verify(SE.K_{odd}, c_{odd}, h_{i-1}, \rho_{h_{i-1}}) = 1
                              b. SE.Verify(SE.K_{\text{even}}, c_{\text{even}}, h_i, \rho_{h_i}) = 1
                              c. \ \left\{ \mathsf{SE.Verify}(\mathsf{SE}.K_{\mathsf{even}}, c_{\mathsf{even}}, b_i^j, \rho_{b_i^j}) = 1 \right\}_{j \in [3]}
                              d. \ \left\{ \mathsf{SE.Verify}(\mathsf{SE}.K_{\mathsf{even}}, c_{\mathsf{even}}, \Pi_i^j, \rho_{\Pi_i^j}) = 1 \right\}_{j \in [3]}
                              e. SE.Verify(SE.K_{\text{even}}, c_{\text{even}}, \Pi'_i, \rho_{\Pi'_i}) = 1
                      * if i is odd:
                              a. \ \mathsf{SE.Verify}(\mathsf{SE}.K_{\mathsf{even}}, c_{\mathsf{even}}, \mathsf{h}_{i-1}, \rho_{\mathsf{h}_{i-1}}) = 1
                              b. SE.Verify(SE.K_{\text{odd}}, c_{\text{odd}}, \mathbf{h}_i, \rho_{\mathbf{h}_i}) = 1
                              c. \ \left\{ \mathsf{SE.Verify}(\mathsf{SE}.K_{\mathsf{odd}}, c_{\mathsf{odd}}, b_i^j, \rho_{b_i^j}) = 1 \right\}_{j \in [3]}
                              d. \ \left\{ \mathsf{SE.Verify}(\mathsf{SE}.K_{\mathsf{odd}}, c_{\mathsf{odd}}, \Pi_i^j, \rho_{\Pi_i^j}) = 1 \right\}_{j \in [3]}
                              e. SE.Verify(SE.K_{\text{odd}}, c_{\text{odd}}, \Pi'_i, \rho_{\Pi'_i}) = 1
                      * \phi(\mathsf{h}_{i-1},\mathsf{h}_i,\{b_i^j,\Pi_i^j\}_{j\in[3]},\Pi_i')=1
                      * if i = T
                              a. \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},y) has \mathsf{rt}_T^3 as root.
                              b. st_T indeed encodes the accept state.
```

Fig. 3: Circuit C_{index}

5.1 Our Construction

Our construction is formulated similar to that of [10]. Specifically, we use the notion of non-interactive BARG for index language and SE Hash functions in our scheme.

Setup for Universal Turing Machine. For a cleaner analysis, we assume without loss of generality that \mathcal{TM} consists of three tapes, namely, $\mathsf{Tp}_1, \mathsf{Tp}_2, \mathsf{Tp}_3$. Tp_1 and Tp_2 are read only tapes that store x and P respectively. Tp_3 is the work tape which is initialized with \square to denote an empty string.

Transition steps for TM. TM's state information along with the head locations of the three tapes are encoded as st . To handle Turing Machines with arbitrarily long tapes, we encode $\{\mathsf{Tp}_i\}_{i\in[3]}$ using three Hash Trees as defined in previous sections and produce tree roots $\mathsf{rt}^1, \mathsf{rt}^2, \mathsf{rt}^3$ respectively.

Let the each intermediate transition state of \mathcal{TM} be encoded as $h_i := (\mathsf{st}_i, \mathsf{rt}_i^1, \mathsf{rt}_i^2, \mathsf{rt}_i^3)$ for $i \in [T]$. A single step of \mathcal{TM} can be interpreted in the manner described below which is similar to one described for a RAM in [23]. We break down the step function at the i^{th} stage into two deterministic polynomial time algorithms:

- StepR: On input st_{i-1} of \mathcal{TM} , outputs head positions $l_{i-1}^1, l_{i-1}^2, l_{i-1}^3$ which denote the memory locations of $\mathsf{Tp}_1, \mathsf{Tp}_2, \mathsf{Tp}_3$ which \mathcal{TM} in the current state st_{i-1} would read from.
- StepW: On input st_{i-1} , and bits $b_{i-1}^1, b_{i-1}^2, b_{i-1}^3$ outputs bit b', location l' and st_i such that \mathcal{TM} upon reading $b_{i-1}^1, b_{i-1}^2, b_{i-1}^3$ at locations $l_{i-1}^1, l_{i-1}^2, l_{i-1}^3$ using HT.Read, would write b' at location l' of Tp_3 , thereby transition to new state st_i .

Now, we translate the i^{th} single step of \mathcal{TM} to the circuit ϕ which is defined such that on input digests $h_{i-1} \coloneqq (\mathsf{st}_{i-1}, \mathsf{rt}_{i-1}^1, \mathsf{rt}_{i-1}^2, \mathsf{rt}_{i-1}^3)$ and $h_i \coloneqq (\mathsf{st}_i, \mathsf{rt}_i^1, \mathsf{rt}_i^2, \mathsf{rt}_i^3)$, bits b_i^1, b_i^2, b_i^3 , and proofs $\Pi_i^1, \Pi_i^2, \Pi_i^3, \Pi_i', \phi(h_{i-1}, h_i, b_i^1, b_i^2, b_i^3, \Pi_i^1, \Pi_i^2, \Pi_i^3, \Pi_i') = 1$ if and only if the following hold:

$$\begin{array}{lll} 1. & (l_i^1, l_i^2, l_i^3) \leftarrow \mathsf{StepR}(\mathsf{st}_{i-1}) & 6. & \mathsf{HT.VerRead}(\mathsf{dk}, \mathsf{rt}_{i-1}^3, l_i^3, b_i^3, \Pi_i^3) = \\ 2. & (b', l', \mathsf{st'}) \leftarrow \mathsf{StepW}(\mathsf{st}_{i-1}, b_i^1, b_i^2, b_i^3) & 1 \\ 3. & \mathsf{st'} = \mathsf{st}_i & \\ 4. & \mathsf{HT.VerRead}(\mathsf{dk}, \mathsf{rt}_{i-1}^1, l_i^1, b_i^1, \Pi_i^1) = \\ & 1 & \\ 5. & \mathsf{HT.VerRead}(\mathsf{dk}, \mathsf{rt}_{i-1}^2, l_i^2, b_i^2, \Pi_i^2) = \\ & 1 & \\ 9. & \mathsf{HT.VerWrite}(\mathsf{dk}, \mathsf{rt}_{i-1}^3, l', b', \mathsf{rt}_i^3, \Pi_i') = \\ & 1 & \\ \end{array}$$

Here, dk denote the hash keys used to build the three hash trees. Note that the efficiency of hash tree implies that ϕ can be constructed such that it can represented as a formula in $L = \operatorname{poly}(\lambda)$ variables. For the T steps of $T\mathcal{M}$, we have the following formula over $M = O(L \cdot T)$ variables:

$$\varPhi(\mathbf{h}_0, \{h_i, b_i^1, b_i^2, b_i^3, \Pi_i^1, \Pi_i^2, \Pi_i^3, \Pi_i'\}_{i \in [T]}) = \bigwedge_{i \in [T]} \phi(h_{i-1}, h_i, b_i^1, b_i^2, b_i^3, \Pi_i^1, \Pi_i^2, \Pi_i^3, \Pi_i')$$

Following the techniques in [10], we use a combination of SE Hash along with ϕ to produce the circuit for index languages.

Our semi-trusted SNARG scheme is given in Figure 2 and the corresponding index language circuit is shown as Figure 3.

Theorem 3. Assuming the existence of Somewhere Extractable Hash functions, non-interactive Batch Arguments for Index Languages, and Collision Resistant Hash Trees as described in section 3,, Figure 2 is a publicly verifiable non-interactive semi-trusted SNARG with CRS size, proof size and verifier time $\operatorname{poly}(\lambda, \log T)$ and prover run time being $\operatorname{poly}(\lambda, T)$.

Completeness. Here we give a sketch arguing completeness of our scheme. Our construction in Figure 2 tells that

```
\begin{split} \Pr[\mathsf{stSNARG.V}(\mathsf{crs}, x, y, H_P, \Pi) &= 1 \big| \mathsf{crs} \leftarrow \mathsf{stSNARG.Setup}(1^\lambda, 1^T), \\ H_P &\leftarrow \mathsf{stSNARG.TrustHash}(\mathsf{crs}, P), \Pi \leftarrow \mathsf{stSNARG.P}(\mathsf{crs}, P, x, y, H_P)] = \\ \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \Pi) &= 1 \big| \mathsf{crs} \leftarrow \mathsf{stSNARG.Setup}(1^\lambda, 1^T), \\ H_P &\leftarrow \mathsf{stSNARG.TrustHash}(\mathsf{crs}, P), \Pi \leftarrow \mathsf{stSNARG.P}(\mathsf{crs}, P, x, y, H_P)] \end{split}
```

where C_{index} is the index circuit as shown in Figure 3. Observing stSNARG.P algorithm in our scheme tells it is sufficient to show that if the prover is honest and uses a valid witness, then $(C_{\mathsf{index}}, i) \in \mathcal{L}_{\mathsf{index}}, \forall i \in \{0\} \cup [T]$. If we can argue that this is indeed the case, then the completeness of BARG gives the desired result.

If $(P,x,y,T,H_P,\mathsf{crs}) \in \mathcal{L}_{\mathcal{TM}}$, then $(C_{\mathsf{index}},0) \in \mathcal{L}_{\mathsf{index}}$ is trivially true by observation. Now, let us look at $(C_{\mathsf{index}},1)$. We start by analysing that $\phi(\mathsf{h}_0,\mathsf{h}_1,\{b_1^j,\Pi_1^j\}_{j\in[3]},\Pi_1')=1$ is true. $\{\mathsf{rt}_1^i=\mathsf{rt}_0^i\}_{i\in[2]}$ follow from the read-only nature of tapes $\mathsf{Tp}_1,\mathsf{Tp}_2$. Since, $\left\{(b_1^j,\Pi_1^j) \leftarrow \mathsf{HT}.\mathsf{Read}(\mathsf{tree}_0^j,l_1^j)\right\}_{j\in[3]}$, the hash tree completeness of read ensures that $\{\mathsf{HT}.\mathsf{VerRead}(\mathsf{dk},\mathsf{rt}_0^i,l_1^i,b_1^i,\Pi_1^i)=1\}_{i\in[3]}=1$ and $\{\mathsf{Tp}_i[l_1^i]=b_1^i\}_{i\in[3]}$. This along with the correctness of Turing Machine StepR function implies that b_1^1,b_1^2,b_1^3 are indeed the correct input for the StepW function of \mathcal{TM} . Finally, $(\mathsf{tree}_1^3,\mathsf{rt}_1^3,\Pi_1') \leftarrow \mathsf{HT}.\mathsf{Write}(\mathsf{tree}_0^3,l_1',b_1')$ implies $\mathsf{HT}.\mathsf{VerWrite}(\mathsf{dk},\mathsf{rt}_0^3,l',b',\mathsf{rt}_1^3,\Pi_1')=1$ from the hash tree completeness of write property. The same property also ensures that Tp_3 changes only at the l'^{th} memory location. When paired with the correctness of StepW, we get that $\mathsf{st}_1=\mathsf{st}'$

The completeness of the SE hash implies that the verification algorithm certainly accepts all the local openings. Thus, $(C_{\mathsf{index}}, 1) \in \mathcal{L}^{\mathsf{index}}$. Now, $(C_{\mathsf{index}}, T) \in \mathcal{L}^{\mathsf{index}}$ because \mathcal{TM} accept (P, x, y) in T steps. We can show in a similar manner that for all other i, $(C_{\mathsf{index}}, i) \in \mathcal{L}^{\mathsf{index}}$. This proves the completeness of the scheme in Figure 2.

Efficiency.

- Runtime of stSNARG.Setup is $poly(\lambda, T)$. This follows from the efficiency of underlying primitives.
- stSNARG. TrustHash computes H_P in time $|P| \cdot \mathsf{poly}(\lambda)$ which is $\mathsf{poly}(|P|, \lambda)$.
- $-|C_{\mathsf{index}}| = \mathsf{poly}(\lambda, \log T)$. This follows from the efficiency of the SE hash and the efficiency of hash tree construction.
- CRS Size: By the corresponding properties of the underlying primitives, $|crs| = poly(\lambda, \log T)$.
- The prover's computation time is dominated by the hashes corresponding to x, P and the Turing Machine step functions that is run T times. This requires a total time of $\mathsf{poly}(\lambda, |x|) + \mathsf{poly}(\lambda, |P|) + \mathsf{poly}(\lambda, T) = \mathsf{poly}(\lambda, |x|, |P|, T)$.
- Proof Length: $|c|+|\Pi| = \mathsf{poly}(\lambda, \log T) + \mathsf{poly}(\lambda, \log T, |C_{\mathsf{index}}|) = \mathsf{poly}(\lambda, \log T)$.
- Verifier Time: Time taken to compute C_{index} and verify the BARG. This is $\mathsf{poly}(\lambda, \log T, |C_{\mathsf{index}}|) = \mathsf{poly}(\lambda, \log T)$.

Soundness. Let us assume for the sake of contradiction that our scheme in Figure 2 is not sound, i.e., there exists a PPT adversary $\mathcal{A} := (\mathcal{A}_1, \mathcal{A}_2)$, a value T and a polynomial function $\mathsf{poly}(\lambda)$ such that for infinitely many values of $\lambda \in \mathbb{N}$.

$$\Pr[\mathsf{G}^{\mathcal{A}} = 1] \ge \frac{1}{\mathsf{poly}(\lambda)},$$

where \mathcal{A} plays Game G described below

Real Game G

- 1. crs \leftarrow stSNARG.Setup $(1^{\lambda}, 1^{T})$
- 2. $(P, \mathsf{aux}) \leftarrow \mathcal{A}_1(1^\lambda, \mathsf{crs})$
- 3. $H_P \leftarrow \mathsf{stSNARG}.\mathsf{TrustHash}(\mathsf{crs},P)$
- 4. $((x,y)(c,\Pi)) \leftarrow \mathcal{A}_2(\mathsf{crs},P,H_P,\mathsf{aux})$
- 5. if stSNARG.V (crs, (x, y), H_P , (c, Π)) = $1 \land ((x, y), T, P, H_P, \text{crs}) \notin \mathcal{L}_{TM}$, return 1
- 6. else return 0

Let S_i denote the following set:

$$S_i = \begin{cases} \mathsf{h}_0 & \text{if } i = 0 \\ \left\{ \mathsf{h}_i, \{b_i\}_{j \in [3]}, \{\Pi_i\}_{j \in [3]}, \Pi_i' \right\} & \text{if } i \in [T] \end{cases}$$

Let D denote the string $\left(\mathsf{h}_0, \left\{\mathsf{h}_i, \left\{b_i\right\}_{j \in [3]}, \left\{\Pi_i\right\}_{j \in [3]}, \Pi_i'\right\}_{i \in [T]}\right)$. $I_{S_i} \subset |D|$ denotes the following:

$$I_{S_i} = \{ [a, b] | a, b \in |D|, D[a, b] = S_i \}.$$

In game G, say we have $(\mathsf{tree}_0^1, \mathsf{rt}_0^1) \leftarrow \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, x), (\mathsf{tree}_0^2, \mathsf{rt}_0^2) \leftarrow \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, P), (\mathsf{tree}_0^3, \mathsf{rt}_0^3) \leftarrow \mathsf{HT}.\mathsf{Hash}(\mathsf{dk}, \square).$ Also, let $\mathsf{st}_0 \coloneqq (0, 0, 0, \mathsf{s}),$ where s is the start state of \mathcal{TM} . We say that $\bar{\mathsf{h}}_0 \coloneqq (\mathsf{st}_0, \mathsf{rt}_0^1, \mathsf{rt}_0^2, \mathsf{rt}_0^3)$ defines a unique "true" digest for the starting step of \mathcal{TM} .

If stSNARG.V(crs, x, H_P, c, Π) = 1, then Algorithm Step(x, P, crs, i) in Figure 4 computes the unique true digest $\bar{\mathbf{h}}_i$ after the i^{th} Turing Machine Step along with the other uniquely correct values of the set $\bar{S}_i \coloneqq \{\bar{\mathbf{h}}_i, \{\bar{b}_i\}_{j \in [3]}, \{\bar{\Pi}_i\}_{j \in [3]}, \bar{\Pi}_i'\}$. We use the notation Step(x, P, crs, i).x to denote $x \in \bar{S}_i$. We proceed by performing an induction on the following sequence outer hybrid games \mathbf{G}_i , i from 1 to T. We use a sequence of inner hybrid games to transition between subsequent outer hybrids. Our induction hypothesis is that, under suitable assumptions, for all $i \in 1$ to T, there exists a negligible function λ such that,

$$\Pr[\mathsf{G}^{\mathcal{A}} = 1] \le \Pr[\mathsf{G}_i^{\mathcal{A}} = 1] + \mathsf{negl}(\lambda).$$

Intuitively, the i^{th} game G_i is similar to the real life soundness game with the following two changes: (1) The key generation for the SE hash and BARG is done in the trapdoor mode at the i^{th} game. This allows for extractability of the i^{th} block of the string D from the commitment c. (2) The adversary wins the game if they break the soundness assumption as the real life game G and the extracted block is indeed the correct one.

```
Algorithm Step(x, y, P, crs, i)
    -\square := \text{empty string}
    -(\mathsf{tree}_0^1,\mathsf{rt}_0^1) := \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},x), (\mathsf{tree}_0^2,\mathsf{rt}_0^2) := \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},P), (\mathsf{tree}_0^3,\mathsf{rt}_0^3) :=
             \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},\square)
    - initialize s with the start state of \mathcal{TM}
    -\mathsf{st}_0 \coloneqq (0,0,0,\mathsf{s})
    -\bar{\mathsf{h}}_0 \coloneqq (\mathsf{st}_0, \mathsf{rt}_0^1, \mathsf{rt}_0^2, \mathsf{rt}_0^3)
    - if i = 0, return \bar{S}_0 := (\mathsf{st}_0, \mathsf{rt}_0^1, \mathsf{rt}_0^2, \mathsf{rt}_0^3)
    - else
                   • for count = 1 to i,
                                       (l_{\mathsf{count}}^1, l_{\mathsf{count}}^2, l_{\mathsf{count}}^3) \leftarrow \mathsf{StepR}(\mathsf{st}_{\mathsf{count}-1})
                                         \left\{(b_{\mathsf{count}}^k, \Pi_{\mathsf{count}}^k) \coloneqq \mathsf{HT}.\mathsf{Read}(\mathsf{tree}_{\mathsf{count}-1}^k, l_{\mathsf{count}}^k)\right\}_{k \in [3]}
                                       (b_{\mathsf{count}}^{\prime 3}, l_{\mathsf{count}}^{\prime 3}, \mathsf{st}_{\mathsf{count}}) \coloneqq \mathsf{StepW}(\mathsf{st}_{\mathsf{count}-1}, b_{\mathsf{count}}^1, b_{\mathsf{count}}^2, b_{\mathsf{count}}^3)
                                        (\mathsf{tree}^3_{\mathsf{count}}, \mathsf{rt}^3_{\mathsf{count}}, \varPi'_{\mathsf{count}}) \coloneqq \mathsf{HT.Write}(\mathsf{tree}^3_{\mathsf{count}-1}, l'^3_{\mathsf{count}}, b'^3_{\mathsf{count}})
                   • \bar{\mathsf{h}}_i \coloneqq (\mathsf{st}_i, \mathsf{rt}_i^1, \mathsf{rt}_i^2, \mathsf{rt}_i^3)
                   • \bar{b}_i \coloneqq (b_i^1, b_i^2, b_i^3)
                   \begin{split} \bullet & \bar{l}_i \coloneqq (l_i^1, l_i^2, l_i^3) \\ \bullet & \bar{l}_i \coloneqq (l_i^1, l_i^2, l_i^3) \\ \bullet & \bar{\mathsf{rt}}_i \coloneqq \mathsf{rt}_{i-1}^1, \mathsf{rt}_{i-1}^2, \mathsf{rt}_i^3 \\ \bullet & \bar{\varPi}_i \coloneqq ( \varPi_i^1, \varPi_i^2, \varPi_i^3, \varPi_i') \end{split} 
                   • return \bar{S}_i := (\bar{\mathsf{h}}_i, \bar{b}_i, \bar{\mathsf{rt}}_i, \bar{\Pi}_i)
```

Fig. 4: Turing Machine i^{th} step.

```
Outer Hybrid Game \mathsf{G}_i
  1. if i is even
                      SE.K_{even} \leftarrow SE.TGen(1^{\lambda}, 1^{M}, I_{S_i})
                      SE.K_{odd} \leftarrow SE.TGen(1^{\lambda}, 1^{M}, I_{S_{i-1}})
  2. if i is odd
                      \mathsf{SE}.K_{\mathsf{even}} \leftarrow \mathsf{SE}.\mathsf{TGen}(1^{\lambda},1^{M},I_{S_{i-1}})
  \begin{aligned} & \mathsf{SE}.K_{\mathsf{odd}} \leftarrow \mathsf{SE}.\mathsf{TGen}(1^{\lambda},1^{M},I_{S_{i}}) \\ & 3. \ \ \mathsf{BARG.crs} \leftarrow \mathsf{BARG}.\mathsf{TGen}(1^{\lambda},1^{T+1},1^{|C_{\mathsf{index}}|},i) \end{aligned}
  4. dk \leftarrow HT.Gen(1^{\lambda})
  5. \; \operatorname{crs} \coloneqq (\operatorname{SE}.K_{\operatorname{even}}, \operatorname{SE}.K_{\operatorname{odd}}, \operatorname{BARG.crs}, \operatorname{dk}).
  6. (P, \mathsf{aux}) \leftarrow \mathcal{A}_1(1^\lambda, \mathsf{crs})
  7. H_P \leftarrow \mathsf{stSNARG}.\mathsf{TrustHash}(\mathsf{crs},P)
  8. ((x,y),(c,\Pi)) \leftarrow \mathcal{A}_2(\mathsf{crs},P,\mathsf{aux})
  9. Parse c as (c_{\sf odd}, c_{\sf even})
10. if i is even and i \neq 0
                \begin{split} & \bullet \  \  (\mathsf{h}_i, \{b_i^k\}_{k \in [3]}, \{\Pi_i^k\}_{k \in [3]}, \Pi_i') \leftarrow \mathsf{SE}.\mathsf{Ext}_{\mathsf{even}}(c_{\mathsf{even}}, \mathsf{SE}.K_{\mathsf{even}}) \\ & \bullet \  \  (\mathsf{h}_{i-1}, \{b_{i-1}^k\}_{k \in [3]}, \{\Pi_{i-1}^k\}_{k \in [3]}, \Pi_{i-1}') \leftarrow \mathsf{SE}.\mathsf{Ext}_{\mathsf{odd}}(c_{\mathsf{odd}}, \mathsf{SE}.K_{\mathsf{odd}}) \end{split} 
11. if i is odd
                • (\mathsf{h}_i, \{b_i^k\}_{k \in [3]}, \{\Pi_i^k\}_{k \in [3]}, \Pi_i') \leftarrow \mathsf{SE}.\mathsf{Ext}_{\mathsf{odd}}(c_{\mathsf{odd}}, \mathsf{SE}.K_{\mathsf{odd}})
               • if i-1>0 then (\mathsf{h}_{i-1},\{b_{i-1}^k\}_{k\in[3]},\{\Pi_{i-1}^k\}_{k\in[3]},\Pi_{i-1}')\leftarrow\mathsf{SE}.\mathsf{Ext}_{\mathsf{even}}(c_{\mathsf{even}},\mathsf{SE}.K_{\mathsf{even}})
                • if i - 1 = 0 then h_0 \leftarrow \mathsf{SE}.\mathsf{Ext}_{\mathsf{even}}(c_{\mathsf{even}}, \mathsf{SE}.K_{\mathsf{even}})
12. if stSNARG.V (crs, (x, y), H_P, (c, \Pi)) = 1 \wedge ((x, y), T, P, H_P, \text{crs}) \notin \mathcal{L}_{TM} \wedge h_i =
            Step(x, y, P, \mathsf{crs}, i).\bar{\mathsf{h}}_i, return 1
13. else return 0
```

Base Case: Assuming key indistinguishability and soundness of SE hash and BARG, we need to show that $\Pr[G^A = 1] \leq \Pr[G_1^A = 1] + \mathsf{negl}(\lambda)$.

We begin by using a sequence of hybrids to transition from G to an intermediate game G_0 . The colored texts in the hybrids below indicate the steps in the hybrids exclusively appear in a particular game. We only present proof sketches for the intermediate lemmas in this section due to lack of space. Concrete proofs have been shifted to the Supplementary Material.

```
Hybrid Games G_a, G_b, G_{ab}, G_0

1. SE.K_{\text{even}} \leftarrow \text{SE.TGen}(1^{\lambda}, 1^{M}, I_{S_0}) \dots (G_a, G_b, G_{ab}, G_0)

2. SE.K_{\text{odd}} \leftarrow \text{SE.Gen}(1^{\lambda}, 1^{M}) \dots (G_a)

3. SE.K_{\text{odd}} \leftarrow \text{SE.TGen}(1^{\lambda}, 1^{M}, I_{S_1}) \dots (G_b, G_{ab}, G_0)

4. BARG.crs \leftarrow \text{BARG.Gen}(1^{\lambda}, 1^{T+1}, 1^{|C_{\text{index}}|}) \dots (G_a, G_b)

5. BARG.crs \leftarrow \text{BARG.TGen}(1^{\lambda}, 1^{T+1}, 1^{|C_{\text{index}}|}, 0) \dots (G_{ab}, G_0)

6. dk \leftarrow \text{HT.Gen}(1^{\lambda})

7. crs := (SE.K_{\text{even}}, SE.K_{\text{odd}}, BARG.crs, dk).

8. (P, aux) \leftarrow \mathcal{A}_1(1^{\lambda}, crs)

9. H_P \leftarrow \text{stSNARG.TrustHash}(\text{crs}, P)

10. if stSNARG.V (crs, (x, y), H_P, (c, \Pi)) = 1 \wedge ((x, y), T, P, H_P, \text{crs}) \notin \mathcal{L}_{TM}, return 1

11. h_0 \leftarrow \text{SE.Ext}_{\text{even}}(c_{\text{even}}, \text{SE.}K_{\text{even}}) \dots (G_0) \dots (G_a, G_b, G_{ab})

12. if stSNARG.V (crs, (x, y), H_P, (c, \Pi)) = 1 \wedge ((x, y), T, P, H_P, \text{crs}) \notin \mathcal{L}_{TM} \wedge h_0 = Step(x, y, P, \text{crs}, 0).\bar{h}, return 1 ...(G_0)
```

Lemma 2. Assuming key indistinguishability of SE, $\left|\Pr[\mathsf{G}^{\mathcal{A}}=1] - \Pr[\mathsf{G}_{a}^{\mathcal{A}}=1]\right| \leq \mathsf{negl}(\lambda)$.

Proof. The only difference in Game G and G_a is that the key generation algorithm of the SE hash ($\mathsf{SE}.\mathsf{Gen}$) is replaced by the trapdoor key generation ($\mathsf{SE}.\mathsf{TGen}$).

If $|\Pr[\mathsf{G}^{\mathcal{A}}=1] - \Pr[\mathsf{G}_a^{\mathcal{A}}=1]| > \mathsf{negl}(\lambda)$, then one can construct a PPT adversary \mathcal{B} that breaks the key indistinguishability of SE using I_{S_0} with Key as input from the key generation algorithm of the SE hash and runs \mathcal{A} on Key. Here, Key is either SE. $\mathsf{Gen}(1^{\lambda}, 1^{M})$ or SE. $\mathsf{TGen}(1^{\lambda}, 1^{M}, I_{S_0})$ based on whether \mathcal{A} is interacting with game G or G_a respectively. Note that the reduction can simulate the other steps of games G or G_a . Now, the probability that \mathcal{B} returns 1 in either case is exactly equal to the probability that \mathcal{A} wins the corresponding games, hence, \mathcal{B} breaks if $|\Pr[\mathsf{G}^{\mathcal{A}}=1] - \Pr[\mathsf{G}_a^{\mathcal{A}}=1]| \geq \mathsf{negl}(\lambda)$. This leads to a contradiction of our assumption.

Lemma 3. Assuming key indistinguishability of SE, $\left|\Pr[\mathsf{G}_a^{\mathcal{A}}=1] - \Pr[\mathsf{G}_b^{\mathcal{A}}=1]\right| \leq \mathsf{negl}(\lambda)$.

This again follows from the key-indistinguishability of SE as shown in the previous lemma as the only difference in these games is that the key generation algorithm for the SE hash has been changed to TGen, hence we skip the proof.

Lemma 4. Assuming key indistinguishability of BARG, $\left|\Pr[\mathsf{G}_b^{\mathcal{A}}=1] - \Pr[\mathsf{G}_{ab}^{\mathcal{A}}=1]\right| \leq \mathsf{negl}(\lambda)$.

Proof. The only difference in Game G_b and G_{ab} is that the key generation algorithm of the BARG (BARG.Gen) is replaced by the trapdoor key generation (BARG.TGen) at index 0.

If $|\Pr[\mathsf{G}_b^{\mathcal{A}} = 1] - \Pr[\mathsf{G}_{ab}^{\mathcal{A}} = 1]| > \mathsf{negl}(\lambda)$, then one can construct a PPT adversary \mathcal{B} getting Key as input that breaks the key indistinguishability of BARG, where Key is either BARG.Gen $(1^{\lambda}, 1^{T+1}, 1^{|C_{\mathsf{index}}|})$ or BARG.TGen $(1^{\lambda}, 1^{T+1}, 1^{|C_{\mathsf{index}}|}, 0)$ based on whether A is interacting with game G_b or G_{ab} respectively. The reduction then following in a similar manner as the SE key indistinguishability adversary described above.

Lemma 5. Assuming somewhere soundness of BARG,

$$\left|\Pr[\mathsf{G}_{ab}^{\mathcal{A}}=1] - \Pr[\mathsf{G}_{0}^{\mathcal{A}}=1]\right| \le \mathsf{negl}(\lambda).$$

Proof. The only difference in Games G_{ab} and G_0 is that there is an additional step which computes the true digest at index 0 and extracts at the 0^{th} index from c_{even} using the extraction function of SE. Finally, the adversary wins if and only if the extracted value matches the true digest along with the usual win conditions in the previous game.

Note that,

$$\begin{split} \left| \Pr[\mathsf{G}_{ab}^{\mathcal{A}} = 1] - \Pr[\mathsf{G}_{0}^{\mathcal{A}} = 1] \right| &\leq \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \varPi) = 1 \land \\ & ((x,y), T, P, H_P, \mathsf{crs}) \notin \mathcal{L}_{\mathcal{TM}} \land \mathsf{h}_0 \neq Step(x, P, \mathsf{crs}, 0).\bar{\mathsf{h}}] \leq \\ & \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \varPi) = 1 \land \mathsf{h}_0 \neq Step(x, P, \mathsf{crs}, 0).\bar{\mathsf{h}}]. \end{split}$$

Let us assume that there exists a PPT adversary A such that for infinitely many values of $\lambda \in \mathbb{N}$,

$$\Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \Pi) = 1 \land \mathsf{h}_0 \neq Step(x, y, P, \mathsf{crs}, 0).\bar{\mathsf{h}}] \geq \frac{1}{\mathsf{polv}(\lambda)}.$$

Notice that $h_0 \neq Step(x, P, crs, 0).\bar{h}$ implies that at least one of the conditions $\mathsf{st}_0 = \mathsf{start}, H_x = \mathsf{rt}_0^1, H_P = \mathsf{rt}_0^2 \text{ and } \mathsf{HT}.\mathsf{Hash}(\mathsf{dk},\square) \text{ having } \mathsf{rt}_0^3 \text{ as root must not}$ be true. If this is indeed true then our construction of C_{index} in Figure 3 implies that $(C_{\mathsf{index}}, 0) \notin \mathcal{L}^{\mathsf{index}}$.

We now construct the following PPT adversary \mathcal{B} playing the semi-adaptive somewhere soundness game of the BARG as follows

Adversary \mathcal{B} playing semi adaptive somewhere soundness game of BARG.

- SE. $K_{\text{even}} \leftarrow \text{SE.TGen}(1^{\lambda}, 1^{M}, I_{S_0})$
- $$\begin{split} \bullet & \ \, \mathsf{SE}.K_{\mathsf{odd}} \leftarrow \mathsf{SE}.\mathsf{TGen}(1^{\lambda},1^{M},I_{S_{1}}) \\ \bullet & \ \, \mathsf{BARG.crs} \leftarrow \mathsf{BARG}.\mathsf{TGen}(1^{\lambda},1^{T+1},1^{|C_{\mathsf{index}}|},0) \end{split}$$
- dk \leftarrow HT.Gen (1^{λ})
- $crs := (SE.K_{even}, SE.K_{odd}, BARG.crs, dk).$
- $(P, \mathsf{aux}) \leftarrow \mathcal{A}_1(1^\lambda, \mathsf{crs})$
- $H_P \leftarrow \mathsf{stSNARG}.\mathsf{TrustHash}(\mathsf{crs},P)$
- $((x,y),(c,\Pi)) \leftarrow \mathcal{A}_2(\mathsf{crs},P,\mathsf{aux})$
- return (C_{index}, Π)

By our assumption, it is clear that BARG.V(BARG.crs, C_{index} , Π) = 1 with non negligible probability but $(C_{index}, 0) \notin \mathcal{L}^{index}$. Thus, \mathcal{B} will break the semi-adaptive somewhere soundness of BARG at index 0. Therefore, it must be the case that for every PPT adversary \mathcal{A} , there exists a negligible function $negl(\lambda)$ such that for all $\lambda \in \mathbb{N}$,

$$\begin{split} \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \varPi) &= 1 \land \mathsf{h}_0 \neq Step(x, y, P, \mathsf{crs}, 0).\bar{\mathsf{h}}] \leq \mathsf{negl}(\lambda) \\ \Longrightarrow & \left|\Pr[\mathsf{G}_{ab}^{\mathcal{A}} = 1] - \Pr[\mathsf{G}_{0}^{\mathcal{A}} = 1]\right| \leq \mathsf{negl}(\lambda) \end{split}$$

Now, we transition from G_0 to the base case for our induction, G_1 using the following sequence of indistinguishable hybrids:

 $\mathsf{G}_{0,a}$ Identical to G_0 except we add an extraction: $(\mathsf{h}_1,\{b_1^k\}_{k\in[3]},\{\Pi_1^k\}_{k\in[3]},\Pi_i')\leftarrow \mathsf{SE}.\mathsf{Ext}_{\mathsf{odd}}(c_{\mathsf{odd}},\mathsf{SE}.K_{\mathsf{odd}})$ which is not used in the hybrid, hence indistinguishability follows.

 $\mathsf{G}_{0,b}$ The BARG key generation's trapdoor is changed from 0 to 1. This can be done due to key indistinguishability of BARG.

 $\begin{aligned} \mathsf{G}_{0,c} \ \ \text{The winning condition is changed to: if stSNARG.V} \left(\mathsf{crs},(x,y),H_P,(c,\Pi)\right) = \\ 1 \wedge \left((x,y),T,P,H_P,\mathsf{crs}\right) \notin \mathcal{L}_{\mathcal{TM}} \wedge \mathsf{h}_0 = Step(x,y,P,\mathsf{crs},0).\bar{\mathsf{h}} \wedge \{b_1^k\}_{k \in [3]} = \\ Step(x,y,P,\mathsf{crs},1).\bar{b} \wedge \{\mathsf{rt}_1^k\}_{k \in [3]} = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{rt}} \wedge \mathsf{st}_1 = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{st}}, \\ \text{return } 1 \end{aligned}$

 $\begin{aligned} \mathsf{G}_{0,d} \ \ \text{The winning condition is changed to: if stSNARG.V} & (\mathsf{crs},(x,y),H_P,(c,\Pi)) = \\ & 1 \wedge ((x,y),T,P,H_P,\mathsf{crs}) \notin \mathcal{L}_{\mathcal{TM}} \wedge \mathsf{h}_0 = Step(x,y,P,\mathsf{crs},0).\bar{\mathsf{h}} \wedge \{b_1^k\}_{k \in [3]} = \\ & Step(x,y,P,\mathsf{crs},1).\bar{b} \wedge \mathsf{rt}_1^3 = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{rt}} \wedge \mathsf{st}_1 = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{st}} \wedge \\ & \mathsf{h}_1 = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{h}}, \text{ return } 1 \end{aligned}$

 $\mathsf{G}_{0,e}$ The winning condition is changed to: if $\mathsf{stSNARG.V}\left(\mathsf{crs},(x,\underline{y}),H_P,(c,\Pi)\right) = 1 \land ((x,y),T,P,H_P,\mathsf{crs}) \notin \mathcal{L}_{\mathcal{TM}} \land \mathsf{h}_1 = Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{h}}, \text{ return } 1$

The indistinguishability of the last three hybrids follow from the following lemmas.

Lemma 6. Assuming semi-adaptive somewhere soundness of BARG, extraction correctness of SE, read and write soundness of HT,

$$\left| \Pr[\mathsf{G}_{0,b}^{\mathcal{A}} = 1] - \Pr[\mathsf{G}_{0,c}^{\mathcal{A}} = 1] \right| \le \mathsf{negl}(\lambda).$$

Proof. The only difference in Games $G_{0,b}$ and $G_{0,c}$ is that we have added some additional conditions for the adversary to win along with the ones in the previous game.

Note that,

$$\begin{split} \left| \Pr[\mathsf{G}_{0,b}^{\mathcal{A}} = 1] - \Pr[\mathsf{G}_{0,c}^{\mathcal{A}} = 1] \right| &\leq \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \varPi) = 1 \land \\ \mathsf{h}_0 &= Step(x, P, \mathsf{crs}, 0).\bar{\mathsf{h}} \land \left(\{b_1^k\}_{k \in [3]} \neq Step(x, y, P, \mathsf{crs}, 1).\bar{b} \right. \\ & \qquad \qquad \lor \{\mathsf{rt}_1^k\}_{k \in [3]} \neq Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{rt}} \lor \mathsf{st}_1 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{st}} \right)]. \end{split}$$

Let us assume that there exists a PPT adversary \mathcal{A} such that for infinitely many values of $\lambda \in \mathbb{N}$,

$$\begin{split} \Pr[\mathsf{BARG.V}(\mathsf{BARG.crs}, C_{\mathsf{index}}, \varPi) &= 1 \land \mathsf{h}_0 = Step(x, y, P, \mathsf{crs}, 0).\bar{\mathsf{h}} \\ &\wedge \left(\{b_1^k\}_{k \in [3]} \neq Step(x, y, P, \mathsf{crs}, 1).\bar{b} \lor \{\mathsf{rt}_1^k\}_{k \in [3]} \neq Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{rt}} \\ &\vee \mathsf{st}_1 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{st}} \right)] \geq \frac{1}{\mathsf{poly}(\lambda)}. \end{split}$$

Notice that $h_0 = Step(x, P, crs, 0).\bar{h}$ implies that the conditions $st_0 = start$, $H_x = \mathsf{rt}_0^1$, $H_P = \mathsf{rt}_0^2$ and $\mathsf{HT.Hash}(\mathsf{dk}, \square)$ having rt_0^3 as root are true. In other words, h_0 is indeed the true digest at step 0.

Assuming extraction correctness of SE, read and write soundness of HT, we construct the following PPT adversary \mathcal{B} playing the semi-adaptive somewhere soundness game of the BARG similar to the one in the proof of Lemma 5.

Thus, \mathcal{B} will break the semi-adaptive somewhere soundness of BARG at index 1 if $(C_{\mathsf{index}}, 1) \notin \mathcal{L}^{\mathsf{index}}$.

It is now left to show that $(C_{index}, 1) \notin \mathcal{L}^{index}$

- Case 1 If the SE verifications in C_{index} do not all return 1, then by construction of C_{index} , we have that $(C_{\mathsf{index}}, 1) \notin \mathcal{L}^{\mathsf{index}}$.
- Case 2 All SE verifications return 1. Extraction Correctness/ Somewhere binding property of SE hash implies that $h_0 = (\mathsf{st}_0, \mathsf{rt}_0^1, \mathsf{rt}_0^2, \mathsf{rt}_0^3), h_1, \{b_1^k, \Pi_1^k\}_{k \in [3]}, \Pi_1'$ were indeed committed by the prover as the Turing machine output at step 0 and step 1. Now, let us analyze $\phi(\mathsf{h}_0,\mathsf{h}_1,\{b_1^k,\varPi_1^k\}_{k\in[3]},\varPi_1')$. By assumption, we know that $h_0 = \bar{h_0}$, i.e., $s\bar{t_0}, \bar{r}t_0^1, \bar{r}t_0^2, \bar{r}t_0^3 = st_0, rt_0^1, rt_0^2, rt_0^3$. StepR being a deterministic function ensures that (l_1^1, l_1^2, l_1^3) are indeed the correct Turing machine memory locations to be read at step 1. Thus $(l_1^1, l_1^2, l_1^3) = (l_1^1, l_1^2, l_1^3)$. This along with the deterministic nature of hash tree read write operations means that we must have,

$$\begin{array}{l} \bullet \ \ (\bar{l}_1^1,\bar{l}_1^2,\bar{l}_1^3) \leftarrow \mathsf{StepR}(\bar{\mathsf{st}}_0) \\ \bullet \ \ \left\{ (\bar{b}_1^j,\bar{l}_1^{13},\bar{\mathsf{st}}_1) \coloneqq \mathsf{StepW}(\bar{\mathsf{st}}_0,\bar{b}_1^1,\bar{b}_1^2,\bar{b}_1^3) \\ \bullet \ \ \left\{ (\bar{b}_1^j,\bar{\Pi}_1^k) \coloneqq \mathsf{HT}.\mathsf{Read}(\mathsf{tr\bar{e}e}_0^k,\bar{l}_1^k) \right\}_{k \in [3]} \bullet \ \ (\mathsf{tr\bar{e}e}_1^3,\bar{\mathsf{rt}}_1^3,\bar{\Pi}_1') \coloneqq \mathsf{HT}.\mathsf{Write}(\mathsf{tr\bar{e}e}_0^3,\bar{l}_1'^3,\bar{b}_1'^3) \\ \end{array}$$

Read and Write Completeness of the hash tree implies

 $\begin{array}{l} \text{HT.VerRead}(\mathsf{dk}_1,\bar{\bar{\mathsf{r}}}_0^1,\bar{l}_1^1,\bar{b}_1^1,\bar{\Pi}_1^1) = 1 \\ \text{HT.VerRead}(\mathsf{dk}_2,\bar{\bar{\mathsf{r}}}_0^2,\bar{l}_1^2,\bar{b}_1^2,\bar{\Pi}_1^2) = 1 \\ \text{HT.VerRead}(\mathsf{dk}_3,\bar{\bar{\mathsf{r}}}_0^3,\bar{l}_1^3,\bar{b}_1^3,\bar{b}_1^3,\bar{b}_1^3) = 1 \\ \text{HT.VerRead}(\mathsf{dk}_3,\bar{\bar{\mathsf{r}}}_0^3,\bar{l}_1^3,\bar{b}_1^2,\bar{l}_1^3,\bar{l}_1^3) = 1 \\ \end{array}$

HT.VerWrite(dk₃, $\bar{\mathbf{r}}_0^1$, \bar{l}_1^1 , \bar{l}_1^1 , \bar{l}_1^1 , \bar{l}_1^2 , \bar{l}_1^2 , \bar{l}_1^2 , \bar{l}_1^2 , \bar{l}_1^2) = 1

If $\{b_1^k\}_{k\in[3]}\neq Step(x,y,P,\mathsf{crs},1).\bar{b}$, then the read soundness assumption of HT implies that

 $(\mathsf{HT.VerRead}(\mathsf{dk},\bar{\mathsf{rt}}_0^1,\bar{l}_1^k,b_1^k,\Pi_1^k)=1)_{k\in[3]}$ happens with a negligible probability. Thus, with all but negligible probability we have that $(C_{index}, 1) \notin \mathcal{L}^{index}$ and we are done.

Let us say this is not the case, i.e., $\{b_1^k\}_{k\in[3]} = Step(x, y, P, \mathsf{crs}, 1).\bar{b}$, then the deterministic nature of the Turing machine write function StepW implies that $\mathsf{st}_1 = \bar{\mathsf{st}}_1$. Thus, for our assumption to be valid, it must be that $\{\mathsf{rt}_1^k\}_{k \in [3]} \neq Step(x,y,P,\mathsf{crs},1)$. $\bar{\mathsf{rt}}$. If $\mathsf{rt}_1^1 \neq \mathsf{rt}_1^1 = \mathsf{rt}_0^1$ or $\mathsf{rt}_1^2 \neq \bar{\mathsf{rt}}_1^2 = \mathsf{rt}_0^2$, then the definition of ϕ implies that $(C_{\mathsf{index}},\underline{1}) \notin \mathcal{L}^{\mathsf{index}}$. If this is not the case, then the only other possible option is $\mathsf{rt}_1^3 \neq \mathsf{rt}_1^3$. Now, the write soundness of HT implies that with all but negligible probability, HT.VerWrite($\mathsf{dk}_3, \bar{\mathsf{rt}}_0^3, \bar{l}_1'^3, \bar{l}_1'^3, \mathsf{rt}_1^3, \Pi_1) \neq 1$ must hold. If this is indeed true then our construction of C_{index} in Figure 3 implies that $(C_{\mathsf{index}}, 1) \notin \mathcal{L}^{\mathsf{index}}$.

Lemma 7.

$$\Pr[\mathsf{G}_{0,c}^{\mathcal{A}} = 1] = \Pr[\mathsf{G}_{0,d}^{\mathcal{A}} = 1].$$

Proof. Note that by definition, $h_1 = \mathsf{st}_1, \mathsf{rt}_1^1, \mathsf{rt}_1^2, \mathsf{rt}_1^3$. We already have that $\mathsf{rt}_1^1, \mathsf{rt}_1^2, \mathsf{rt}_1^3 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{rt}}$ and $\mathsf{st}_1 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{st}}$. Thus $h_1 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{h}}$ if and only if $\mathsf{rt}_1^3 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{rt}} \wedge \mathsf{st}_1 = Step(x, y, P, \mathsf{crs}, 1).\bar{\mathsf{st}}$.

Lemma 8.

$$\Pr[\mathsf{G}_{0,d}^{\mathcal{A}} = 1] \le \Pr[\mathsf{G}_{0,e}^{\mathcal{A}} = 1].$$

Proof. The number of conditions for the adversary to win simply decreases from Game $G_{0,d}$ to Game $G_{0,e}$, thus the probability of success must not increase.

A closer observation shows that $\mathsf{G}_{0,e}$ is indeed identical to the case when one puts i=1 in game G_i .

Combining these together, we show the base case of the induction to be true. Thus,

$$\Pr[\mathsf{G}^{\mathcal{A}} = 1] \le \Pr[\mathsf{G}_1^{\mathcal{A}} = 1] + \mathsf{negl}(\lambda).$$

Assuming that our induction hypothesis holds for some $j \in [T-1]$, we prove that it holds for j+1 as well. We note that by chain rule, it suffices to show that $\Pr[\mathsf{G}_j^{\mathcal{A}}=1] \leq \Pr[\mathsf{G}_{j+1}^{\mathcal{A}}=1] + \mathsf{negl}(\lambda)$. We can show this by a sequence of indistinguishable inner hybrids to transition from Game G_j to G_{j+1} which look like the following:

- $\mathsf{G}_{j,a}$ Identical to G_j except the SE hash extraction is done at S_{j+1} instead of S_j . Indistinguishability follows from the key indistinguishability of SE hash.
- $\mathsf{G}_{j,b}$ Extraction for one of the SE hashes changes from $I_{S_{j-1}}$ to $I_{S_{j+1}}$. However, this does not affect the reduction in any way as extraction at indices j-1 and j+1 are not used by the reduction at any stage.
- $\mathsf{G}_{j,c}$ The BARG key generation has a trapdoor at j+1. This can be done due to key indistinguishability of BARG.
- G_{j,d} The winning condition is changed to: if stSNARG.V (crs, $(x, y), H_P$, (c, Π)) = $1 \wedge ((x, y), T, P, H_P, \text{crs}) \notin \mathcal{L}_{TM} \wedge h_j = Step(x, y, P, \text{crs}, j).\bar{h}_j \wedge \{b_{j+1}^k\}_{k \in [3]} = Step(x, y, P, \text{crs}, j + 1).\bar{b}_{j+1} \wedge \text{rt}_{j+1}^3 = Step(x, y, P, \text{crs}, j + 1).\bar{\text{rt}}_{j+1} \wedge \text{st}_1 = Step(x, y, P, \text{crs}, 1).\bar{\text{st}}, \text{ return } 1.$
- $\begin{aligned} \mathsf{G}_{j,e} & \text{ The winning condition is changed to: if } \mathsf{stSNARG.V} \left(\mathsf{crs}, (x,y), H_P, (c,H)\right) = \\ & 1 \wedge \left((x,y), T, P, H_P, \mathsf{crs}\right) \notin \mathcal{L}_{\mathcal{TM}} \wedge \mathsf{h}_j = Step(x,y,P,\mathsf{crs},j).\bar{\mathsf{h}}_j \wedge \{b_{j+1}^k\}_{k \in [3]} = \\ & Step(x,y,P,\mathsf{crs},j+1).\bar{b}_{j+1} \wedge \mathsf{rt}_{j+1}^3 = Step(x,P,\mathsf{crs},j+1).\bar{\mathsf{rt}}_{j+1} \wedge \mathsf{st}_1 = \\ & Step(x,y,P,\mathsf{crs},1).\bar{\mathsf{st}} \wedge \mathsf{h}_{j+1} = Step(x,y,P,\mathsf{crs},j+1).\bar{\mathsf{h}}, \text{ return } 1 \end{aligned}$

$$\mathsf{G}_{j,f} \text{ if stSNARG.V} \left(\mathsf{crs}, (x,y), H_P, (c,\Pi)\right) = 1 \land \left((x,y), T, P, H_P, \mathsf{crs}\right) \notin \mathcal{L}_{\mathcal{TM}} \land \mathsf{h}_{j+1} = Step(x,y,P,\mathsf{crs},j+1).\bar{\mathsf{h}}, \text{ return } 1$$

The last three steps follow identically as Lemmas 67,8.

Observe $G_{j,f}$ is identical to outer Game G_{j+1} with indices renamed.

Thus, combining the lemmas above, we get

Lemma 9. Assuming extraction correctness of SE, semi-adaptive somewhere soundness of BARG, read and write soundness of HT,

$$\Pr[\mathsf{G}_i^{\mathcal{A}} = 1] \le \Pr[\mathsf{G}_{i+1}^{\mathcal{A}} = 1] + \mathsf{negl}(\lambda).$$

This follows from the combination of previous lemmas where we showed that the winning probability in the sequence of inner hybrids are either negligibly close to each other or increases (from Game $G_{j,e}$ to Game $G_{j,f}$).

Finally, we will show that the winning probability of \mathcal{A} is 0 in the final game G_T .

Lemma 10. Assuming extraction correctness of SE hash,

$$\Pr[\mathsf{G}_T^{\mathcal{A}} = 1] = 0.$$

Proof. The extraction correctness of SE ensures that h_T was indeed the state committed by the prover. Now, $h_T = \bar{h}_T$ cannot be true since our assumption of $(x, y, T, P, H_P, \operatorname{crs}) \notin \mathcal{L}_{TM}$ means that Turing Machine state after T steps cannot be an accept state. Thus, the adversary's win conditions cannot be simultaneously satisfied.

Note that this step does not require us to resort to BARG soundness. Due to our specific construction of \bar{h}_T , all we need ensure is that the state committed by the prover does not correspond to the correct state.

Compiling the lemmas together and using chain rule, it must be true that

$$\Pr[\mathsf{G}^{\mathcal{A}} = 1] \le \mathsf{negl}(\lambda)$$

which is a contradiction to our assumption that the scheme is not sound.

Lemma 11. Assuming T = poly(m, n), $T, m, n \leq 2^{\lambda}$, the stSNARG protocol in Figure 2 implies the unconditional existence of a publicly verifiable non interactive succinct delegation scheme sDel as defined above.

Proof. We provide an explicit construction of sDel assuming a semi-trusted SNARG stSNARG. Without loss of generality, we can assume that T is known a-priory.

- sDel.Setup(1^{λ}): Run stSNARG.Setup to generate crs.
- sDel.ProgAuth $(1^{\lambda}, crs)$: Generate a program $P \in \{0, 1\}^m$, state and run stSNARG.TrustHash(crs, P) to get H_P .
- sDel. $I(1^{\lambda}, \text{crs})$: Generate $x \in \{0, 1\}^n$.

- sDel. $W(\mathsf{crs}, P, \mathsf{state}, H_P, x)$: Generate $y \in \{0, 1\}$ and run stSNARG. $P(\mathsf{crs}, P, x, y, H_P)$ to get Π .
- sDel. $V(crs, x, y, H_P, \Pi)$: Run stSNARG. $V(crs, x, y, H_P, \Pi)$ return V's output.

Completeness and soundness of sDel follows from the completeness of stSNARG in a straightforward way. Refer to Supplementary material for detailed analysis. The proof size and verifier run time of stSNARG is $poly(\lambda, \log T) = poly(\lambda, \log |P|, \log |x|)$. Similarly, the prover run time of sDel is also $poly(\lambda, |P|, |x|)$.

6 Semi-Trusted Succinct Non-Interactive Argument with Zero Knowledge (ZK-stSNARG)

A publicly verifiable semi-trusted non interactive argument with zero-knowledge scheme

 ${\sf ZKstSNARG.Setup}, {\sf ZKstSNARG.TrustHash}, {\sf ZKstSNARG.P}, {\sf ZKstSNARG.V}) \\ {\sf is defined as} \\$

- ZKstSNARG.Setup(1^{λ} , 1^{T}): A randomized setup algorithm which on input security parameter λ , and number of Turing Machine steps T, outputs crs.
- ZKstSNARG.TrustHash(crs, P): A deterministic an honest algorithm which on input crs and a program $P \in \{0,1\}^m$ for some $m < 2^{\lambda}$, computes a succinct digest H_P of P. It then produces a statistically binding and extractable commitment C_P of H_P under randomness r_1 . It then gives out a pair public output $POut = C_P$ and private output $SOut = (H_P, r)$. Here SOut is made available to the prover only.
- ZKstSNARG.P(crs, P, x, y, SOut, POut): A deterministic prover algorithm which on input the crs, $P \in \{0,1\}^m$ for some $m < 2^{\lambda}$, $x \in \{0,1\}^n$ for some $n < 2^{\lambda}$, $y \in \{0,1\}$, SOut, and POut outputs a proof Π .
- ZKstSNARG.V(crs, x, y, POut, Π): A deterministic verification algorithm which on input crs, x, y, public output POut of stSNARG.TrustHash and proof Π , either accepts(output 1) or rejects(output 0) it.

We define the following language

$$\mathcal{L}_{\mathcal{TM}} \coloneqq \{ (P, x, y, T, \mathsf{POut}, \mathsf{crs}) \ | \exists (H_P, r_1) \text{ such that } \mathcal{TM}(P, x, y) = 1 \land (\mathsf{POut}, (H_P, r_1)) = \mathsf{ZKstSNARG}.\mathsf{TrustHash}(\mathsf{crs}, P) \}.$$

A ZKstSNARG satisfies the standard completeness, soundness and efficiency properties as stSNARG. It also has an additional property:

Non Interactive Zero Knowledge. For all $(P, x, y, T, \mathsf{POut}, \mathsf{crs}) \in \mathcal{L}_{\mathcal{TM}}$, there exists a PPT simulator $\mathsf{Sim} := (\mathsf{Sim}_1, \mathsf{Sim}_2, \mathsf{Sim}_3)$ such that the distributions of

$$\begin{split} (\mathsf{crs}, x, y, \mathsf{POut}, \varPi) \big| (\mathsf{crs}, \mathsf{aux}) &\leftarrow \mathsf{Sim}_1(1^\lambda, 1^T), \\ (\mathsf{POut}, \mathsf{aux}') &\leftarrow \mathsf{Sim}_2(\mathsf{crs}, \mathsf{aux}), \\ \varPi &\leftarrow \mathsf{Sim}_3(\mathsf{aux}', \mathsf{crs}, (x, y), \mathsf{POut}) \end{split}$$

and

$$\begin{split} (\mathsf{crs}, x, y, \mathsf{POut}, \varPi) \big| \mathsf{crs} &\leftarrow \mathsf{ZKstSNARG.Setup}(1^\lambda, 1^T), \\ (\mathsf{POut}, \mathsf{SOut}) &\leftarrow \mathsf{ZKstSNARG.TrustHash}(\mathsf{crs}, P), \\ \varPi &\leftarrow \mathsf{ZKstSNARG.P}(\mathsf{crs}, P, x, y, \mathsf{POut}, \mathsf{SOut}) \end{split}$$

are indistinguishable.

To achieve non interactive zero knowledge, we use the following additional primitives, namely (1) a statistically binding extractable commitment scheme $\mathsf{Com}_{\mathsf{bind}}$ as defined in Section 3, and (2) a Non Interactive Zero Knowledge argument $\mathsf{NIZK} := (\mathsf{NIZK}.\mathsf{Gen}, \mathsf{NIZK}.\mathsf{P}, \mathsf{NIZK}.\mathsf{V}).$

The protocol in Figure 5 demonstrates the extension of stSNARG to achieve Zero-Knowledge. The CRS in Figure 5 contains a statistically binding commitment to 0. This lets us extend \mathcal{L}_{TM} to the language,

$$\mathcal{L}_{\mathsf{hyb}} \coloneqq \left\{ \left. (P, x, y, T, C_P, \mathsf{crs}) \, \middle| \, \exists (H_P, r_1) \text{ such that } \mathcal{TM}(P, x, y) = 1 \right. \\ \left. \wedge \left(C_P, (H_P, r_1) \right) = \mathsf{ZKstSNARG.TrustHash}(\mathsf{crs}, P) \right. \\ \left. \vee \left(\exists r \text{ such that crs contains a commitment to 1 under randomness } r \right). \right\}$$

such that any witness to \mathcal{L}_{TM} is vacuously a witness to \mathcal{L}_{hyb} due to binding property of the commitment. We use NIZK for the following NP language:

$$\mathcal{L} \coloneqq \left\{ \left. (c.\mathsf{com}, \varPi.\mathsf{com}, (\mathsf{crs}, x, y, T), C_P) \, \middle| \, \exists r_1, r_2, r_3, r_4, c, \varPi, H_P \text{ such that} \right. \\ \left. \left(C_P = \mathsf{Com.C}(\mathsf{Com}_{\mathsf{bind}}.Key_1, H_P; r_1) \wedge c.\mathsf{com} = \mathsf{Com.C}(\mathsf{Com}_{\mathsf{bind}}.Key_2, c; r_2) \right. \\ \\ \wedge \varPi.\mathsf{com} = \mathsf{Com.C}(\mathsf{Com}_{\mathsf{bind}}.Key_3, \varPi; r_3) \wedge \mathsf{stSNARG.V}(\mathsf{crs}, ((x, y), T, H_P), (c, \varPi)) = 1 \right) \\ \\ \vee \mathsf{crs} \text{ contains } \mathsf{Com.C}(\mathsf{Com}_{\mathsf{bind}}.Key_4, 1; r_4) \right\}$$

Also, note that in this construction, the underlying stSNARG is built for the index circuit C'_{index} which is identical to C_{index} except that H_P is a part of the input and not hard-coded in the circuit as it is not known to the verifier.

Theorem 4. Assuming the existence of semi-trusted SNARGs and Extractable Statistically Binding Commitment Schemes, and NIZK as described in sections 3 and 5, Figure 5 is a publicly verifiable non-interactive semi-trusted SNARG with zero knowledge such that CRS size, proof size and verifier time are $poly(\lambda, \log T)$ and prover run time is $poly(\lambda, T)$.

```
Protocol 2 (stSNARG with Zero-Knowledge)
  - ZKstSNARG.Setup(1^{\lambda}, T):
         • \operatorname{crs}_1 \leftarrow \operatorname{stSNARG.Setup}(1^{\lambda}, 1^T)
         • Com<sub>bind</sub>.Key_1 \leftarrow Com.Gen(1^{\lambda})
         • Com<sub>bind</sub>.Key_2 \leftarrow Com.Gen(1^{\lambda})
         • Com<sub>bind</sub>.Key_3 \leftarrow Com.Gen(1^{\lambda})
         • \mathsf{Com}_{\mathsf{bind}}.Key_4 \leftarrow \mathsf{Com}.\mathsf{Gen}(1^{\lambda})
         • r_4 \leftarrow \$ \{0,1\}^{\lambda}, z \leftarrow \mathsf{Com.C}(\mathsf{Com}.Key_4, 0; r_4)
         • NIZK.crs \leftarrow NIZK.Gen(1^{\lambda})
         • return (crs_1, Com_{bind}.Key_1.Com_{bind}.Key_2, Com_{bind}.Key_3, z, NIZK.crs).
  ZKstSNARG.TrustHash(crs, P)
         • H_P \leftarrow \mathsf{stSNARG}.\mathsf{TrustHash}(\mathsf{crs},P)
         • r_1 \leftarrow \$ \{0,1\}^{\lambda}, C_P \leftarrow \mathsf{Com.C}(\mathsf{Com_{bind}}.Key_1, H_P; r_1) return (SOut :=
             (P, r_1), POut := C_P).
  - \mathsf{ZKstSNARG.P}(\mathsf{crs}, x, y, \mathsf{SOut}, \mathsf{POut}) :
         • (c, \Pi) \leftarrow \mathsf{stSNARG.P}(\mathsf{crs}, x, y, H_P)
         • r_2 \leftarrow \$ \{0,1\}^{\lambda}, c.\mathsf{com} \leftarrow \mathsf{Com}.\mathsf{C}(\mathsf{Com}_{\mathsf{bind}}.Key_2, c; r_2)
         • r_3 \leftarrow \$ \{0,1\}^{\lambda}, \Pi.\mathsf{com} \leftarrow \mathsf{Com}.\mathsf{C}(\mathsf{Com}_{\mathsf{bind}}.Key_3, \Pi; r_3)
         • NIZK.\Pi \leftarrow \text{NIZK.Prove} (\text{NIZK.crs}, (c.\text{com}, \Pi.\text{com}, (\text{crs}, x, y, T), C_P),
             ((H_P, r_1), (c, r_2), (\Pi, r_3), \bot)
         • return\ (c.com, \Pi.com, NIZK.\Pi).
  - ZKstSNARG.V(crs, (x, y), POut = C_P, c.com, \Pi.com, NIZK.\Pi):
         • return 1 if and only if
             NIZK.V(NIZK.crs, (c.com, \Pi.com, (crs, x, y, T), C_P), NIZK.\Pi) = 1.
```

Fig. 5: Semi-Trusted Universal Turing Machine Delegation with Non Interactive Zero-Knowledge

Completeness and Efficiency. Completeness follows from the completeness of the underlying stSNARG, NIZK and the binding property of the commitment. Similarly succinctness follows from the efficiency of stSNARG, NIZK, and the binding commitment Com_{bind}.

Soundness. The soundness following by a straightforward reduction using the CRS indistinguishability and Statistical Binding of $\mathsf{Com}_{\mathsf{bind}}$, and the soundness of the underlying $\mathsf{stSNARG}$. We can construct an adversary that breaks the soundness of the underlying $\mathsf{stSNARG}$ using the following steps:

- 1. Change the keys for Com_{bind} to be generated by TGen. This can be done due to CRS indistinguishability.
- 2. The reduction can now extract the committed proof from $c.\mathsf{com}$ and $\Pi.\mathsf{com}$. This is because the reduction has access to the trapdoor commitment key.
- The stSNARG can now output the extracted proof. The extraction correctness of Com_{bind} ensures that if ZKstSNARG is not sound, then this adversary breaks the soundness of stSNARG.

A formal analysis is presented in the Supplementary Material.

 $Zero ext{-}Knowledge.$

```
zk - stSNARG Simulator NIZK.Sim := (Sim_1, Sim_2, Sim_3)
    • \mathsf{Sim}_1(1^{\lambda}, 1^T) :
            1. \ \mathsf{SE}.\overset{\,\,{}_{'}}{K_{\mathsf{even}}} \leftarrow \mathsf{SE}.\mathsf{Gen}(1^{\lambda},1^{M_{\lambda,T}}.1^{L_{\lambda}})
            2. SE.K_{odd} \leftarrow SE.Gen(1^{\lambda}, 1^{M}, 1^{L})
            3. \; \; \mathsf{BARG.crs} \leftarrow \mathsf{BARG.Gen}(1^{\lambda}, 1^{T+1}, 1^{|C_{\mathsf{index}}|})
            4. dk \leftarrow HT.Gen(1^{\lambda})
            5. \mathsf{Com}_{\mathsf{bind}}.Key_1 \leftarrow \mathsf{Com}.\mathsf{Gen}(1^{\lambda})
            6. \mathsf{Com}_{\mathsf{bind}}.Key_2 \leftarrow \mathsf{Com}.\mathsf{Gen}(1^{\lambda})
            7. \mathsf{Com}_{\mathsf{bind}}.Key_3 \leftarrow \mathsf{Com}.\mathsf{Gen}(1^{\lambda})
            8. \mathsf{Com}_{\mathsf{bind}}.Key_4 \leftarrow \mathsf{Com}.\mathsf{Gen}(1^{\lambda}), r_4 \leftarrow \mathbb{N}, z \leftarrow \mathsf{Com}.\mathsf{C}(\mathsf{Com}.Key_4, 1; r_4)
            9. NIZK.crs \leftarrow NIZK.Gen(1^{\lambda})
          10. return
                   \mathsf{crs} \coloneqq (\mathsf{SE}.K_{\mathsf{even}}, \mathsf{SE}.K_{\mathsf{odd}}, \mathsf{BARG}.\mathsf{crs}, \mathsf{dk}, \mathsf{Com}_{\mathsf{bind}}.Key_1.\mathsf{Com}_{\mathsf{bind}}.Key_2, \mathsf{Com}_{\mathsf{bind}}.Key_3, z, \mathsf{NIZK}.\mathsf{crs})
                   and aux := r_4
    • Sim<sub>2</sub>(crs, aux):
            1. r_1 \leftarrow \$\{0,1\}^{\lambda}, C_P \leftarrow \mathsf{Com.C}(\mathsf{Com}_{\mathsf{bind}}.Key_1,0;r_1) \text{ return } \mathsf{POut} \coloneqq C_P.
            2. return (POut, aux' := aux)
     \begin{split} \bullet & \ \mathsf{Sim}_3(\mathsf{crs},\mathsf{aux}',(x,y),\mathsf{POut} \coloneqq C_P): \\ & \ 1. \ \ r_2 \leftarrow \$ \left\{0,1\right\}^{\lambda}, c.\mathsf{com} \leftarrow \mathsf{Com}.\mathsf{C}(\mathsf{Com}_{\mathsf{bind}}.Key_2,0;r_2) \end{split} 
            2. Generate a dummy proof \hat{\Pi}
            3. r_3 \leftarrow \$ \{0,1\}^{\lambda}, \Pi.\mathsf{com} \leftarrow \mathsf{Com}.\mathsf{C}(\mathsf{Com}_{\mathsf{bind}}.Key_3,0;r_3)
            4. \mathsf{NIZK}.\Pi \leftarrow \mathsf{NIZK}.\mathsf{Prove}\left(\mathsf{NIZK}.\mathsf{crs}, (c.\mathsf{com}, \Pi.\mathsf{com}, (\mathsf{crs}, x, y, T), C_P), (\bot, \bot, \bot, \mathsf{aux})\right)
            5. return (c.com, \Pi.com, NIZK.\Pi).
```

The proof of zero-knowledge follows from a sequence of hybrids.

- We define a game G' which is identical to G_0 except that crs has a commitment of 1 instead of 0. Note that an honest prover does not make use of this section of the crs in its proof. Consider hyb' as the output distribution of intermediate G'. All other algorithms in G' remains identical as G_0 . hyb $_0$ must be indistinguishable from hyb', otherwise we can construct an efficient adversary that breaks the computational hiding property of Com_{bind} .
- The hybrid game G'' with output distribution hyb" works like G' except stSNARG.P computes $(c.\mathsf{com}, \mathsf{NIZK}.\Pi)$ honestly and then ignores $c.\mathsf{com}$ and outputs $(c_1, \mathsf{NIZK}.\Pi)$ where c_1 is the statistical binding commitment to the 0 string using $\mathsf{Com}_{\mathsf{bind}}$. The indistinguishability of hyb' and hyb" follows from the computational hiding property of $\mathsf{Com}_{\mathsf{bind}}$.
- We now define another hybrid game G''' where everything remains identical as G'' but the NIZK proof NIZK.P proves that crs has a commitment of 1 using randomness r as a witness. This is indeed a valid witness for the same language $\mathcal{L}^*_{\text{hyb}}$. Observe that G'' and G''' have identical CRS. However, NIZK.P in each case uses different witnesses, namely r and $((c, r_{\text{com}_2}), \Pi)$ respectively. Thus, the Witness Indistinguishability of NIZK implies indistinguishability of G'' and G'''.
- In the next hybrid G"", trusted commitment generator is replaced by Sim₂ which on input crs simply outputs a hiding commitment to the 0 string. Note that the output of Sim₂ is not used anywhere else in the proof and its output is computationally indistinguishable from the public output of ZKstSNARG.TrustHash(crs, P) because of the hiding property of commitment scheme.

- In the final game G_1 , Sim_1 uses the same crs as the previous hybrid. Sim_3 ignores all operations performed by the prover and only outputs c_1 which is the statistical binding commitment to the 0 string using Com_{bind} and sends a NIZK proof as G''''. The output distributions of G'''' and G_1 are indeed identical as the output of Sim_3 solely depends on the output of Sim_1 , Sim_2 and the commitment of the 0 string c_1 .

Combining all the hybrids, we prove that G_0 and G_1 have output distributions which are computationally indistinguishable.

Public Verifiable Non Interactive Succinct Delegation with Zero Knowledge A direct extension of Lemma 11 gives us the following corollary,

Corollary 2. Assuming T = poly(m, n), $T, m, n \leq 2^{\lambda}$, the ZKstSNARG protocol in Figure 5 implies the unconditional existence of a publicly verifiable non interactive succinct delegation scheme with zero knowledge.

The zero knowledge simulator for the delegation scheme $\mathsf{zk}-\mathsf{sDel}.\mathsf{Sim} := (\mathsf{zk}-\mathsf{sDel}.\mathsf{Sim}_1, \mathsf{zk}-\mathsf{sDel}.\mathsf{Sim}_2)$ can simply run the $\mathsf{stSNARG}$ ZK-simulator. More specifically, $\mathsf{zk}-\mathsf{sDel}.\mathsf{Sim}_1$ and $\mathsf{zk}-\mathsf{sDel}.\mathsf{Sim}_2$ call $\mathsf{zk}-\mathsf{stSNARG}.\mathsf{Sim}_1$ and $\mathsf{zk}-\mathsf{stSNARG}.\mathsf{Sim}_2$ respectively above. The proof follows in a straightforward manner, hence we skip the details.

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