

Bounded Tamper Resilience: How to go beyond the Algebraic Barrier

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Joint work with

Ivan Damgård, Sebastian Faust & Daniele Venturi



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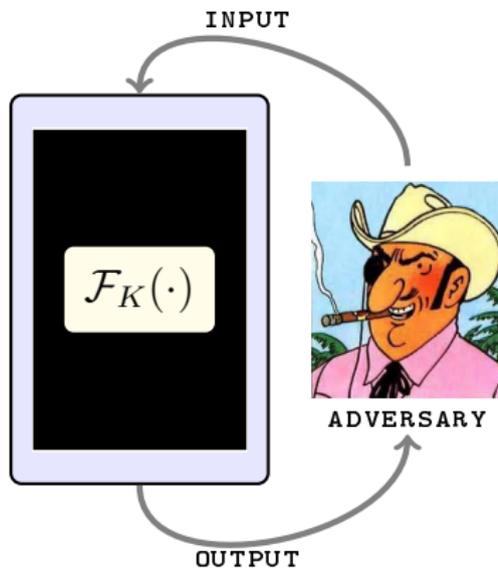
Physical Attacks: Theory vs Reality



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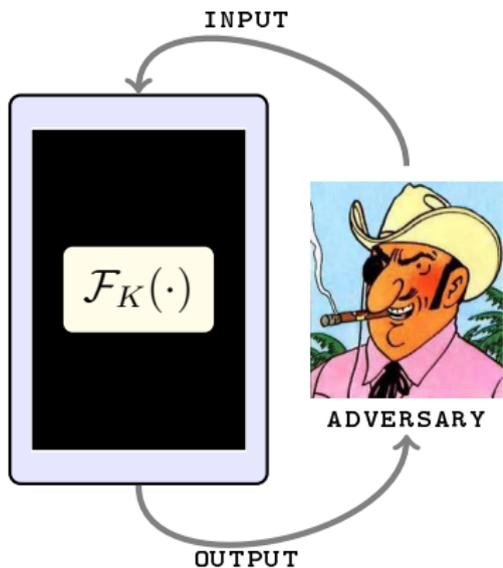
Standard Analysis:
Blackbox



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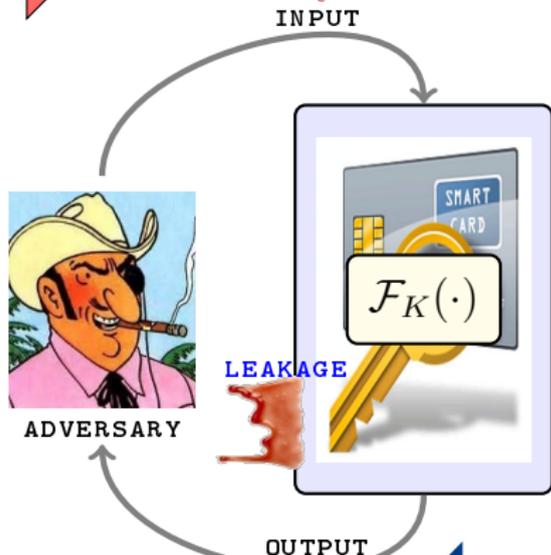
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IMPLEMENT

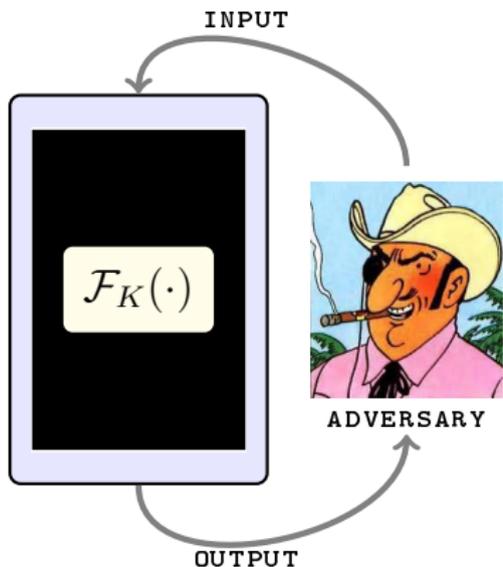
Reality:
Physical Attacks



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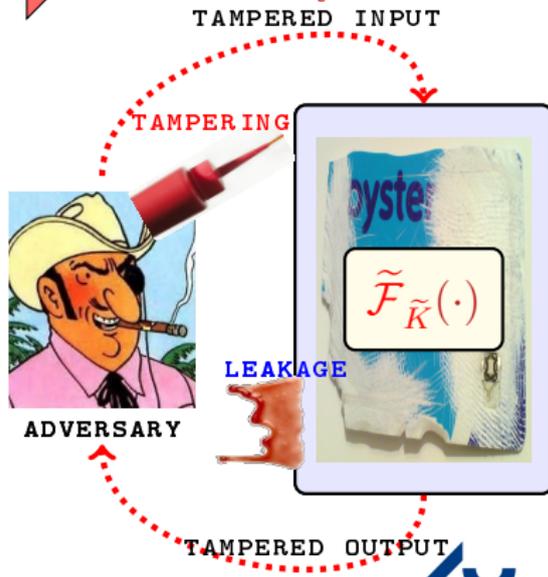
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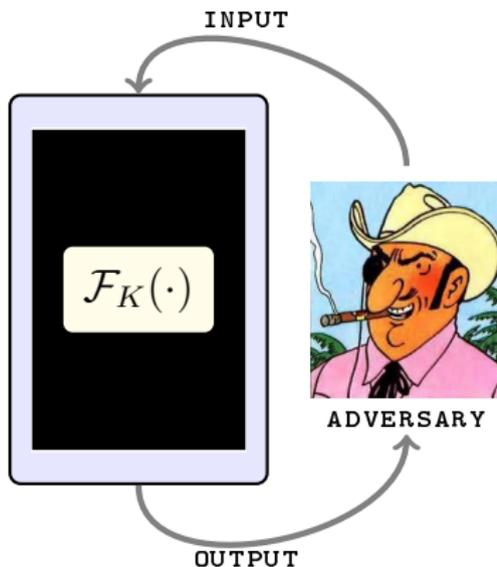
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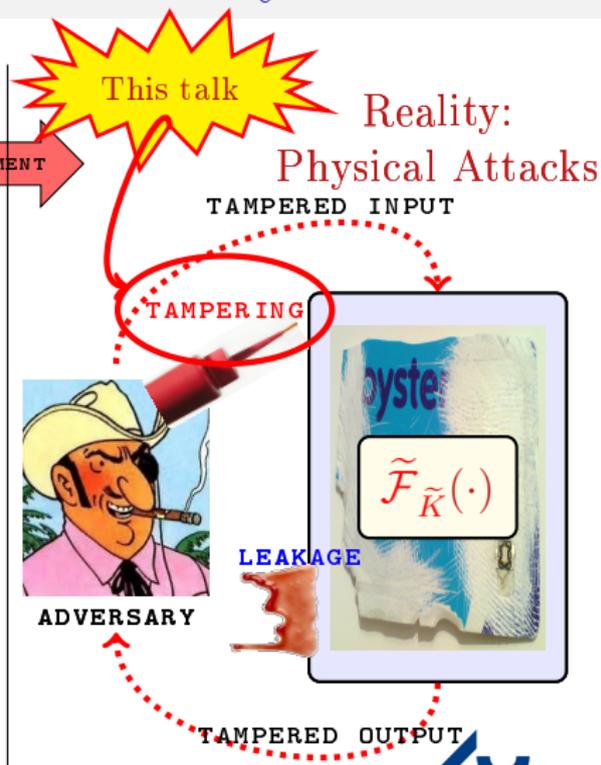
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Physical Attacks: Theory vs Reality

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IMPLEMENT



Why care about Tampering ?



Why care about Tampering ?

Captures Practical Attacks

Fault Attack!



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Fault Attack!

Example

Boneh et al. [JOC'01]: Inject **single** (random) fault to the signing-key of some type of RSA-sig
 \implies **factor** RSA-modulus !



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Devastating !

More ...

Anderson & Kuhn [USENIX'96]

Skorobogatov et al. [CHES'02]

Coron et al. [CHES'09]

...



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Models of Tampering

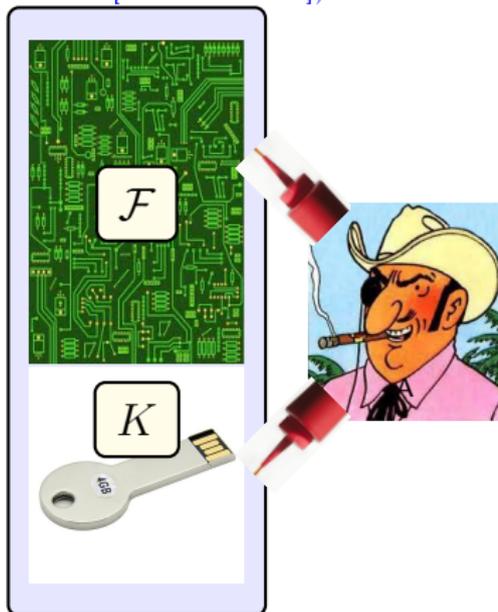


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Models of Tampering

Memory & Computation

(Ishai et al. [EUROCRYPT'06])

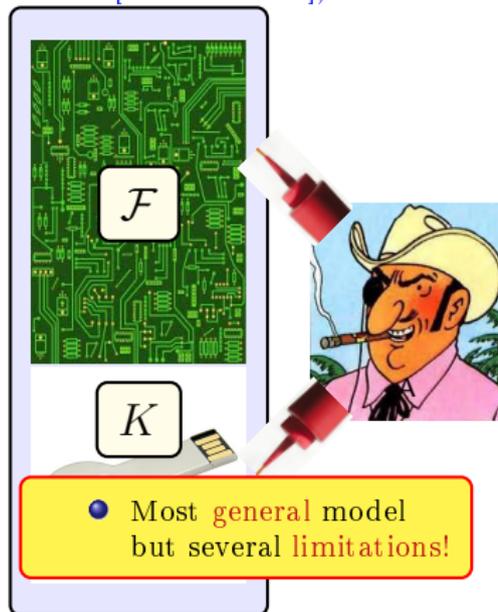


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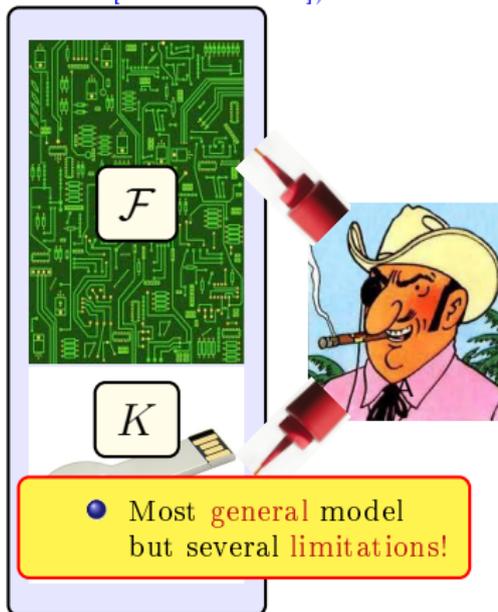


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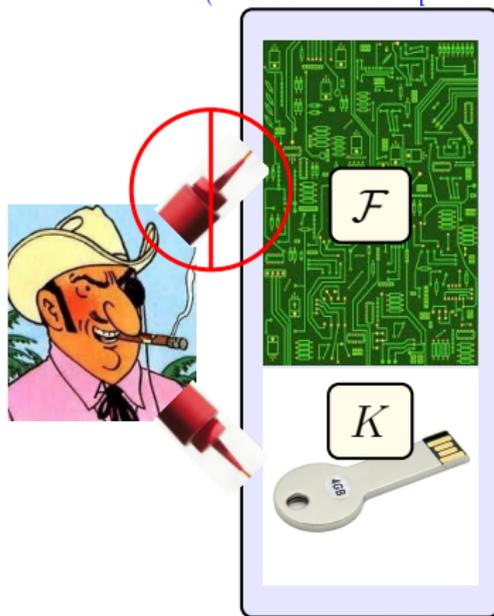
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Only Memory

(Gennaro et al. [TCC'04])

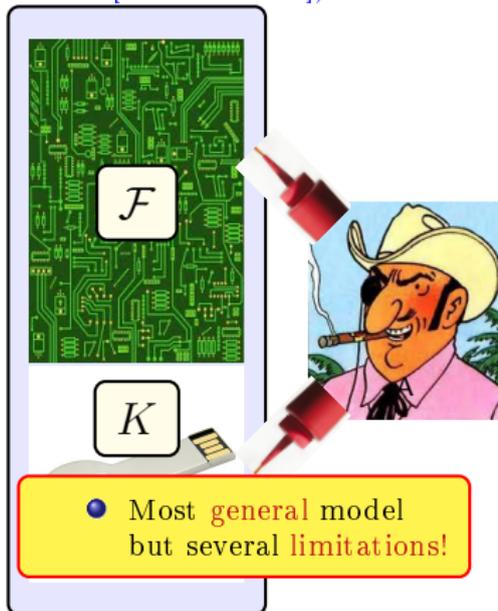


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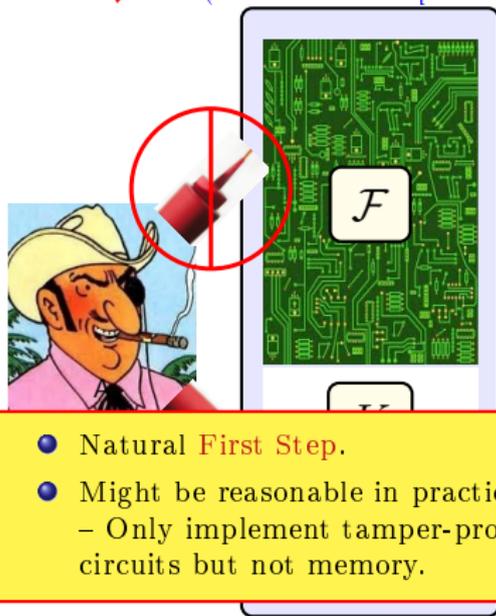


- Most **general** model but several **limitations!**

Our Focus

Only Memory

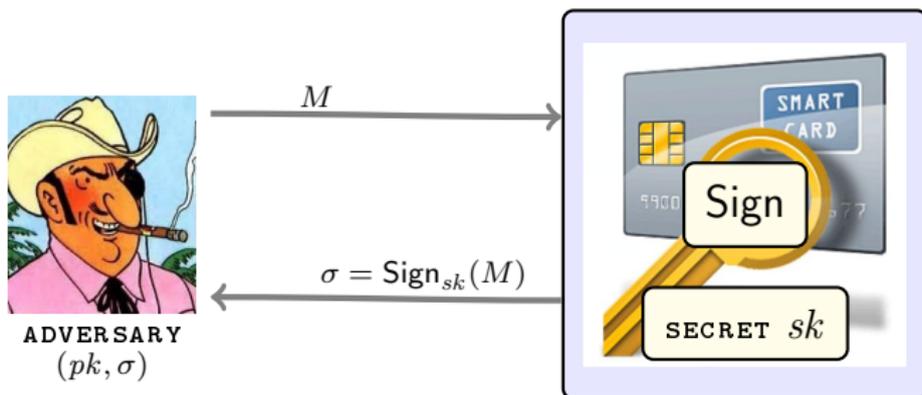
(Gennaro et al. [TCC'04])



- Natural **First Step**.
- Might be reasonable in practice:
 - Only implement tamper-proof circuits but not memory.

Memory Tampering: Illustrative Example

Untampered Output



Memory Tampering: Illustrative Example

Fix family Γ

$$1 \leq i \leq t \\ (t = \text{poly}(k))$$

Tampering

CHOOSE $T_i(\cdot) \leftarrow \Gamma$

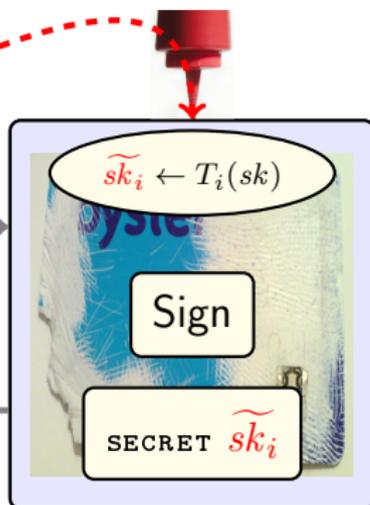
$T_i(\cdot)$

M



ADVERSARY
 $(pk, \sigma, \tilde{\sigma}_1, \dots, \tilde{\sigma}_i)$

$$\tilde{\sigma}_i = \text{Sign}_{\tilde{sk}_i}(M)$$



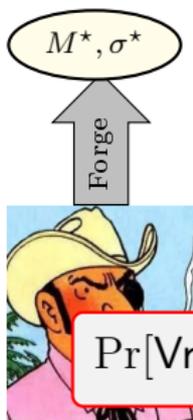
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Requirement

$$\Pr[\text{Vrfy}_{pk}(M^*, \sigma^*) = 1] \leq \text{negl}$$



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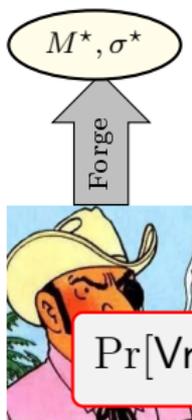
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a.k.a. **RKA-model**

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Memory Tampering: Illustrative Example

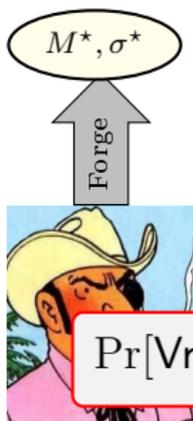
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a.k.a. RKA-model

Challenge

Natural Goal:
Broaden Γ



ADVERSARY
($pk, \sigma, \tilde{\sigma}_1, \dots, \tilde{\sigma}_i, \dots, \tilde{\sigma}_t$)

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....RKA Model



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First model: Bellare and Kohno [EUROCRYPT'03].



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More

Lucks[FSE'04], BC[CRYPTO'10], BCM[ASIACRYPT'11], BPT[ASIACRYPT'12],
AHI[ICS'11], BR[FSE'13], GOR[TCC'11], Wee[PKC'12]...



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RKA(in short)

- 1 **Unrestricted** t – any polynomial.



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Drawback!

May **NOT** be realistic – arbitrary fault may not be captured by algebraic functions.

This Work: An alternative Model



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Our Goal: Going beyond algebraic barrier

We want tamper-resilience for **unrestricted** Γ .



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Impossibility – Gennaro et al.[TCC'04]

Both unrestricted and continuous tampering impossible!



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Our Solution: **Bounded Tampering**

- **Unrestricted** Γ – tamper with **any** efficient function !
- Bounded t – tamper only **bounded** number of times.



Our Contributions: Overview



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A new model: **BLT**

Bounded **L**eakage & **T**ampering – **F**irst model of Bounded Tampering.



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– Analogous to Leakage-resilient Cryptography  - **HOT!**



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Concrete schemes based on **s**tandard assumptions (DL, Factoring)



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- ① ID-schemes based on some class of Σ -protocols are BLT secure – e.g. Okamoto.
- ② BHHO encryption scheme is BLT-secure.

Moreover...

- Boost to Continuous Tampering using untamperable **Floppy**
- New Technique: Reduce tamper-resilience from leakage-resilience

Recall: ID-Scheme

P wants to convince V that P knows
secret sk w.r.t pk



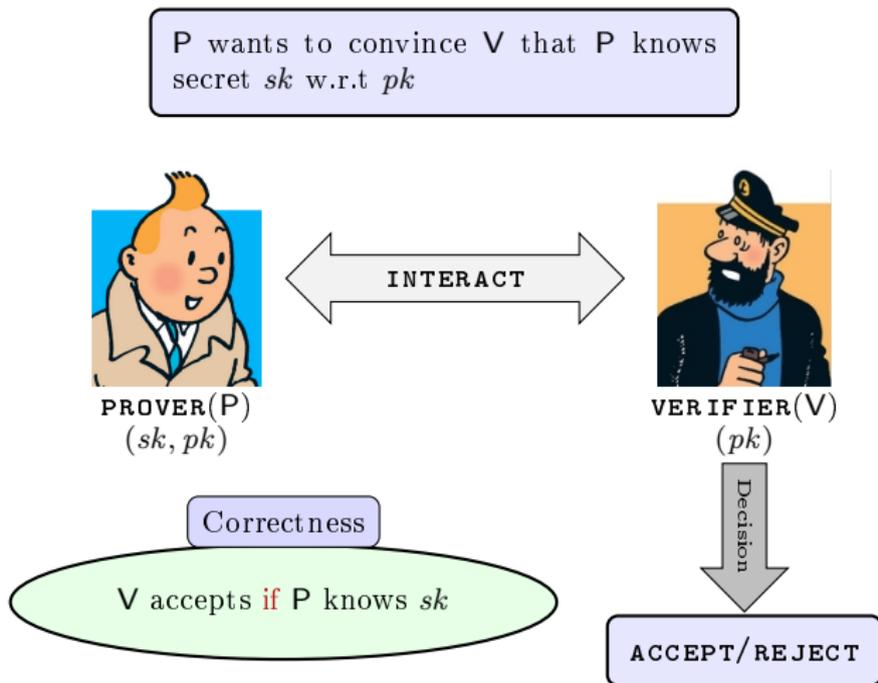
PROVER(P)
(sk, pk)



VERIFIER(V)
(pk)



Recall: ID-Scheme



BLT Model for ID-Schemes



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BLT Model for ID-Schemes

Parameter
 t



PROVER(P)
(sk)



ADVERSARY(A)
(pk)



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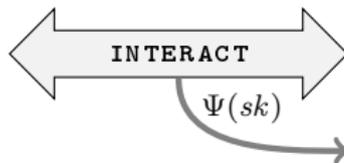
BLT Model for ID-Schemes

Untampered Query

Parameter
 t



PROVER(P)
 (sk)

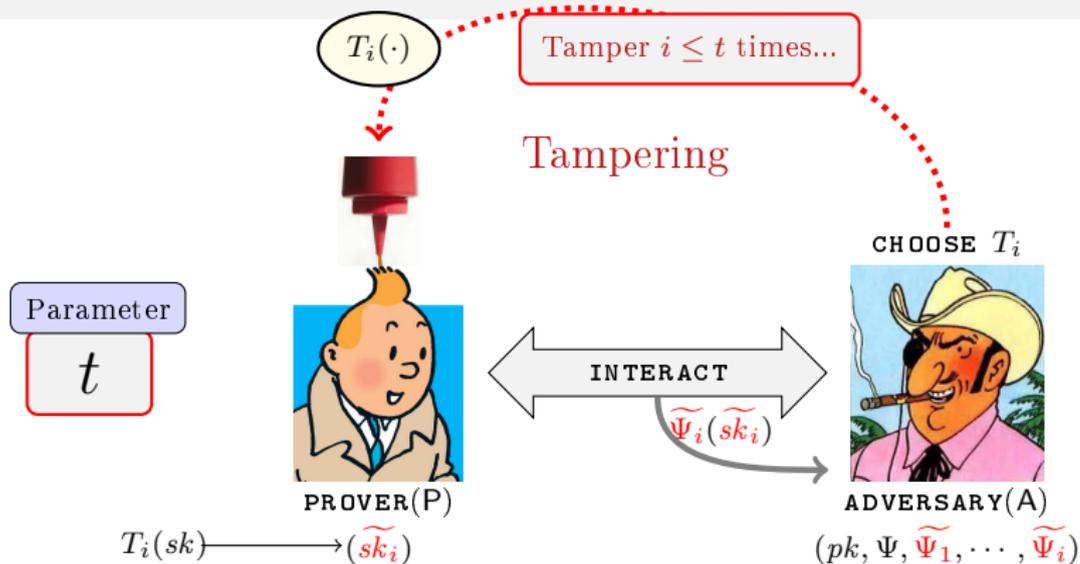


ADVERSARY(A)
 (pk, Ψ)



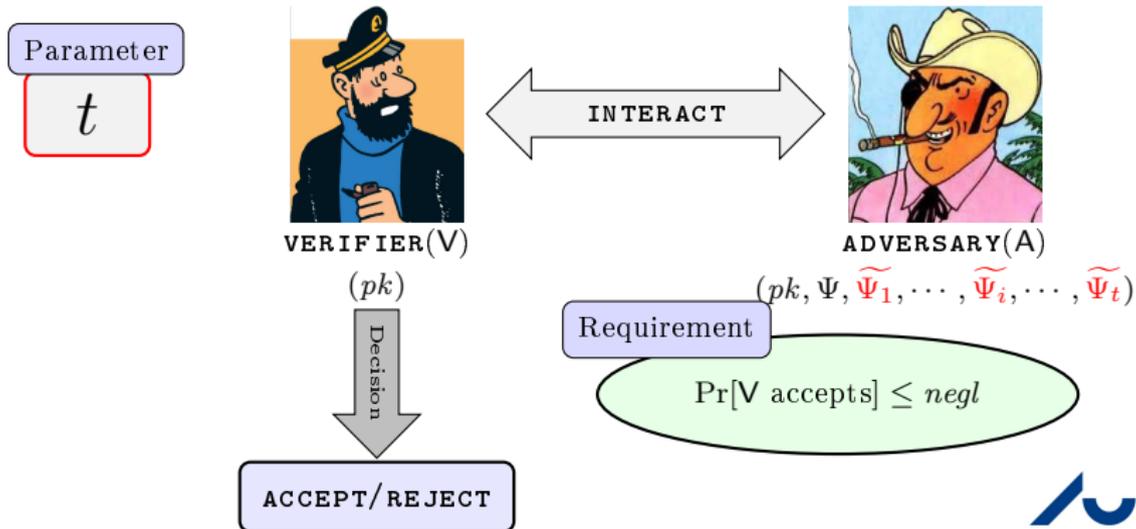
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BLT Model for ID-Schemes



BLT Model for ID-Schemes

Challenge Phase

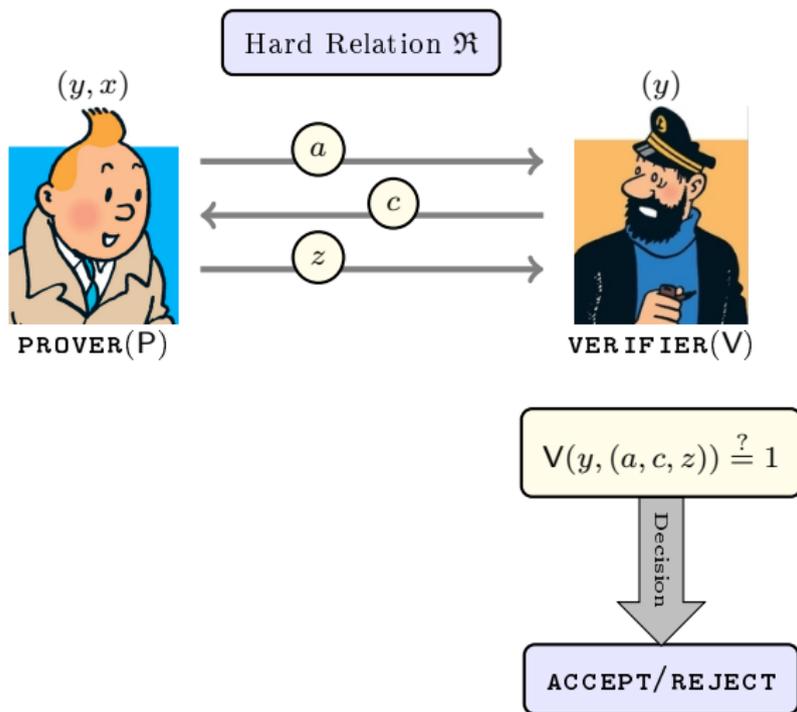


ID-Scheme from Σ -protocol (Cramer '96)Hard Relation \mathfrak{R}

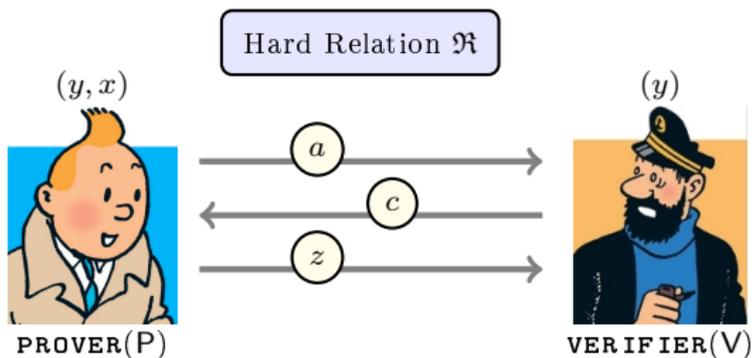
Definition (informal)

\mathfrak{R} is hard if no PPT adversary can output (y, x, x') such that $(y, x) \in \mathfrak{R} \wedge (y, x') \in \mathfrak{R}$.



ID-Scheme from Σ -protocol (Cramer '96)

ID-Scheme from Σ -protocol (Cramer '96)



Theorem(informal)

This is BLT-secure for $t \leq \frac{|x|}{|y|} - 1$

$$V(y, (a, c, z)) \stackrel{?}{=} 1$$

Decision

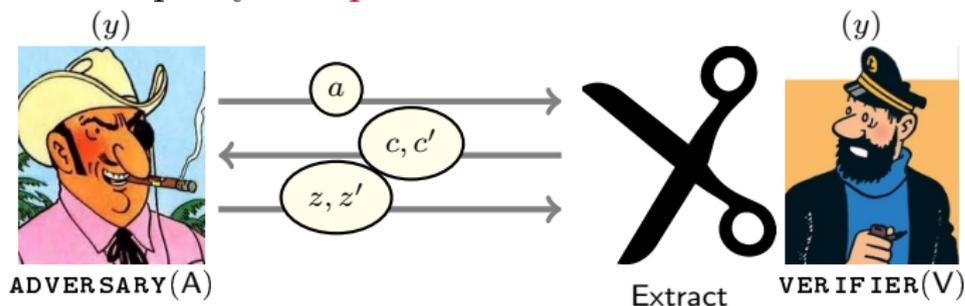
ACCEPT/REJECT



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ID-Scheme from Σ -protocol (Cramer '96)

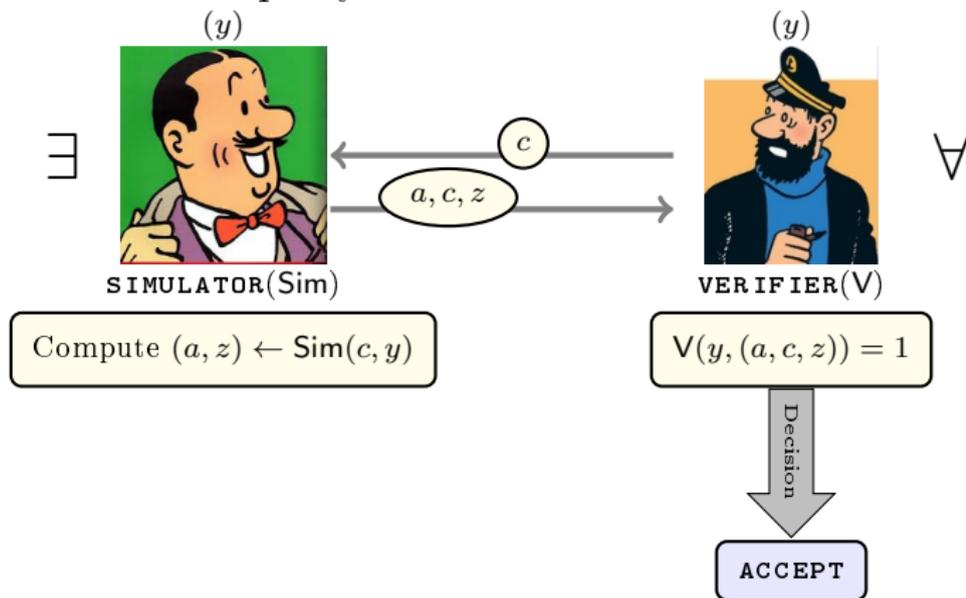
Property-1: **Special Soundness**



$$V(y, (a, c, z)) = 1 \wedge V(y, (a, c', z')) = 1$$

$$(y, x^*) \in \mathfrak{R} : x^* \leftarrow \text{Extract}((a, c, z), (a, c', z'))$$



ID-Scheme from Σ -protocol (Cramer '96)Property-2: **Honest Verifier ZK**

BLT-security (Proof Intuitions)

Idea

Reduction from the Hard Relation \mathfrak{R}



CHALLENGER(C)



REDUCTION(B)



ADVERSARY(A)



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BLT-security (Proof Intuitions)

Idea

Reduction from the Hard Relation \mathfrak{R}

Hardness Game w.r.t \mathfrak{R}



CHALLENGER(C)



REDUCTION(B)



ADVERSARY(A)

Goal: Find $(y, x, x^*) : (y, x) \in \mathfrak{R} \wedge (y, x^*) \in \mathfrak{R} \wedge (x \neq x^*)$

Sample $(y, x) \in \mathfrak{R}$.



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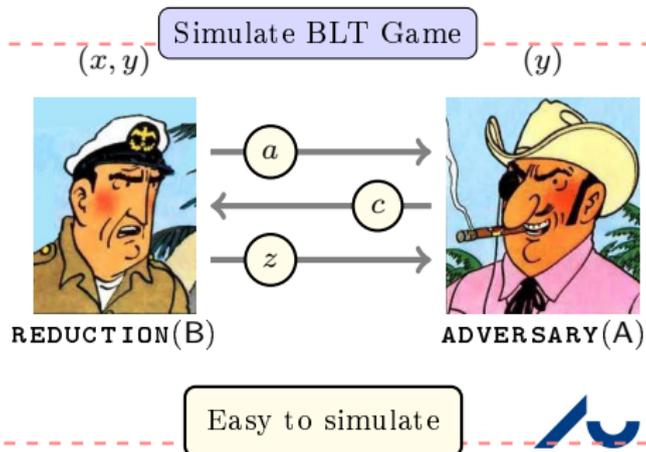
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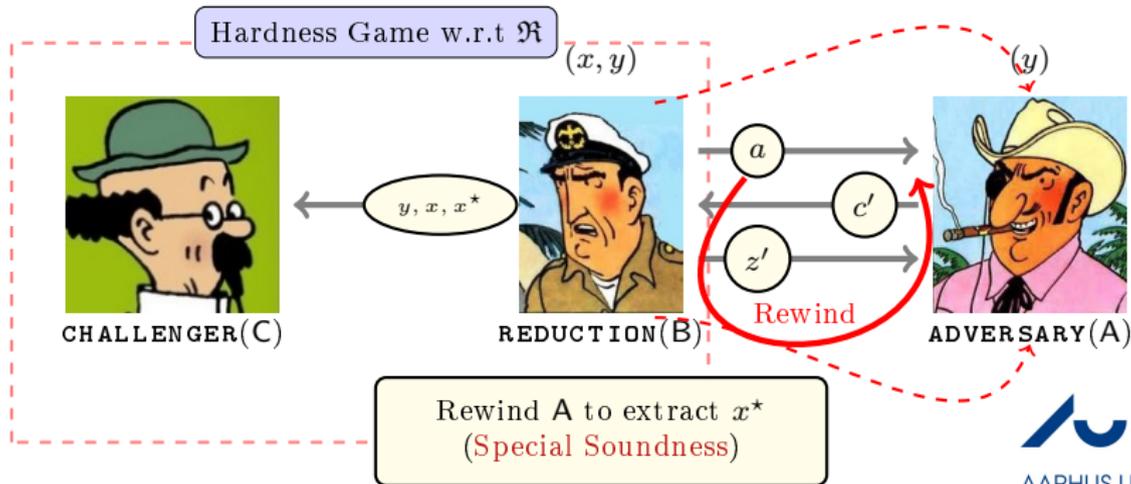


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BLT-security (Proof Intuitions)

Idea

Reduction from the Hard Relation \mathfrak{R}



BLT-security (Proof Intuitions)

Main Challenge

Each tampering experiment outputs poly-many transcripts—**HUGE!**
To Prove: This does **NOT** leak much about x .

Simulate BLT Game

(x, y)



REDUCTION(B)

(y)



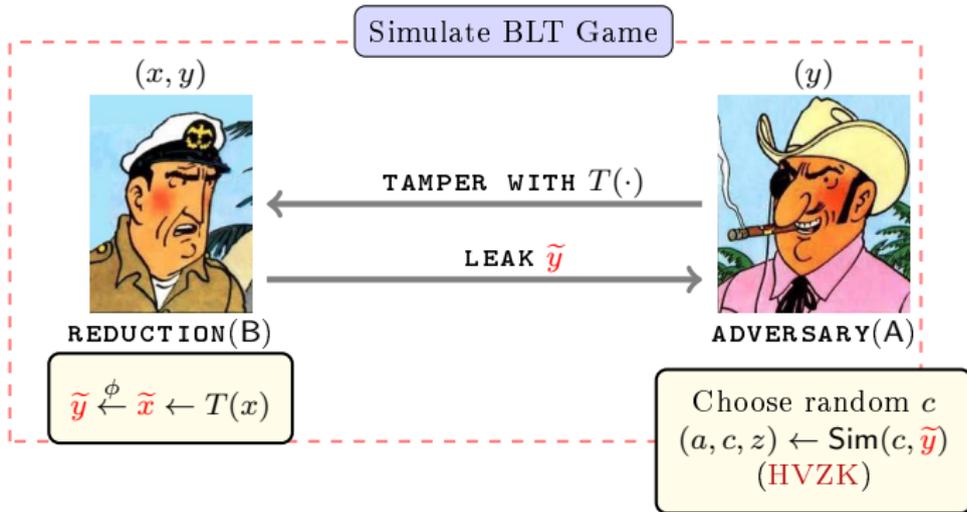
ADVERSARY(A)



BLT-security (Proof Intuitions)

Technique

For each tampering:
LEAK tampered public statement
 Only $|y|$ bits – **SHORT!**



Concrete Instantiation and Signatures



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Concrete Instantiation and Signatures

- Generalized Okamoto ID-Scheme is bounded-leakage-resilient
 - Alwen et al. [CRYPTO'09].



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- Additionally allows **tampering with the public parameters** (e.g. characteristic prime p) – but independently from $sk!$ – **impossible** when tampering jointly.



Concrete Instantiation and Signatures

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- Also **BLT**-secure—**this work**.
- Additionally allows **tampering with the public parameters** (e.g. characteristic prime p) – but independently from $sk!$ – **impossible** when tampering jointly.
- BLT-secure **signatures** using Fiat-Shamir transform. – requires random oracles.



IND-CCA-BLT



IND-CCA-BLT

Parameter

 t **CHALLENGER(C)**
(sk)**ADVERSARY(A)**
(pk)

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IND-CCA-BLT

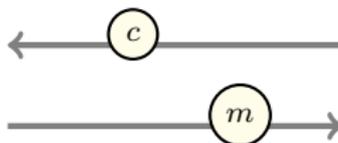
Untampered Query

Parameter
 t



CHALLENGER (C)
 (sk)

$m \leftarrow \text{Dec}(sk, c)$

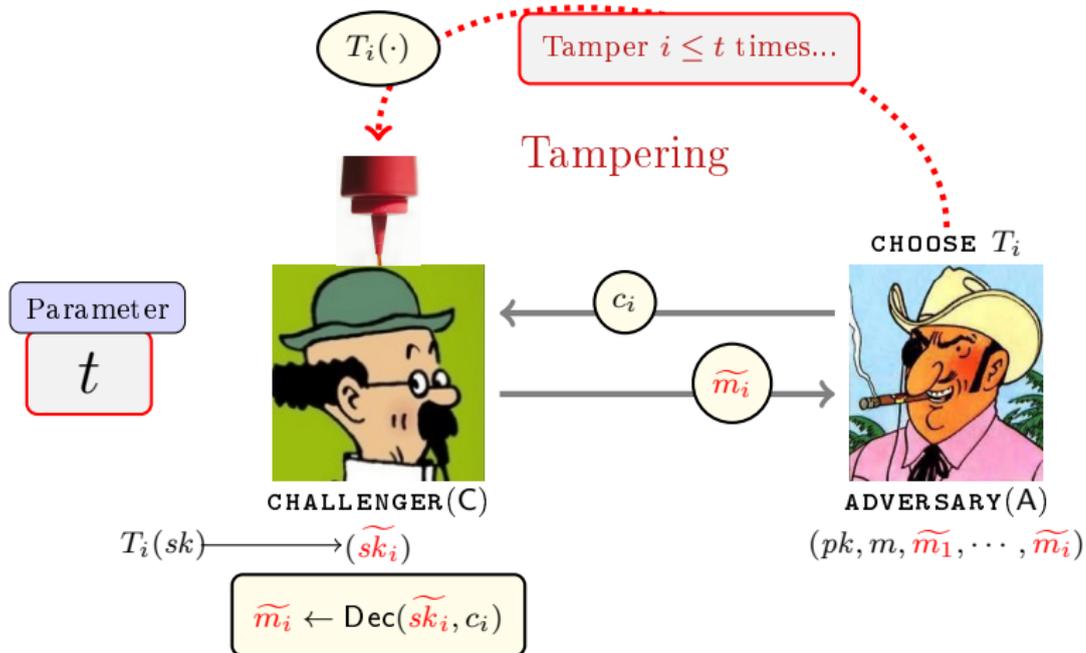


ADVERSARY (A)
 (pk, m)



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IND-CCA-BLT



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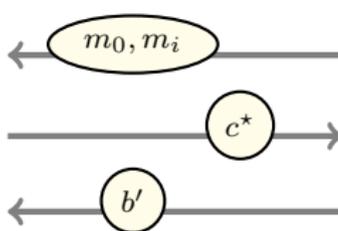
Challenge Phase

Parameter

t



CHALLENGER (C)



ADVERSARY (A)

$(pk, m, \tilde{m}_1, \dots, \tilde{m}_i, \dots, \tilde{m}_t)$

Choose $b \xleftarrow{\$} \{0, 1\}$
 $c^* \leftarrow \text{Enc}(pk, m_b)$

Requirement

$$|\Pr[b = b'] - \frac{1}{2}| \leq \text{negl}$$



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Results: BLT-secure PKE



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A general transformation



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Two Steps.



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- 2 **IND-CPA-BLT** \implies **IND-CCA-BLT** – using a **tSE-NIZK** (similar to Dodis et al.[ASIACRYPT'10])



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Limitation!

Can **not** tamper(or leak) after challenge phase.

Continuous tamper-resilience: Floppy Model

Each user has a



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Continuous tamper-resilience: Floppy Model

Each user has a



$\mathcal{CS}(sk)$
BLT-secure

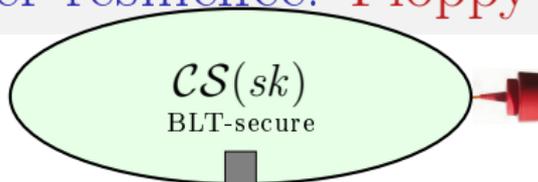


$A(pk)$



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Continuous tamper-resilience: Floppy Model



$A(pk)$

Floppy

Holds secret update key uk
Sample fresh randomness r
Update $(sk') \leftarrow \text{Refresh}(uk, r)$

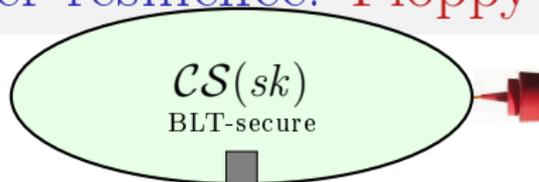


$A(pk)$



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Continuous tamper-resilience: Floppy Model



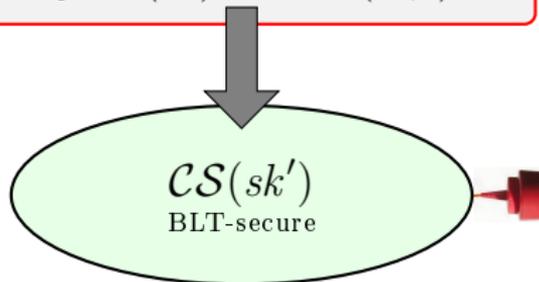
$A(pk)$

Floppy

Holds secret update key uk
Sample fresh randomness r
Update $(sk') \leftarrow \text{Refresh}(uk, r)$



$A(pk)$

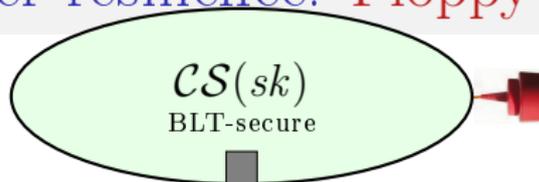


$A(pk)$



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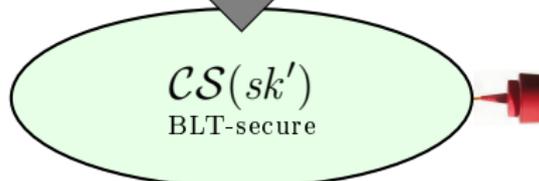
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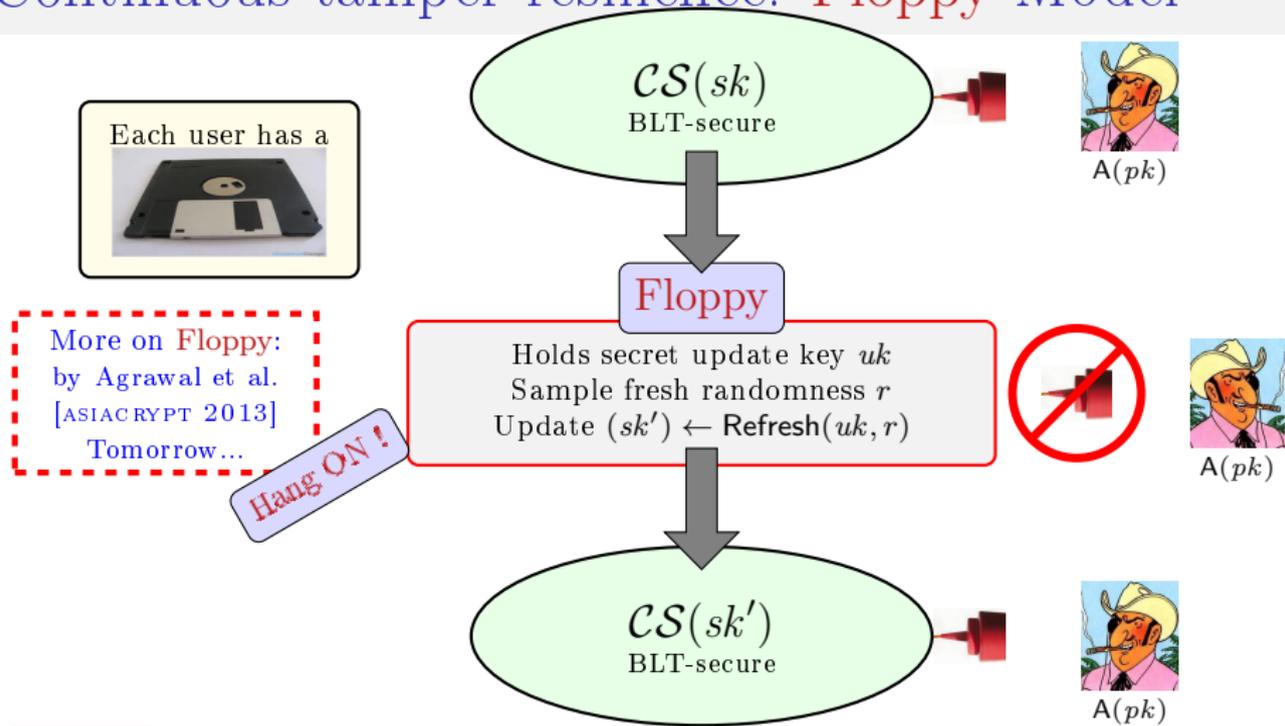
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Results

- 1 ID: A general transformation: any BLT-ID + cma-Sig \implies Floppy-ID.
- 2 PKE: A concrete construction from BHHO.

SITY

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Conclusion



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- First model of **Bounded Tamper Resilience**.



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- Open Questions.
 - Boosting to continuous tampering model **without Floppy**.
 - **Post-challenge** tampering for BLT-PKE.





THANK YOU !



Question(s)?



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