Functional Encryption for Turing Machines with Dynamic Bounded Collusion from LWE

Shweta Agrawal

Narasimha Sai Vempati

Monosij Maitra

Shota Yamada

















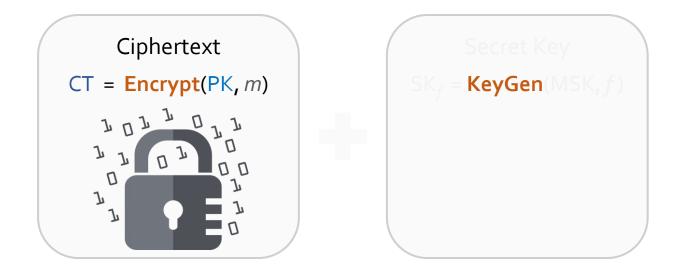
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Setup (1^{λ}) : (PK, MSK)



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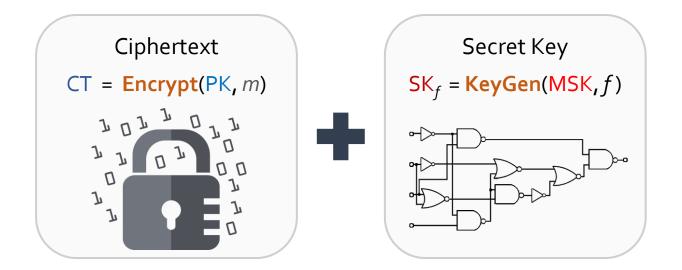
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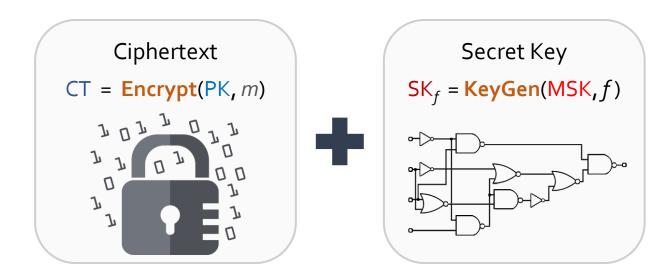
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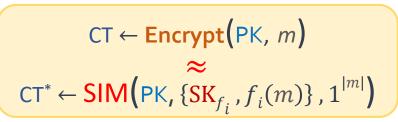


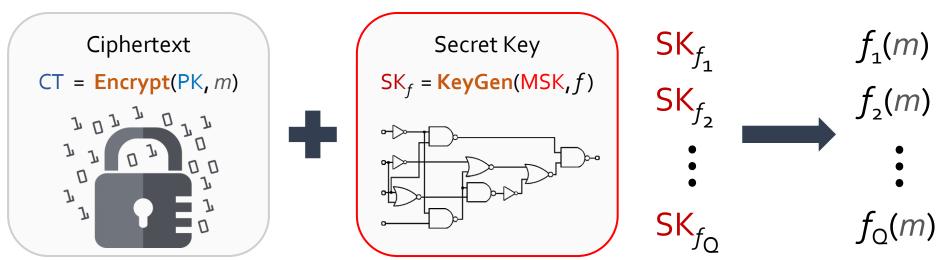
Security:

Any PPT adv. learns only f(m) and nothing else.

 $\mathbf{Decrypt}(\mathsf{CT},\mathsf{SK}_f) = f(m)$

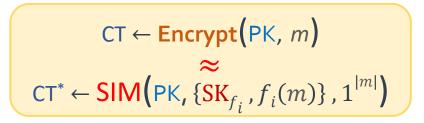
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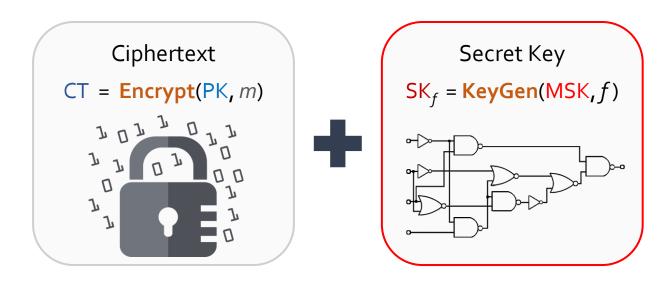




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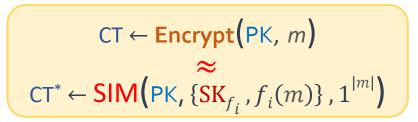


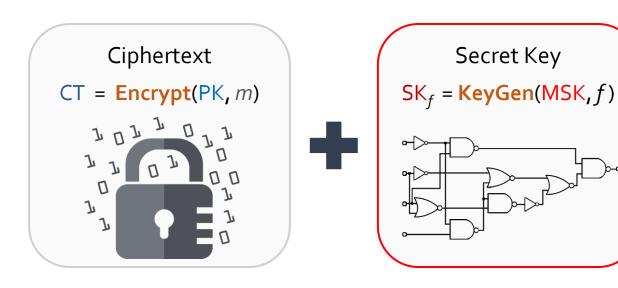


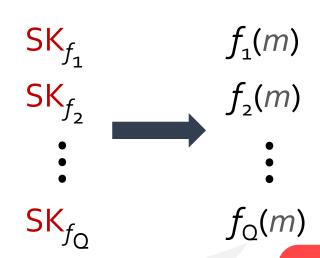
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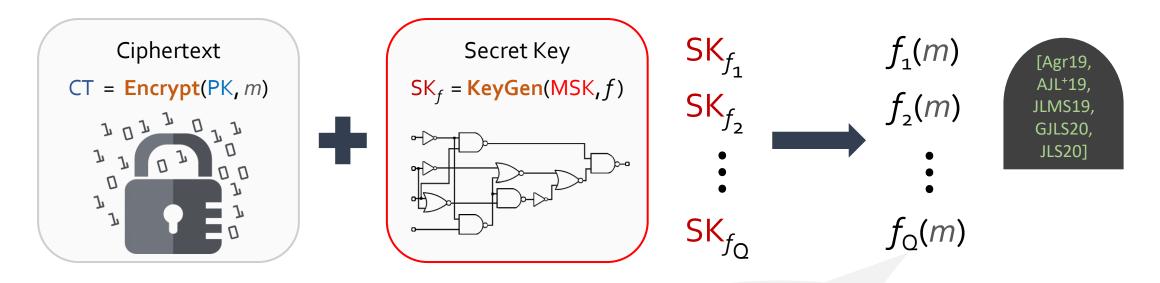






Q = unbounded poly Collusion resistant Full SIM security impossible [BSW11, AGVW13]

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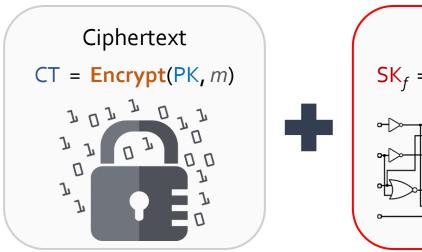


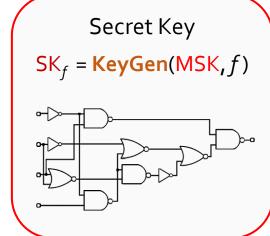
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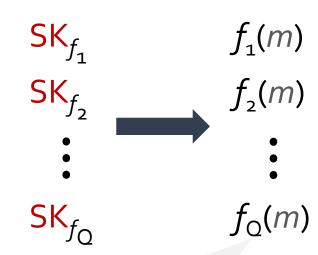
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IND security

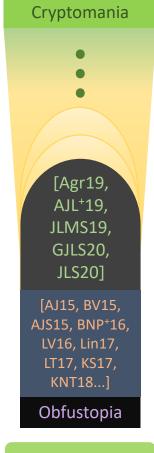
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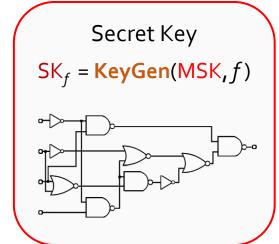


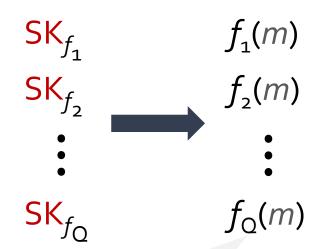
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Q = unbounded poly Collusion resistant Complex constructions
Mixed assumptions

Cryptomania

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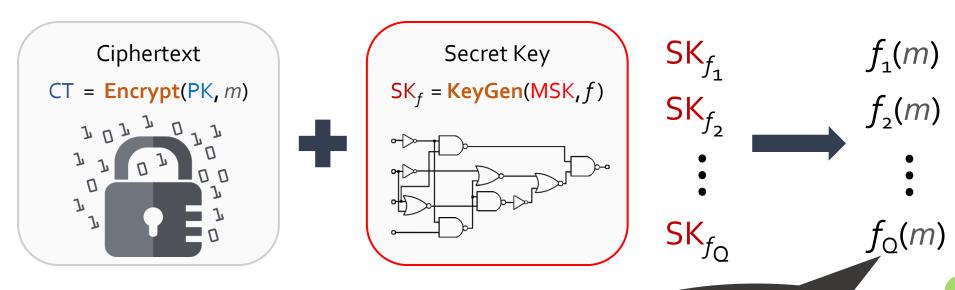
- [Agr19, AJL+19, JLMS19, GJLS20, JLS20]
- [AJ15, BV15, AJS15, BNP+16, LV16, Lin17, LT17, KS17, KNT18...]

Obfustopia

IND security

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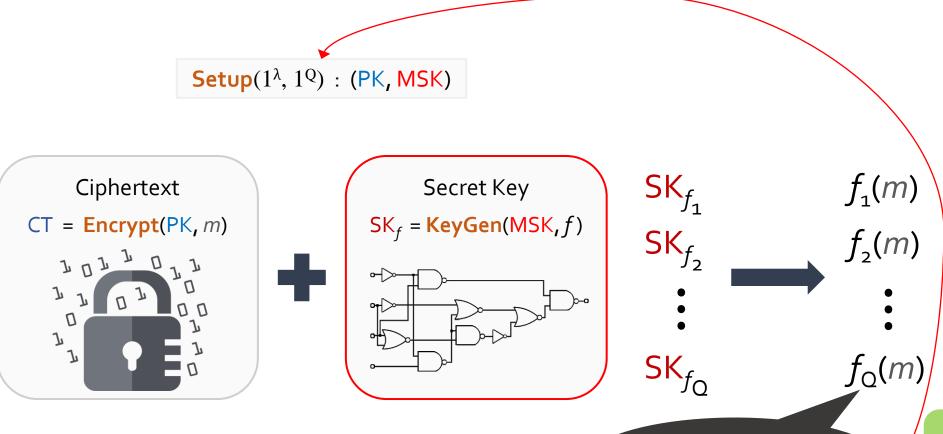


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Bounded collusion

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possible: [GVW12,
AR17, Agr17, AV19]



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Bounded collusion

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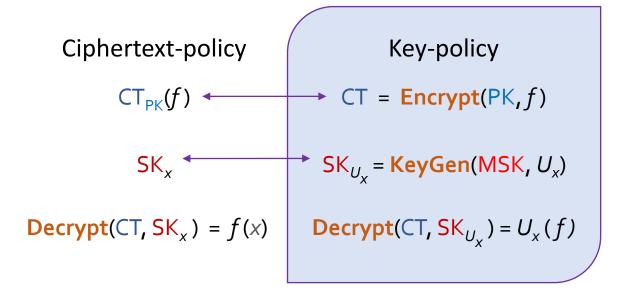
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 - From *IBE*

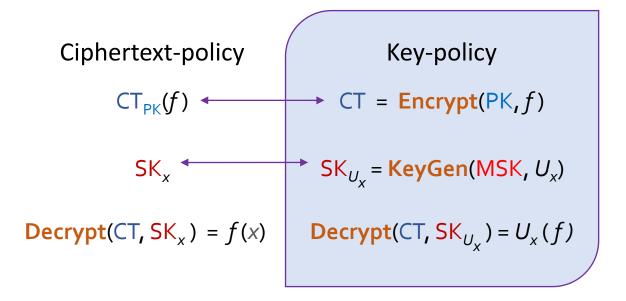


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Limitations of Prior Work

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Unbounded size, bounded depth & output circuits

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Stronger security for [GKP+13b]

Our Results

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Model of computation (*Uniform*: TM,FA)

Circuits have *fixed* input sizes Incurs *worst-case* runtime

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 - *PK*-FE [AS17]: *1-key*, *LWE*
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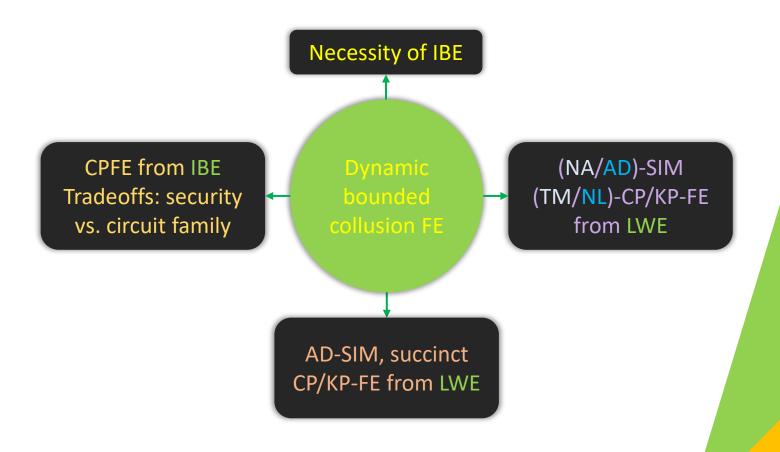
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t is *not* a global bound, can *vary per input*.

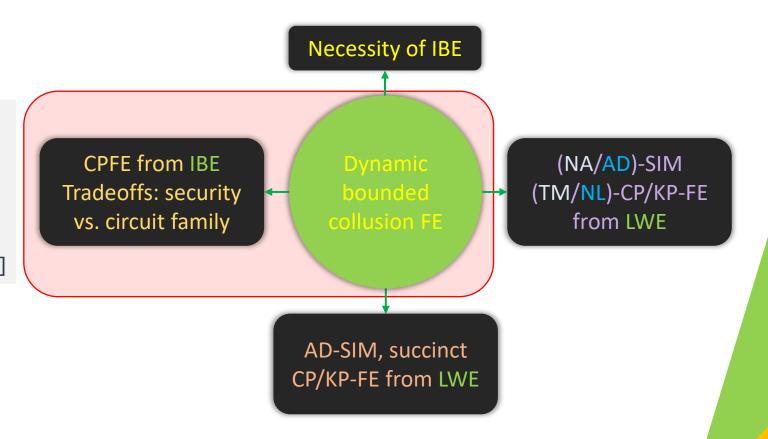
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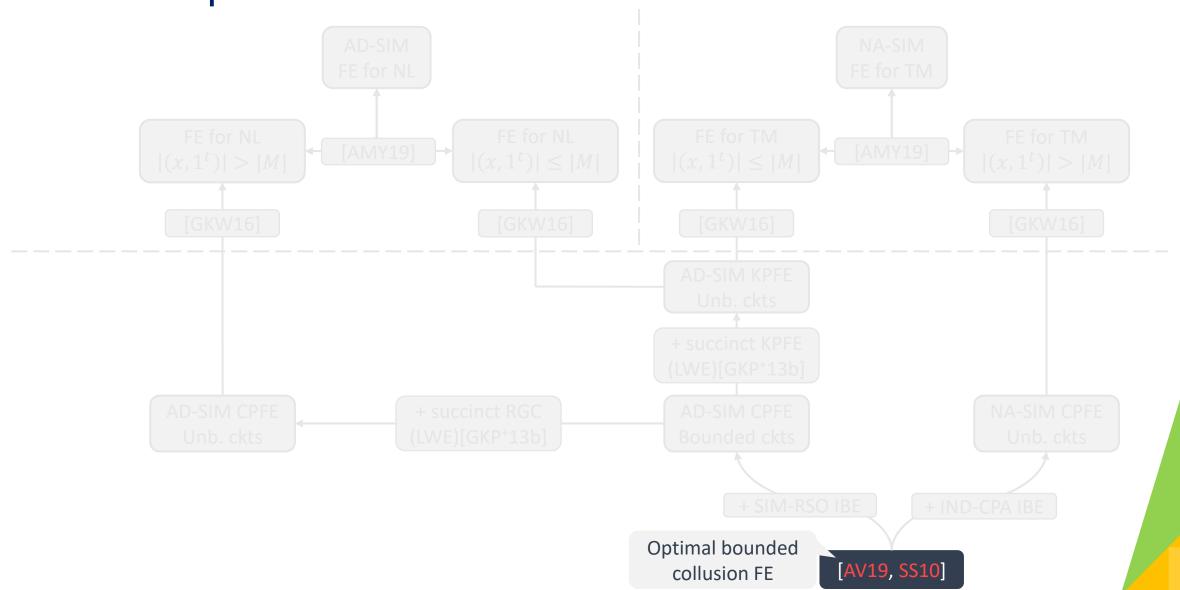
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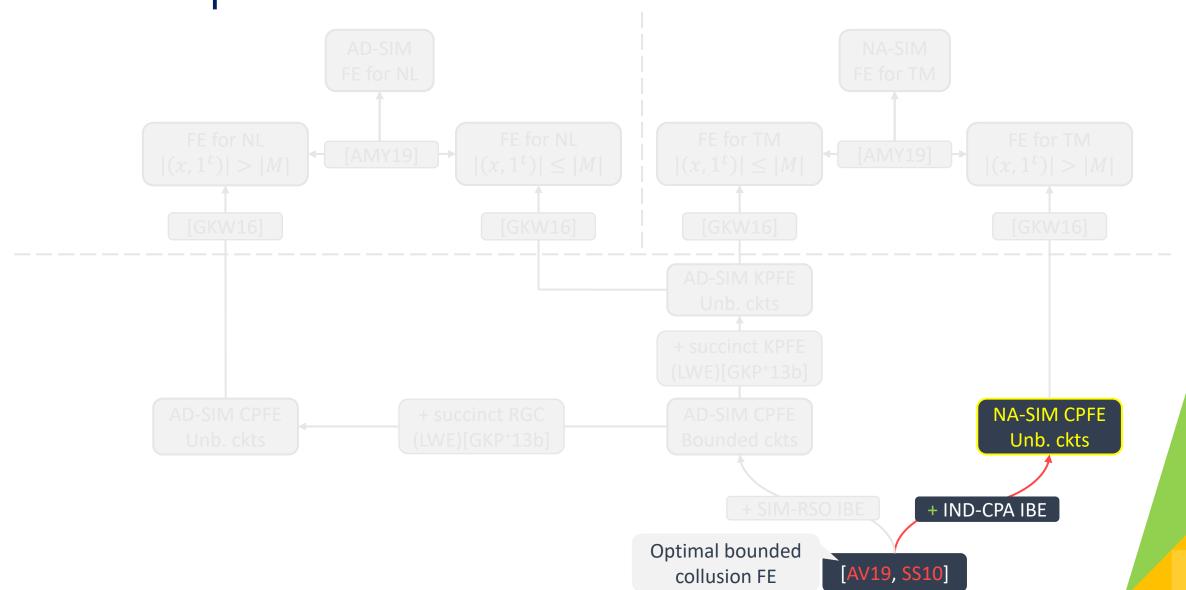
Concurrent Work:

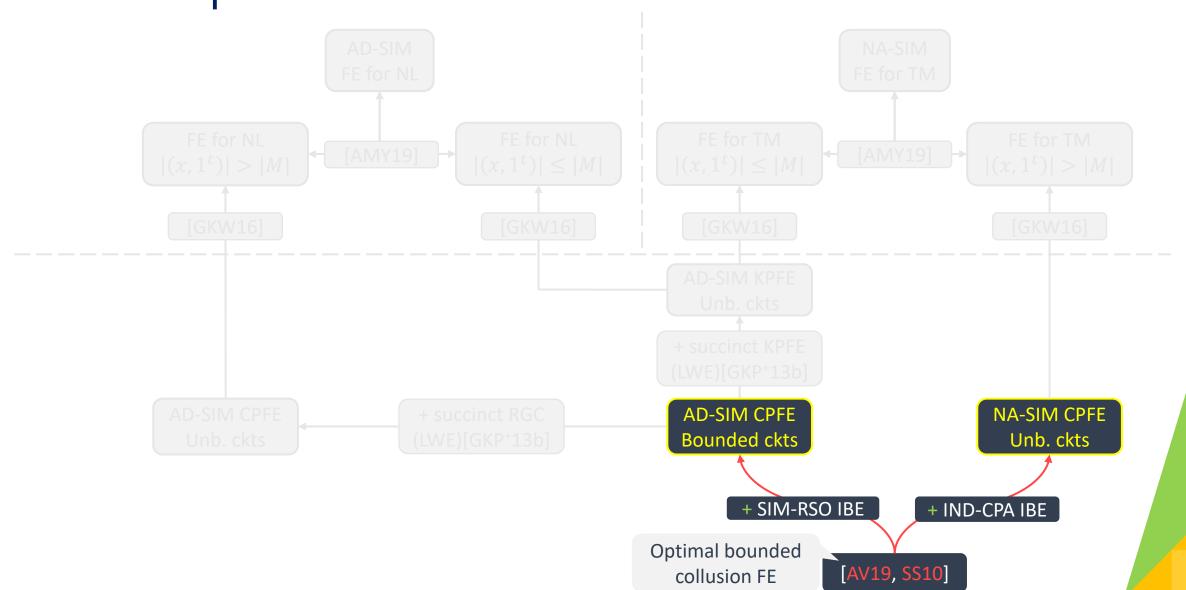
- [GGLW21] also introduced DBC
- *Similar techniques*, but for KPFE:
 - IBE + existing KPFE [GVW12, AV19]



Techniques



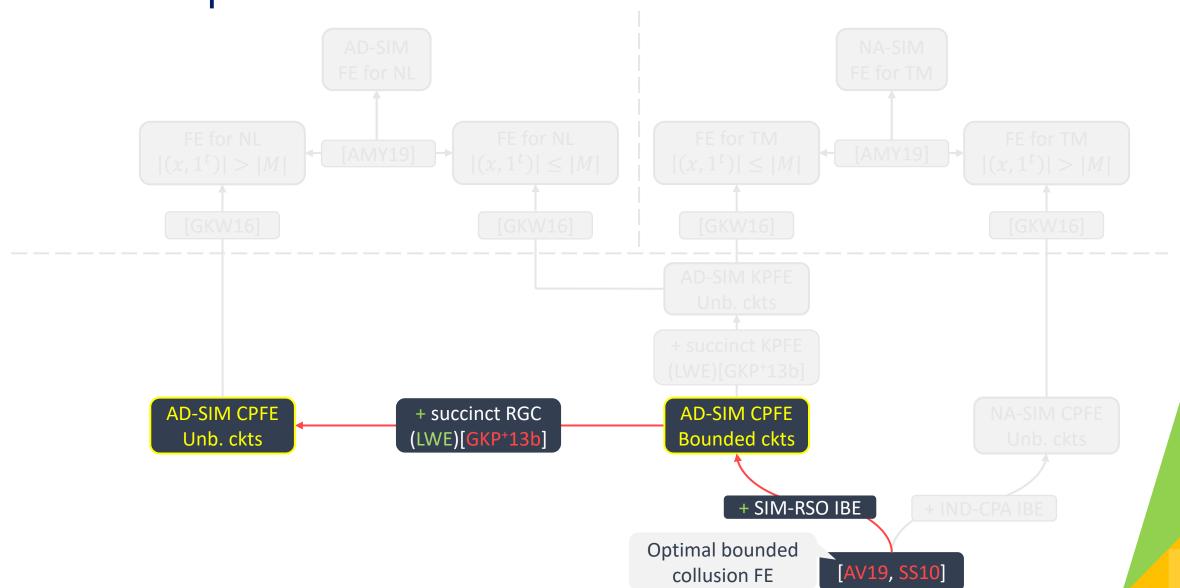


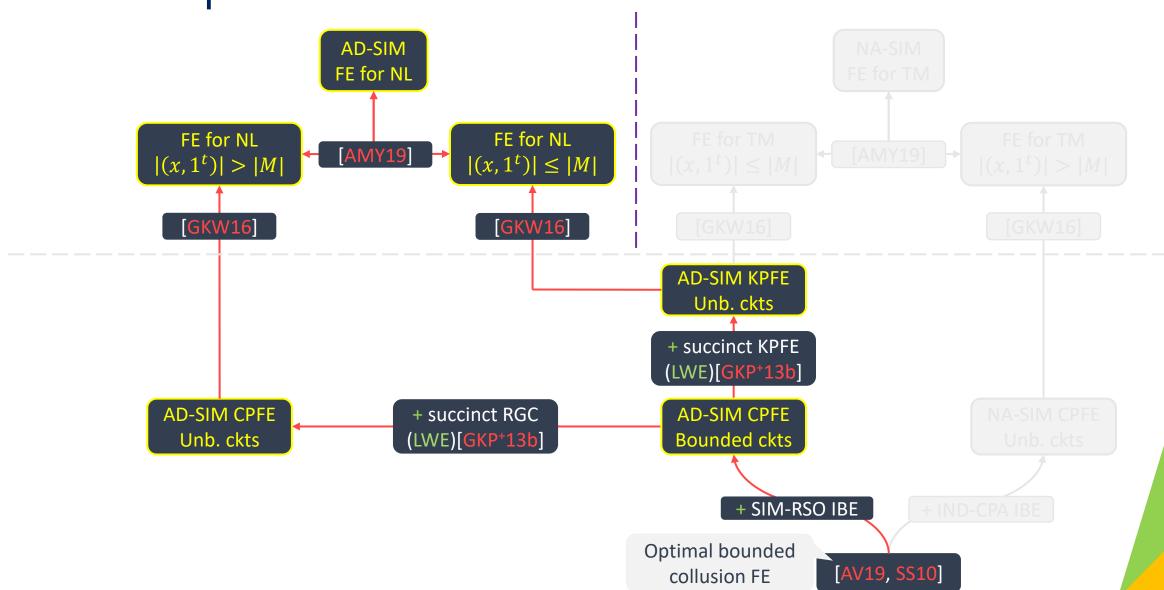


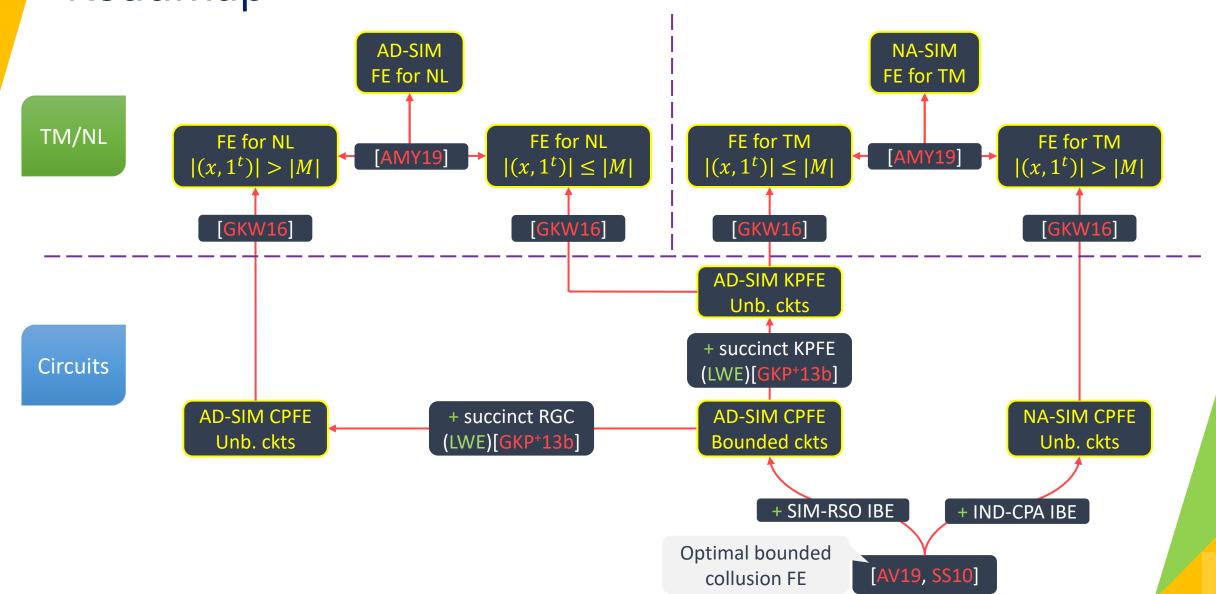


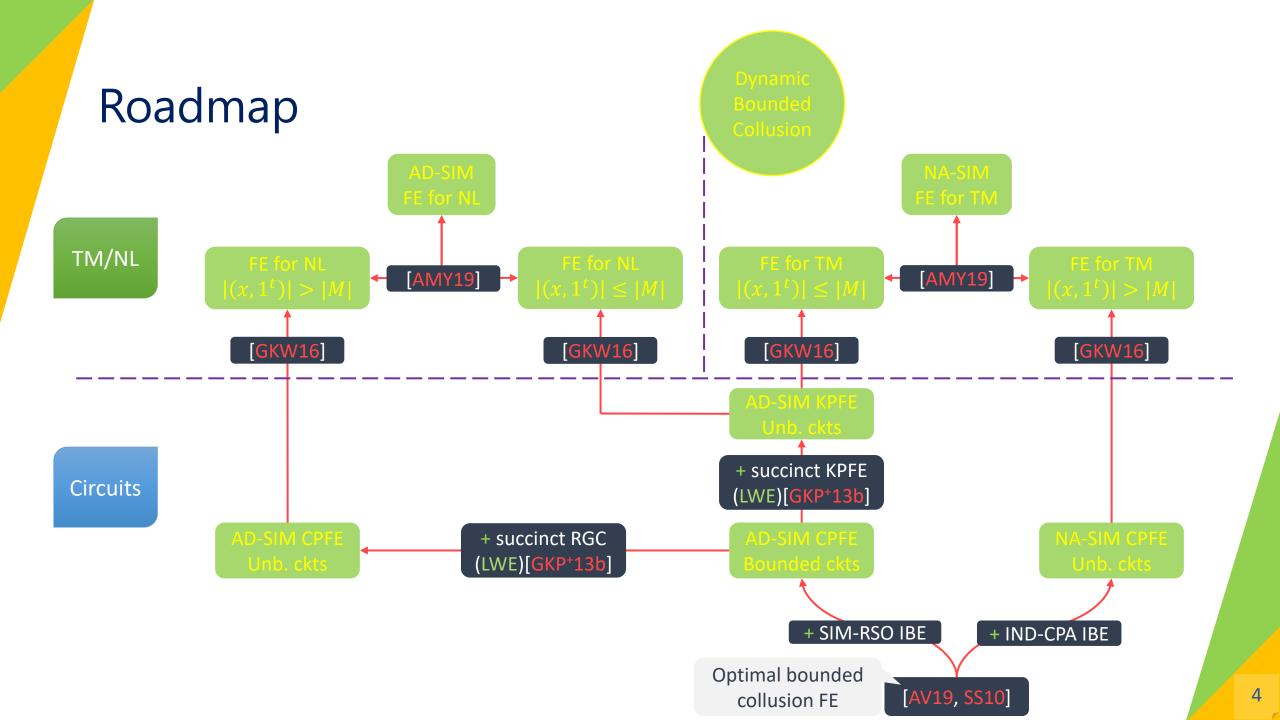




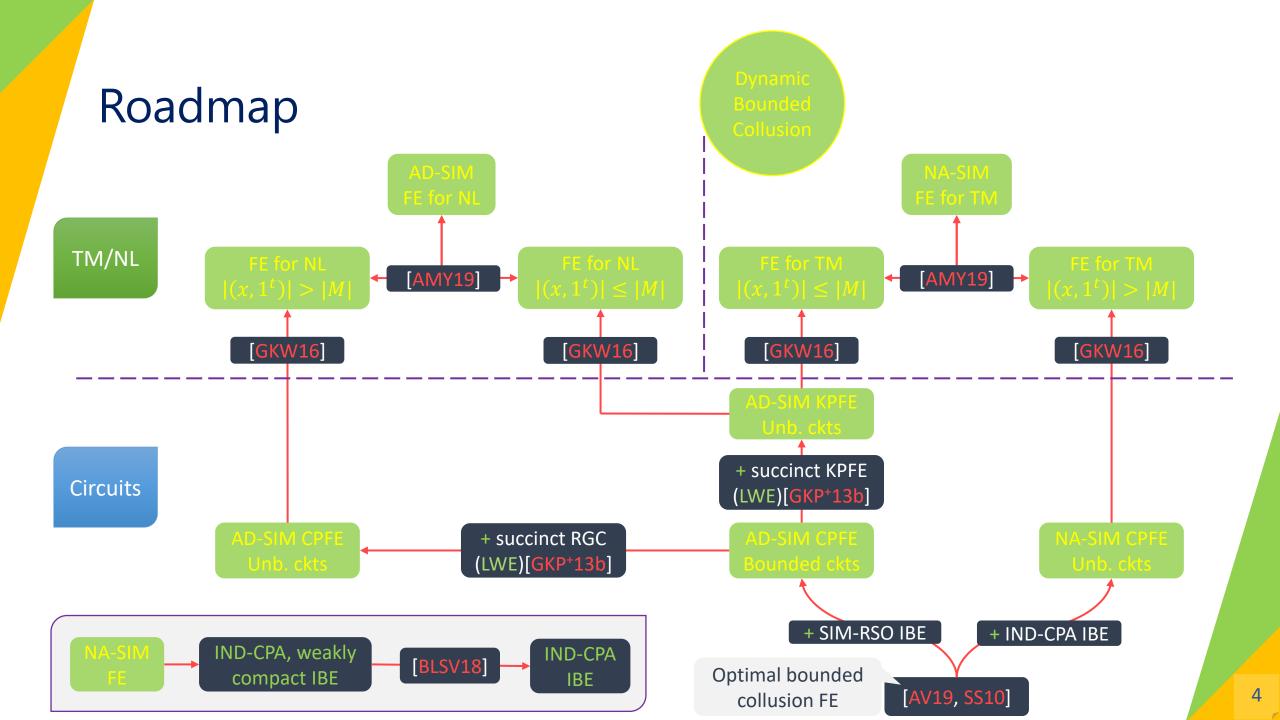


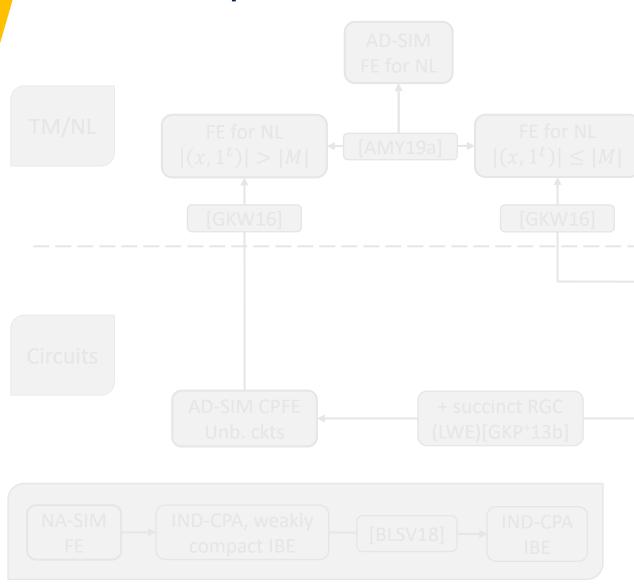


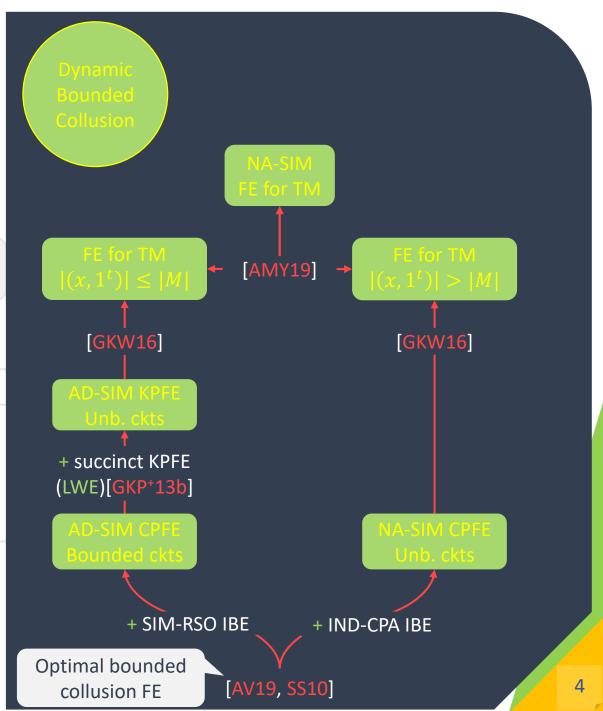






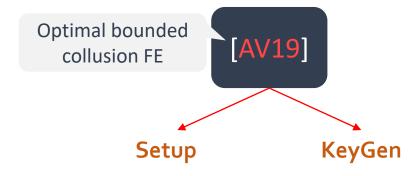




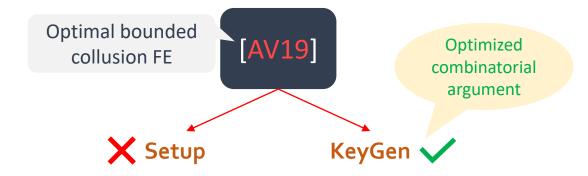


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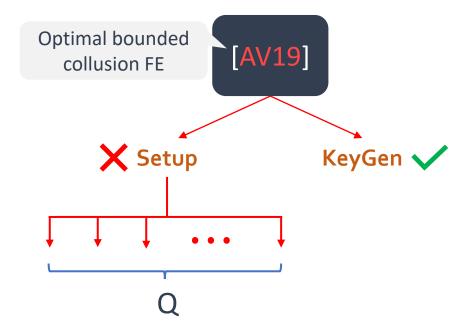
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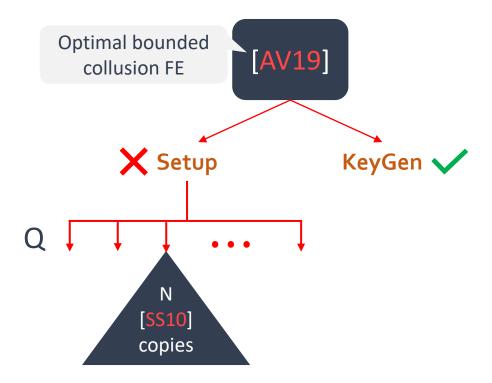
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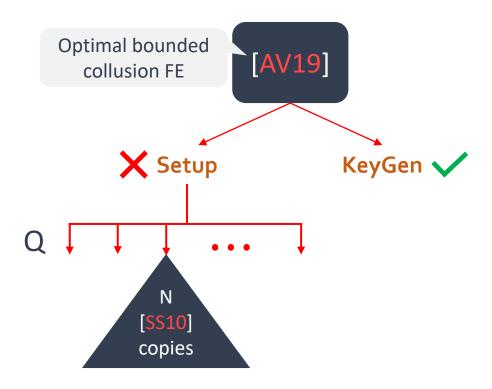


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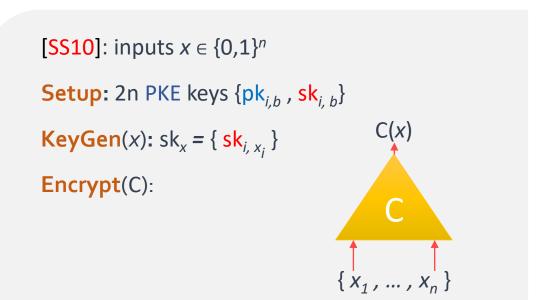
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[SS10]: inputs x \in \{0,1\}^n
```

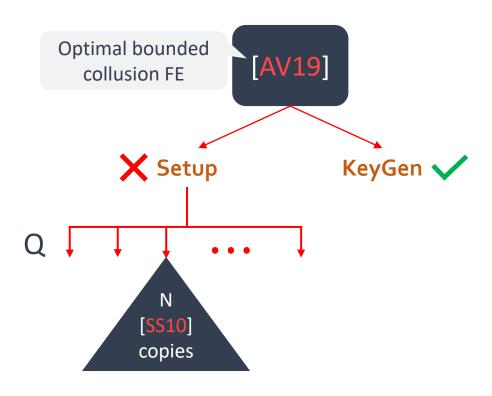
Setup: 2n PKE keys $\{pk_{i,b}, sk_{i,b}\}$

KeyGen(x): $sk_x = \{ sk_{i, x_i} \}$

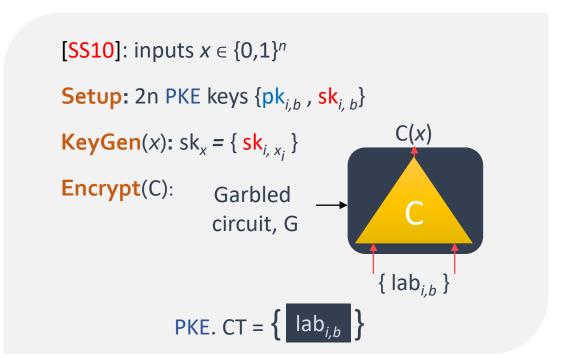


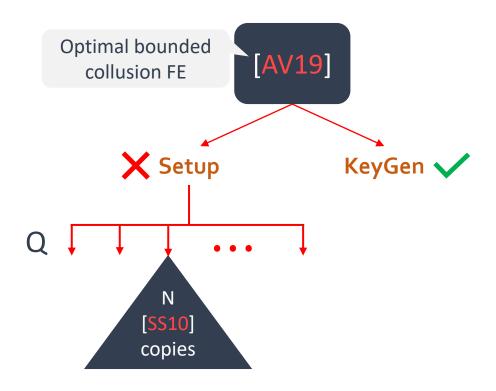
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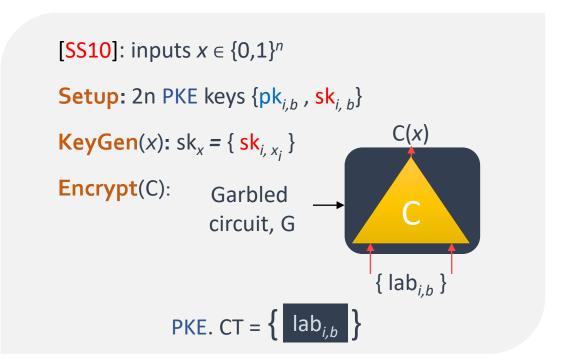


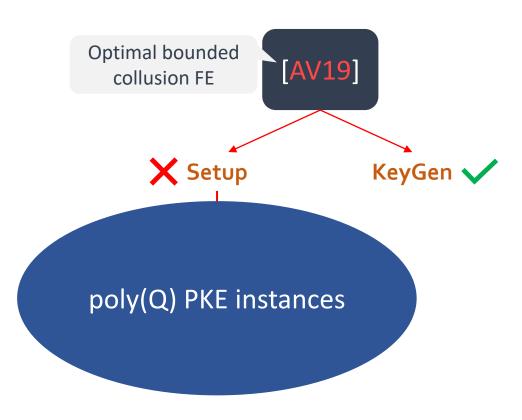
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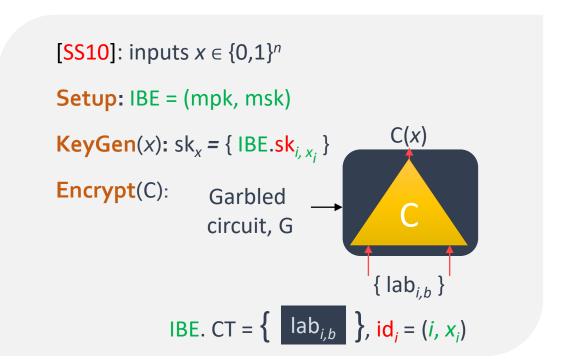


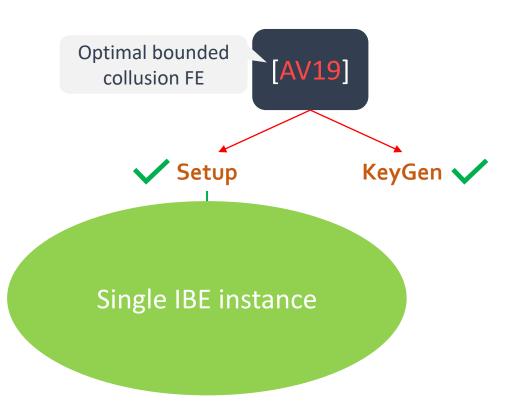
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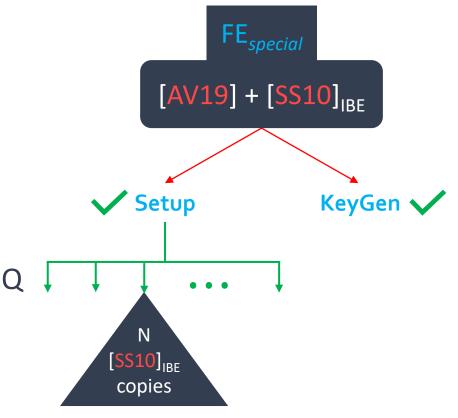


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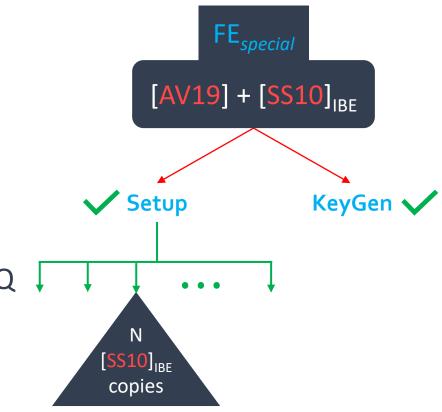




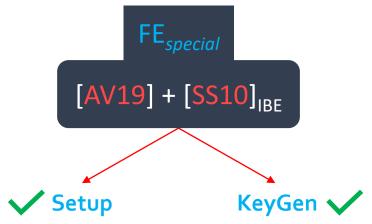
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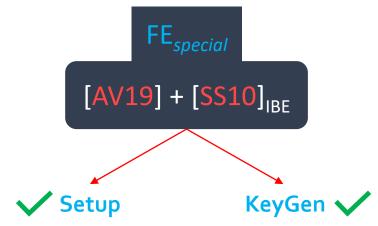
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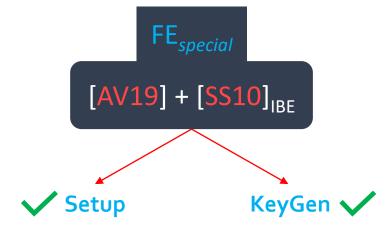
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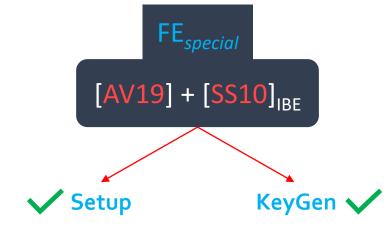
CPFE _{DBC}	Setup	KeyGen
FE _{special}	Setup(2)	KeyGen(2)
	Setup(2 ²)	KeyGen(2 ²)
	•	:
	Setup(2^{λ})	KeyGen(2^{λ})

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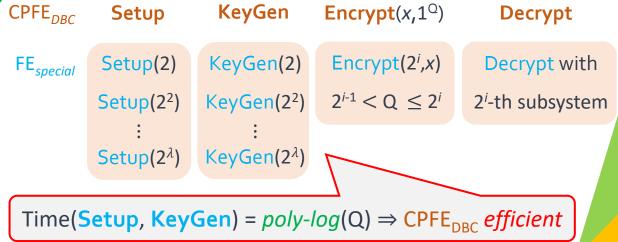


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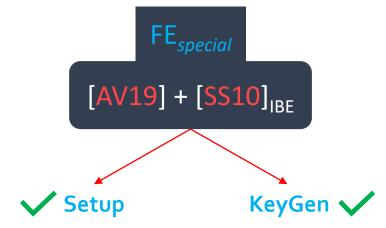
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- Time(Setup, KeyGen) must be independent of Q.
- Weaker need: Time(Setup, KeyGen) = poly-log(Q).
- [AV19]: Time(KeyGen) = poly-log(Q)
- [AV19]: Time(SetUp) = poly-log(Q)



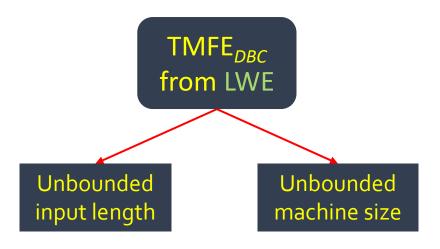
- FE_{special} (Setup, KeyGen) still need to get rid of Q...
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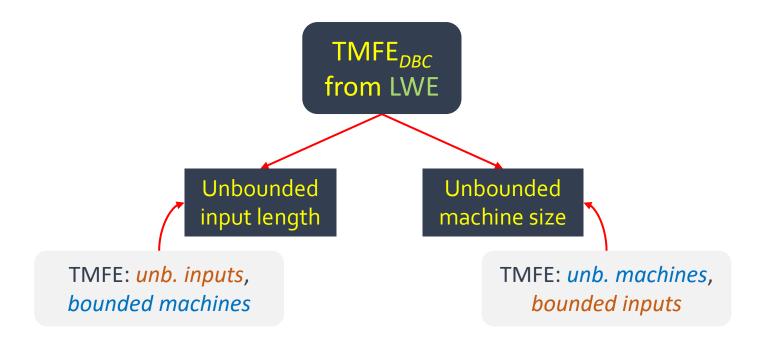
IND-CPA IBE \Rightarrow NA-SIM, unbounded circuits

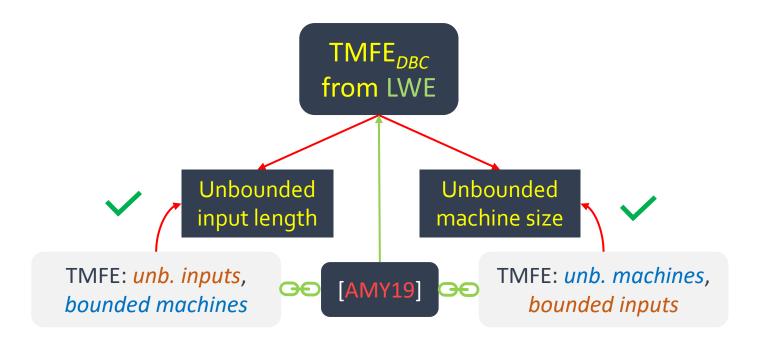
SIM-RSO IBE \Rightarrow AD-SIM, bounded circuits

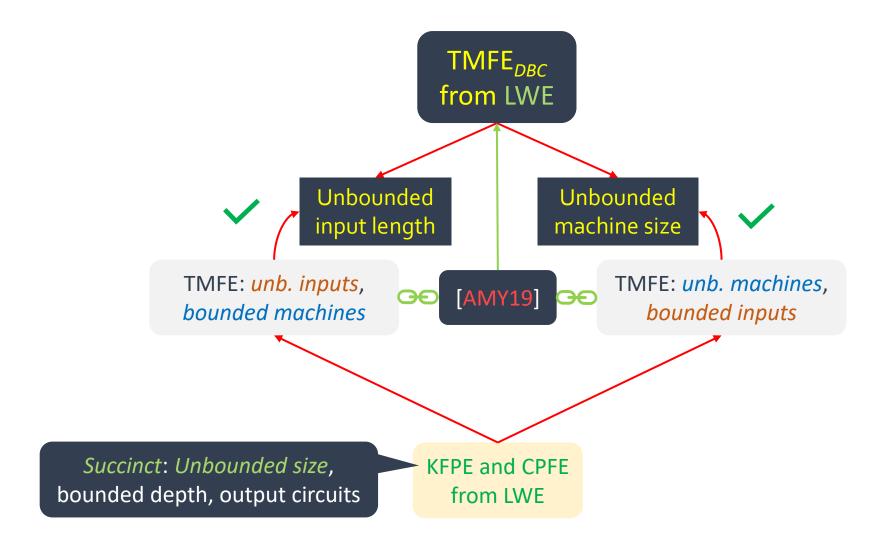
NA-SIM FE \Rightarrow IND-CPA IBE

```
Setup
                              KeyGen
                                              Encrypt(x,1^{\circ})
                                                                        Decrypt
             Setup(2)
                             KeyGen(2)
                                               Encrypt(2^i,x)
                                                                      Decrypt with
FE<sub>special</sub>
                                               2^{i-1} < Q \le 2^i
                                                                    2<sup>i</sup>-th subsystem
            Setup(2<sup>2</sup>)
                            KeyGen(2<sup>2</sup>)
            Setup(2^{\lambda})
                            KeyGen(2^{\lambda})
 Time(Setup, KeyGen) = poly-log(Q) \Rightarrow CPFE_{DBC} efficient
```









DBC CPFE

IND-CPA IBE \Rightarrow NA-SIM, unbounded circuits

SIM-RSO IBE \Rightarrow AD-SIM, bounded circuits

NA-SIM FE \Rightarrow IND-CPA IBE

[GKP+13b]

NA-SIM, 1-key KPFE
succinct, from LWE

DBC CPFE

IND-CPA IBE \Rightarrow NA-SIM, unbounded circuits

SIM-RSO IBE ⇒ AD-SIM, *bounded* circuits

NA-SIM FE ⇒ IND-CPA IBE

DBC, AD-SIM CPFE bounded circuits

[GKP+13b]

NA-SIM, 1-key KPFE
succinct, from LWE

Setup(1^{λ}): CPFE_{DBC}.(PK, MSK)

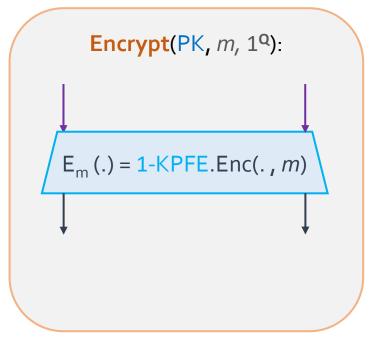
DBC, AD-SIM CPFE bounded circuits

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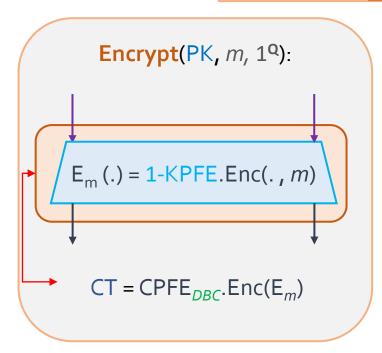


DBC, AD-SIM CPFE bounded circuits [GKP+13b] *NA-SIM, 1-key* KPFE succinct, from LWE AD-SIM, DBC KPFE succinct, from LWE (Improves [GKP+13b])

Setup (1^{λ}) : CPFE_{DBC}.(PK, MSK) **Encrypt**(PK, *m*, 1^Q): $E_m(.) = 1-KPFE.Enc(., m)$ $CT = CPFE_{DBC}.Enc(E_m)$

DBC, AD-SIM CPFE bounded circuits [GKP+13b] *NA-SIM, 1-key* KPFE succinct, from LWE AD-SIM, DBC KPFE succinct, from LWE (Improves [GKP+13b])

Setup(1^{λ}): CPFE_{DBC}·(PK, MSK)



KeyGen(MSK, f):

- 1. Sample 1-KPFE(PK, MSK)
- 2. Get 1-KPFE.sk $_f$ with MSK

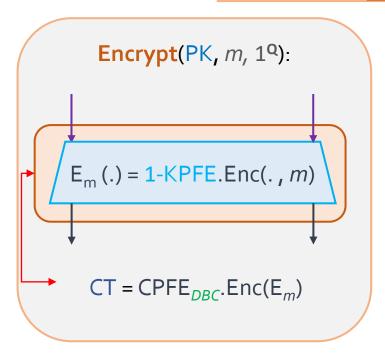
 $SK_r = (sk_{pk}, 1-KPFE.sk_r)$

DBC, AD-SIM CPFE bounded circuits

[GKP+13b]

NA-SIM, 1-key KPFE
succinct, from LWE

Setup(1^{λ}): CPFE_{DBC}·(PK, MSK)



KeyGen(MSK, f):

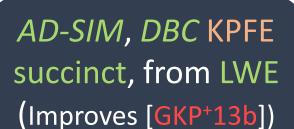
- 1. Sample 1-KPFE(PK, MSK)
- 2. Get 1-KPFE.sk $_f$ with MSK
- 3. $sk_{PK} \leftarrow CPFE_{DBC}$ KeyGen(MSK, PK)

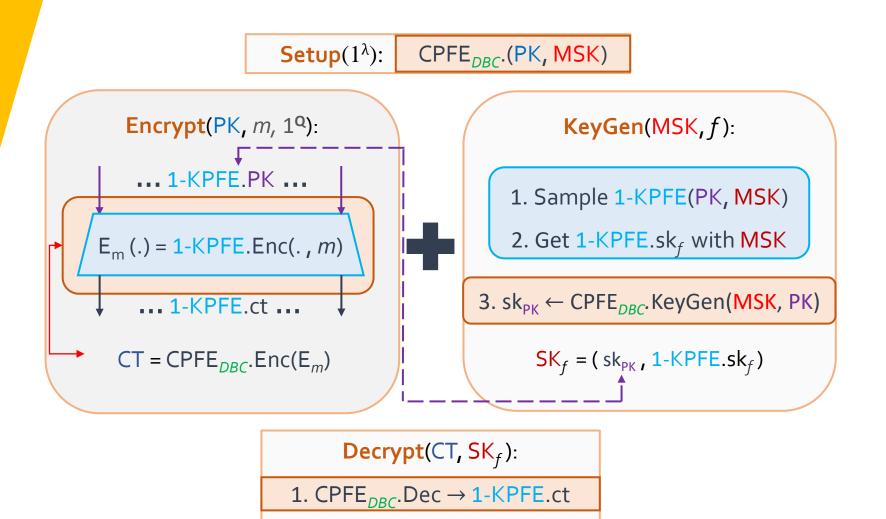
$$SK_f = (sk_{PK}, 1-KPFE.sk_f)$$

DBC, AD-SIM CPFE bounded circuits

[GKP+13b]

NA-SIM, 1-key KPFE
succinct, from LWE

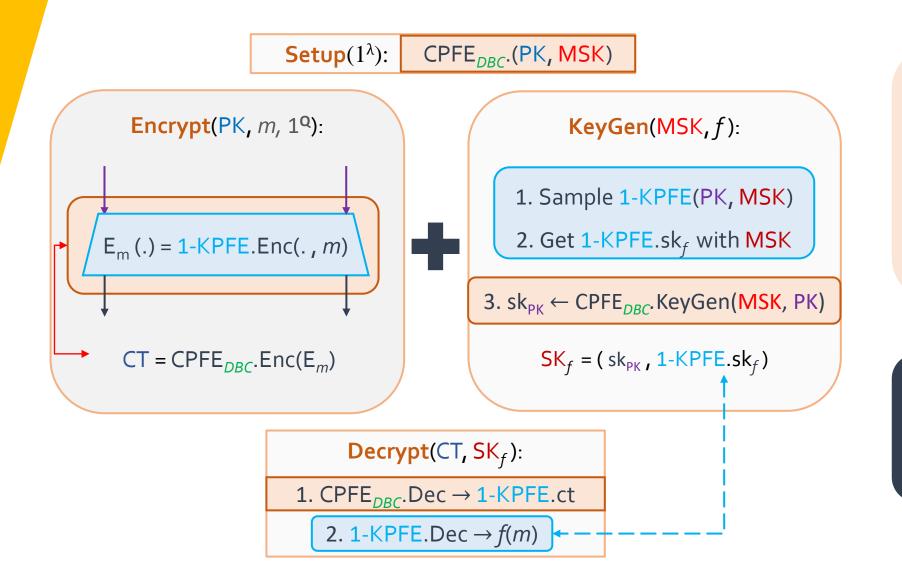




DBC, AD-SIM CPFE bounded circuits

[GKP+13b]

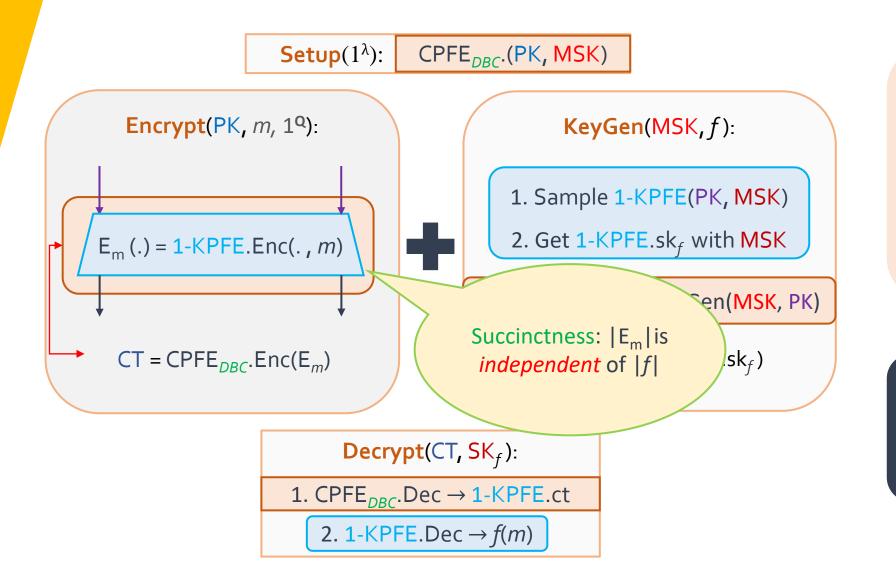
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DBC, AD-SIM CPFE bounded circuits

[GKP+13b]

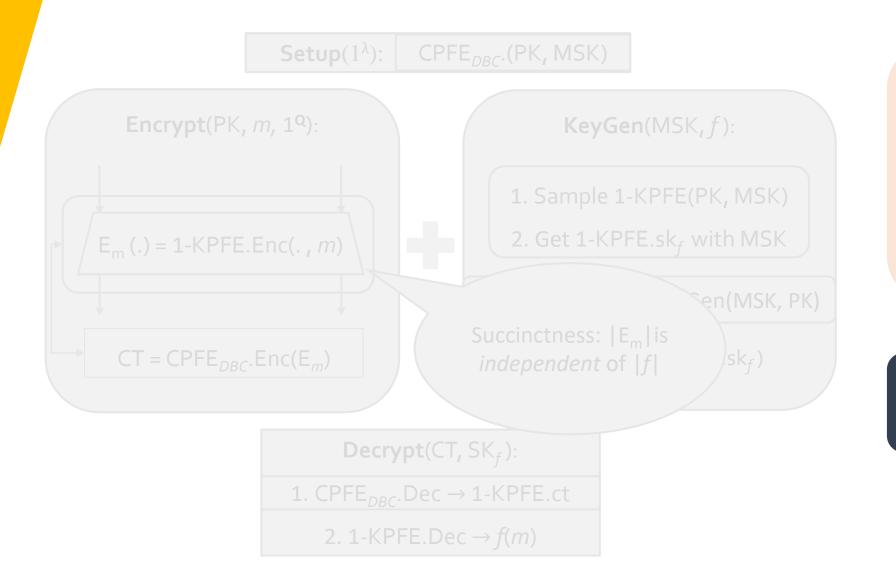
NA-SIM, 1-key KPFE
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DBC, AD-SIM CPFE bounded circuits

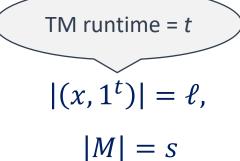
[GKP+13b]

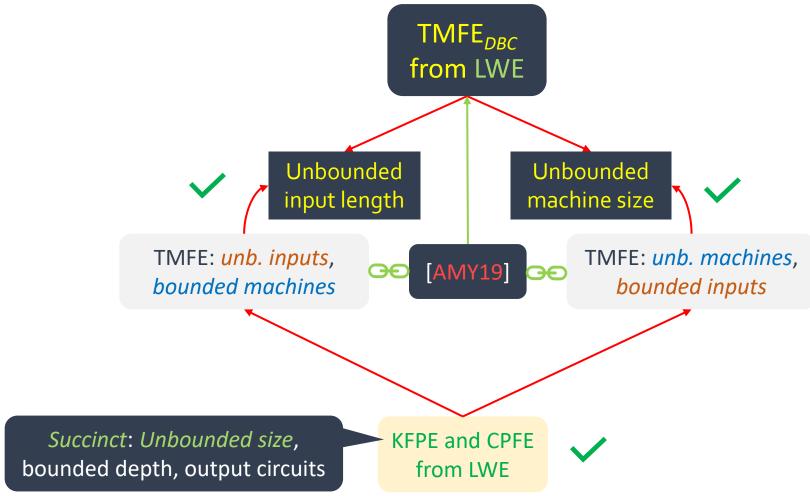
NA-SIM, 1-key KPFE succinct, from LWE

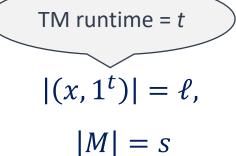


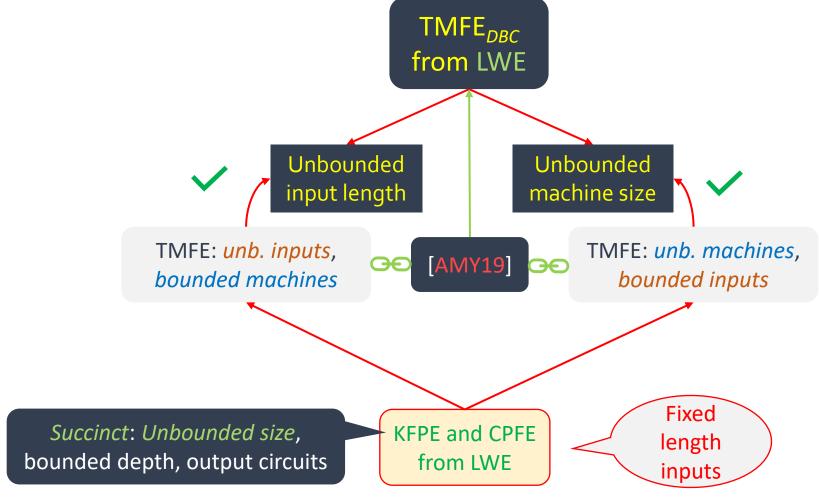
DBC, AD-SIM CPFE bounded circuits [GKP+13b] Succinct RGC from LWE AD-SIM, DBC CPFE

succinct, from LWE









TM runtime = t $|(x, 1^t)| = \ell,$ |M| = s $\ell \le s$

TMFE_{DBC} from LWE

 $\ell > s$

CPFE

... ... CPFE,

KPFE

... ... KPFE,

TMFE_{DBC}

from LWE

TM runtime = t $|(x,1^t)|=\ell,$ |M| = s $\ell \leq s$

 $\ell > s$

CPFE

 $CPFE_i$

Circuit $U_{i,x,t}$ (M): Run M(x) for t steps

 $\operatorname{ct}_{i} = \operatorname{Enc}_{i}(U_{i,x,t}, \mathbf{1}^{Q}), \forall i \in [\ell]$

KPFE

KPFE,

TMFE_{DBC} from LWE

TM runtime = t $|(x, 1^t)| = \ell,$ |M| = s

 $\ell \leq s$

KPFE

... ... KPFE,

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KeyGen $_s$ (M)

TMFE_{DBC} from LWE

TM runtime = t $|(x, 1^t)| = \ell,$

|M| = s

 $\ell \leq s$

KPFE

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 $\ell > s$

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 $KeyGen_s(M)$

 $M(x) = Dec(sk_s, ct_s)$

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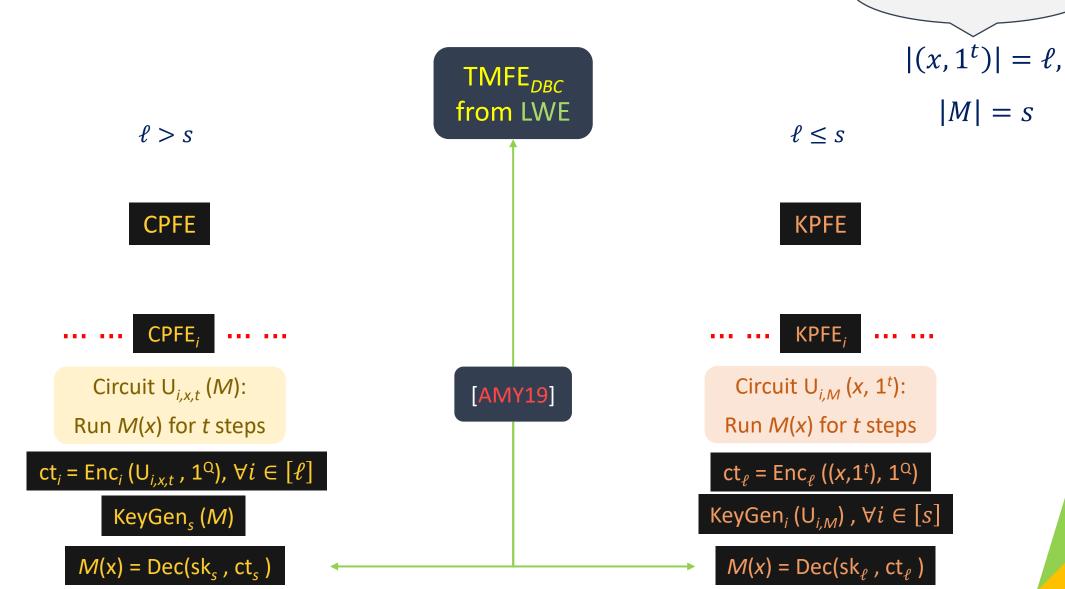
... ... KPFE,

Circuit $U_{i,M}(x, 1^t)$: Run M(x) for t steps

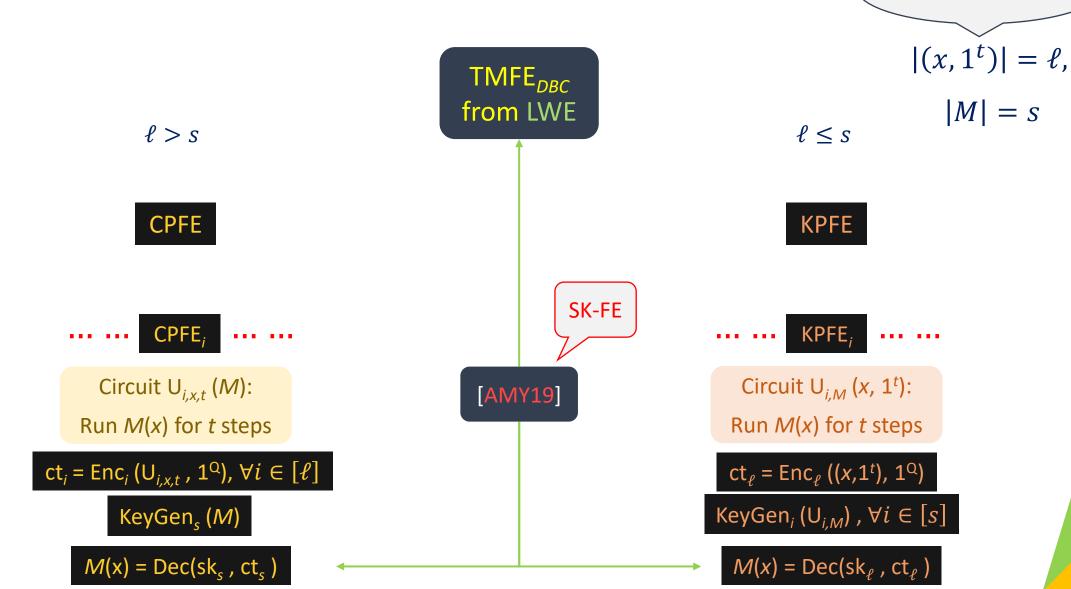
 $\mathsf{ct}_\ell = \mathsf{Enc}_\ell \; ((x, 1^t), \; 1^\mathsf{Q})$

 $KeyGen_i (U_{i,M}), \forall i \in [s]$

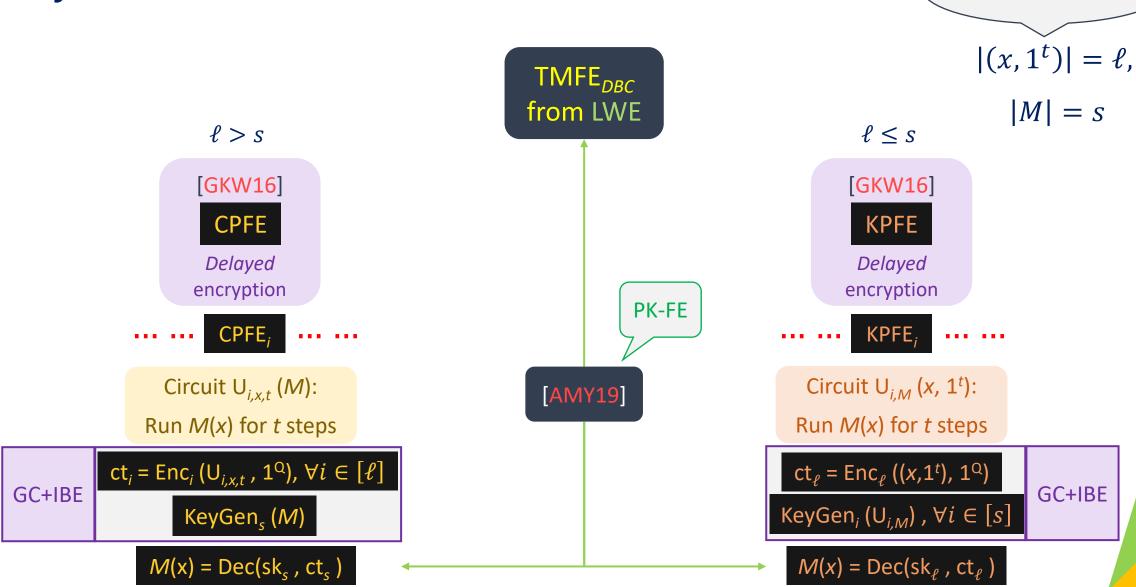
 $M(x) = Dec(sk_{\ell}, ct_{\ell})$



TM runtime = t



TM runtime = t



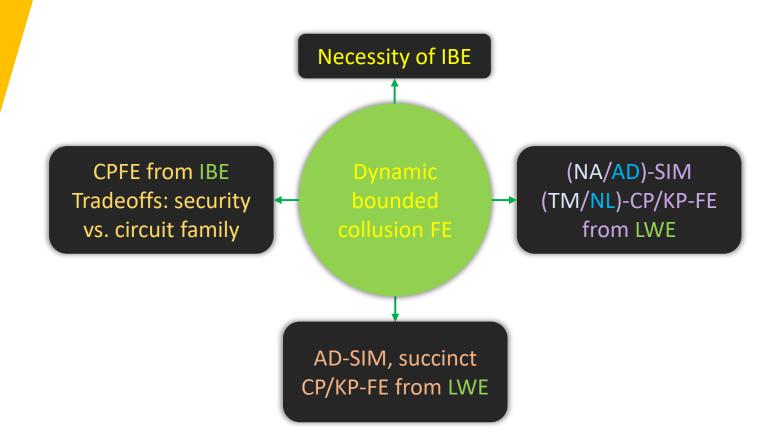
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TM runtime = t

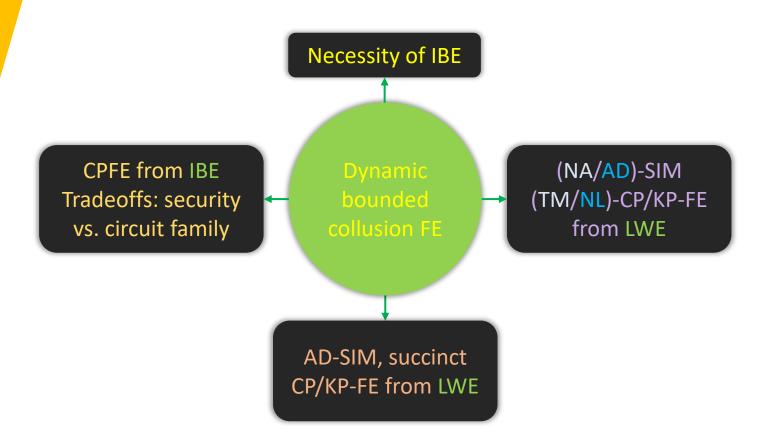
|M| = s

GC+IBE

Summary and Open Problems



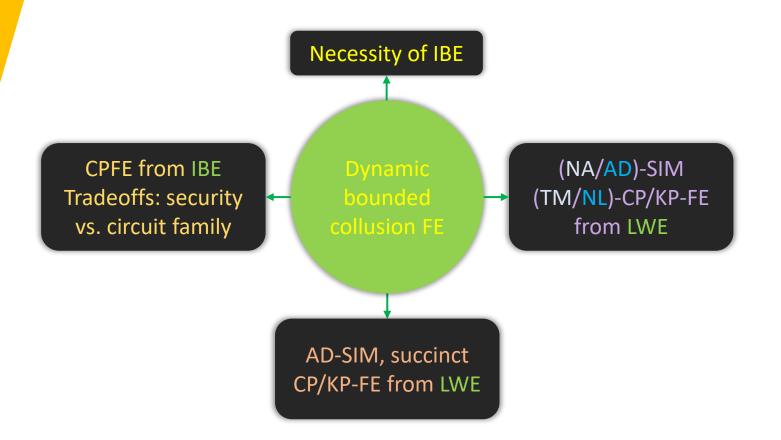
Summary and Open Problems



Open problems:

- DBC TM-FE with AD-SIM security
- Remove runtime dependence on |CT|
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