

Pushing the Limits of Valiant's **Universal Circuits**: **Simpler, Tighter and More Compact**

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Universal Circuits -- General-purpose Circuits

- **Universal Circuit** UC_n

for any $|C| \leq n$, $\exists p_C \in \{0,1\}^m$, s.t.,

$$\forall x: UC_n(x, p_C) = C(x).$$

- **Theorem** [Valiant76]: There exists explicit universal circuit **UC** that simulates any **C** with

- size up to n
- fan-in and fan-out 2

where UC is represented by an edge-universal-graph (**EUG**) on graph and

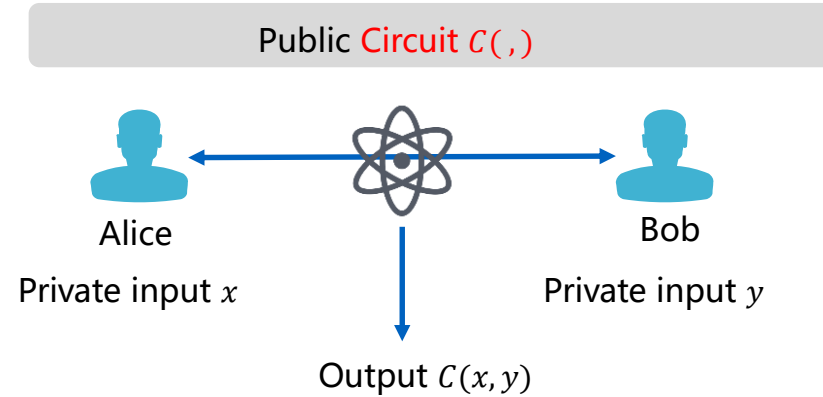
$$\text{Size}(\mathbf{EUG}) \sim 4.75n \log n, \quad \text{Size}(\mathbf{UC}) \sim 19n \log n.$$

- **State-of-the-art:** $3.64n \log n \leq \text{Size}(\mathbf{EUG}) \leq 4.5n \log n$ in Valiant' framework
our work: $2.95n \log n \leq \text{Size}(\mathbf{EUG}) \leq 3n \log n$

Application in Multiparty Computation

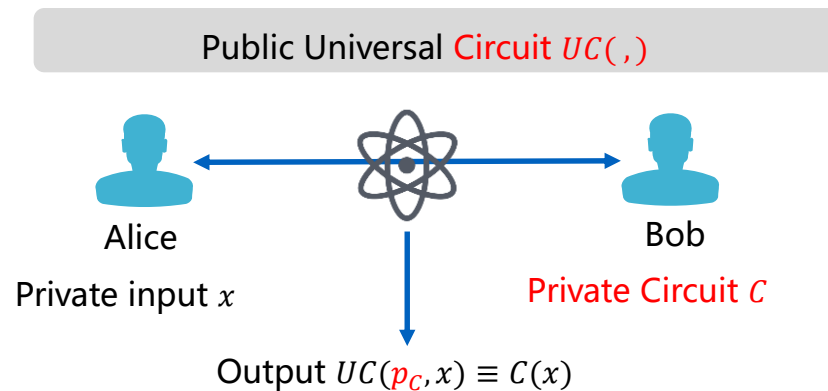


- Multiparty Computation preserves **input privacy**



- **Q: Function privacy?** e.g., Bob holds **private circuit** C

A: Universal Circuits



Edge Embedding and Edge Universal Graphs

Edge-Embedding:

injective map $\rho: G = (V, E) \rightarrow G^* = (V^*, E^*)$,

s.t.,

- $V \rightarrow V^*$
- $(v_i, v_j) \in E \rightarrow \text{path}(\rho(v_i), \dots, \rho(v_j)) \in E^*$

Edge-Universal Graphs (EUG):

- G^* is $EUG_d(n)$
 \Leftrightarrow for any $G \in DAG_d(n)$, $\exists \rho: G \rightarrow G^*$

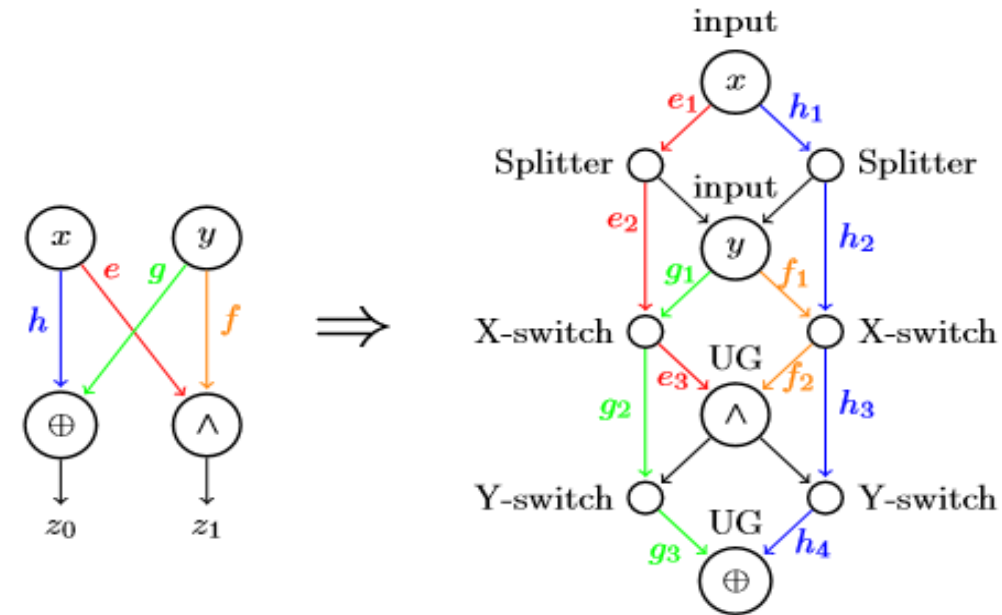
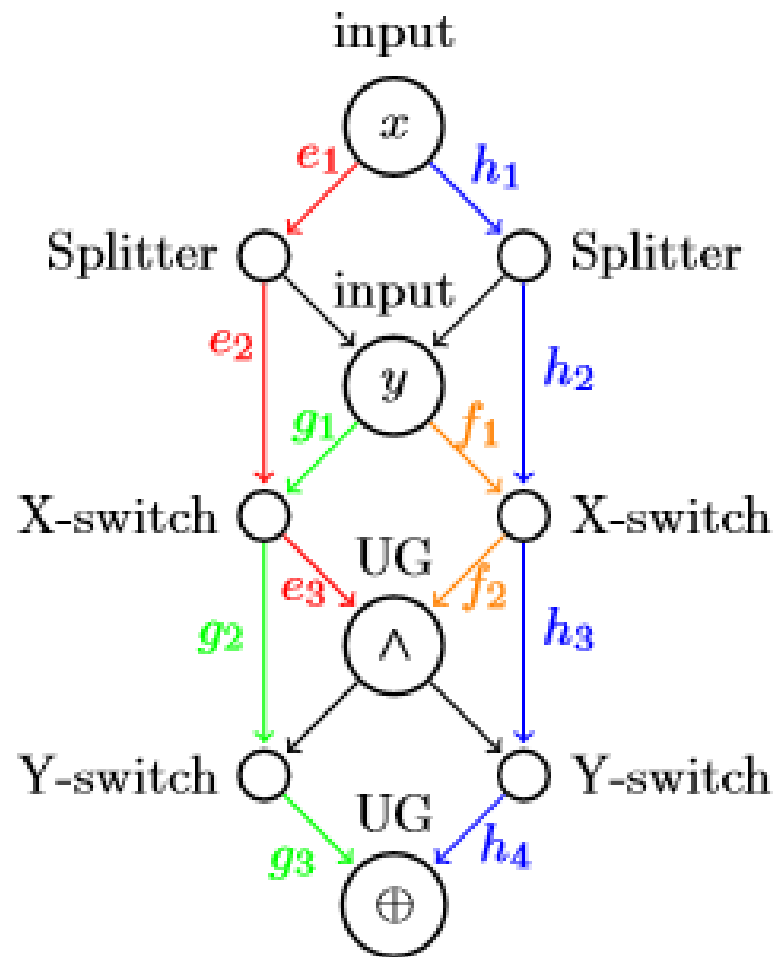
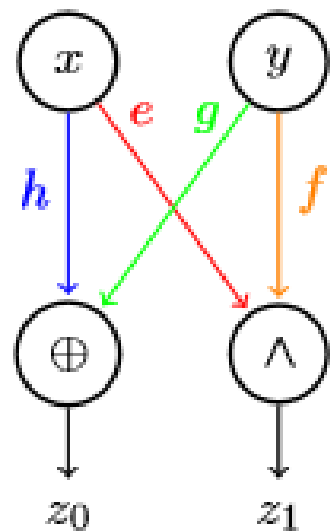
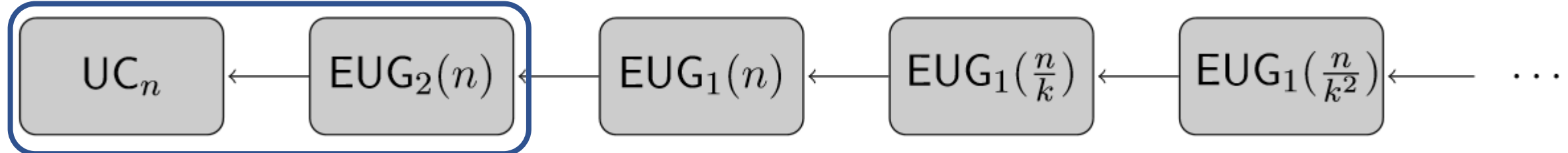
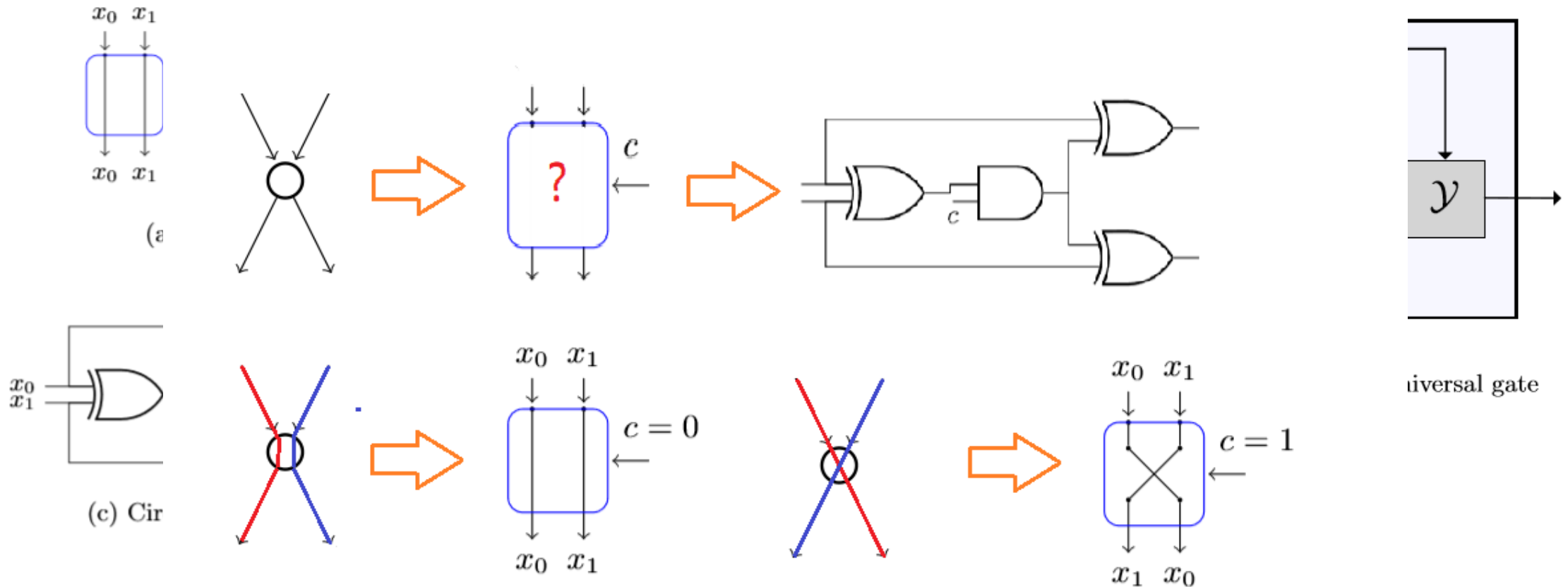


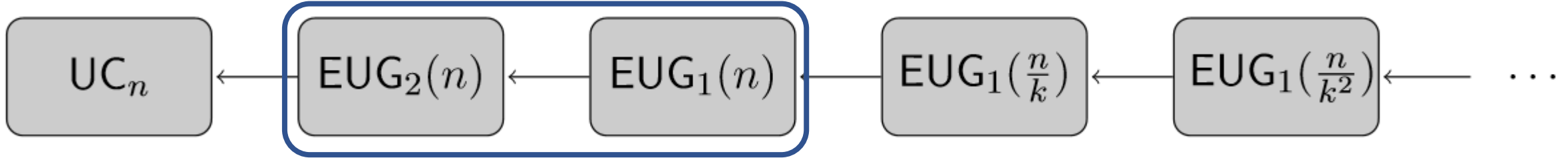
Fig. 2. An example of edge-embedding.



Switching Gates and Universal Gates

- X-switching gate = 4 basic gates (AND and XOR)
- Y-switching gate = 3 basic gates
- Universal gate = 3 Y-switching gates

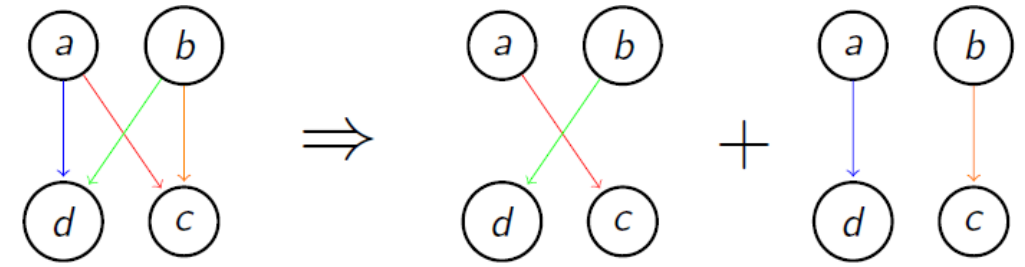


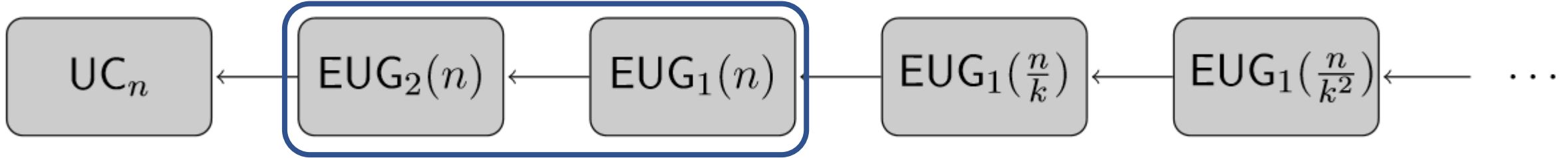


[Valiant76] For any graph $G = (V, E) \in DAG_d(n)$, edge set E can be divided into d disjoint sets E_1, E_2, \dots, E_d such that

$$E = \bigcup_{i=1}^d E_i$$

and each $(V, E_i) \in DAG_1(n)$ for $1 \leq i \leq d$.

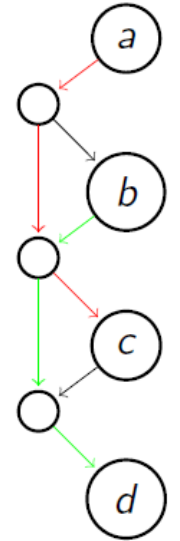
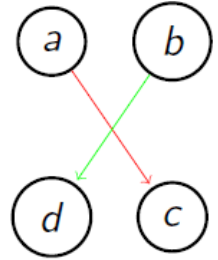


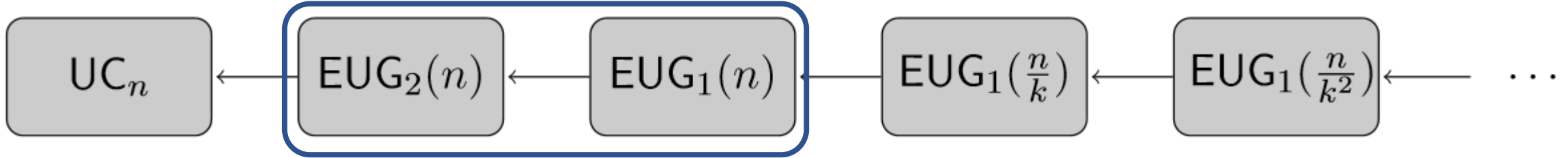


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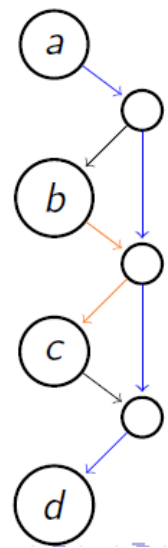
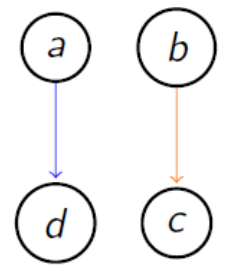


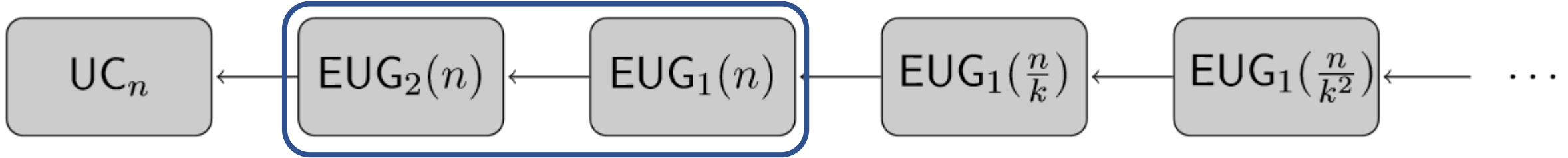


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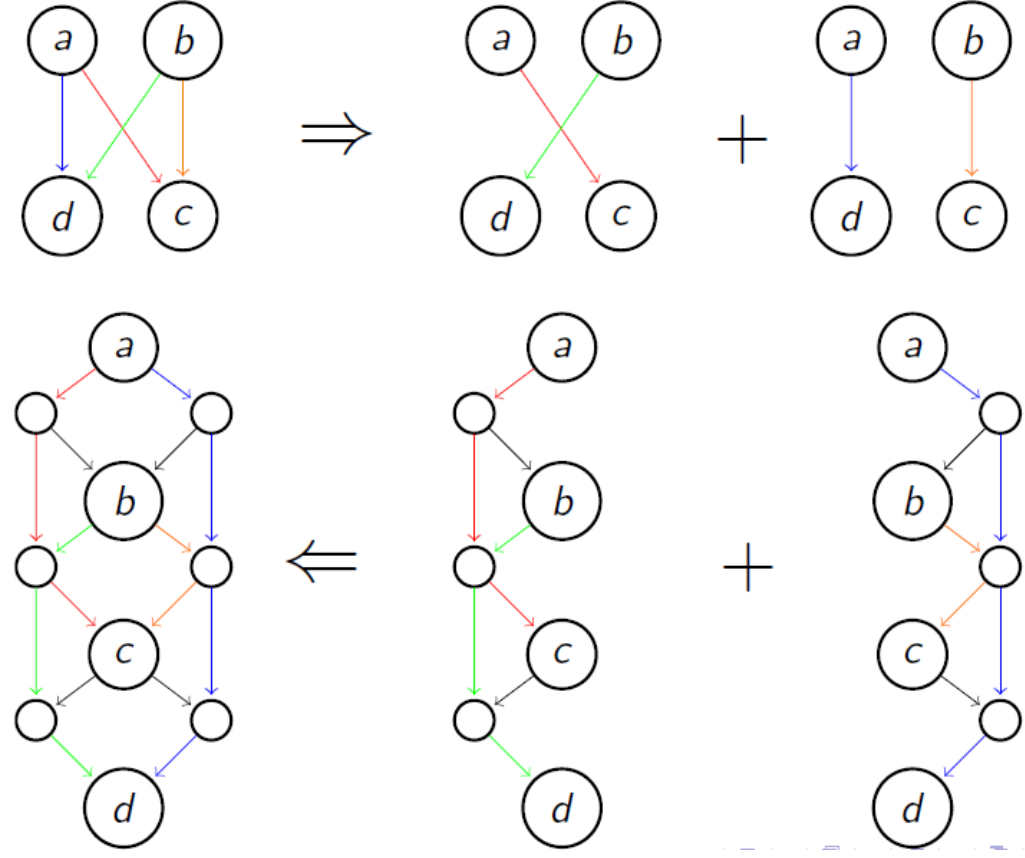


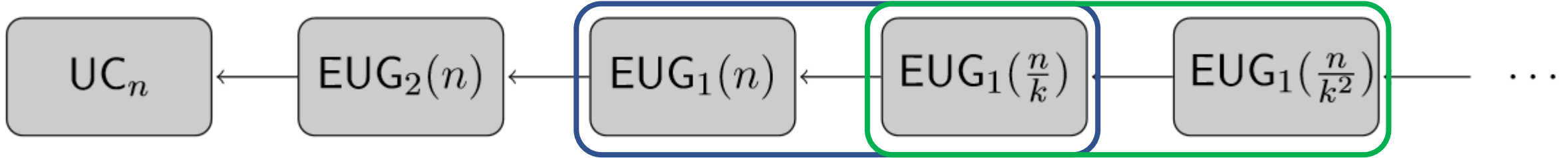


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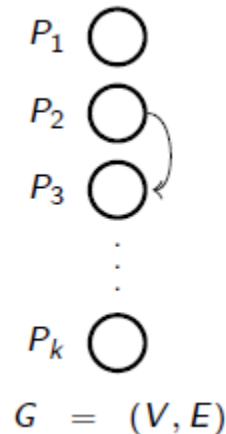


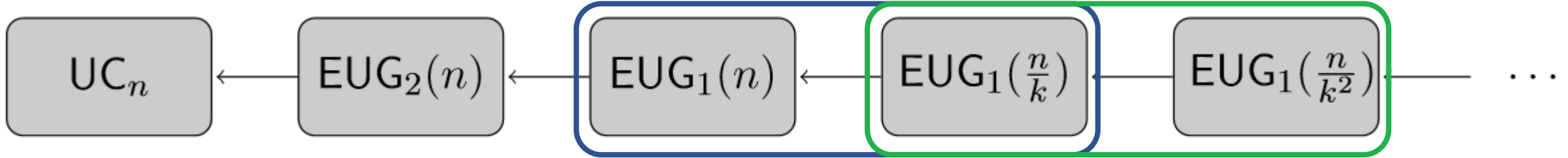
- **Definition 1** (Augmented DAG) for any graph $G = (V, E) \in \text{DAG}_1(k)$, $G' = (V', E') \in \text{DAG}_1(3k)$ is the augmented DAG of G if

$$V' = \{in_1, \dots, in_k\} \cup V \cup \{out_1, \dots, out_k\}$$

$$E' = E \cup E_{aux}$$

every $e \in E_{aux}$: either $e = (in_{i_1}, p_{j_1})$ or $e = (p_{i_2}, out_{j_2})$.



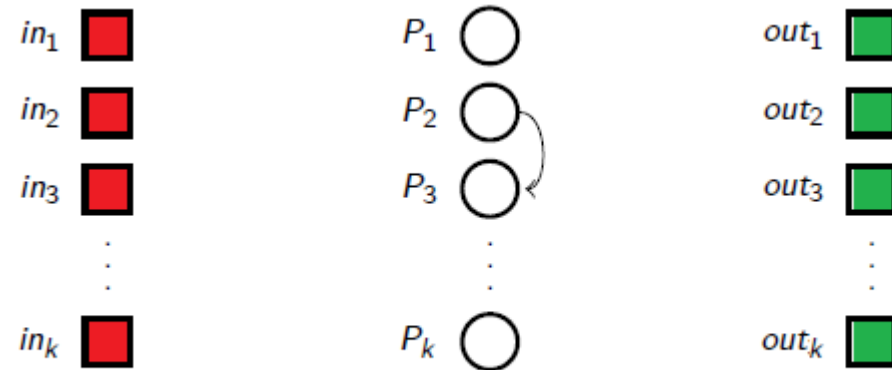


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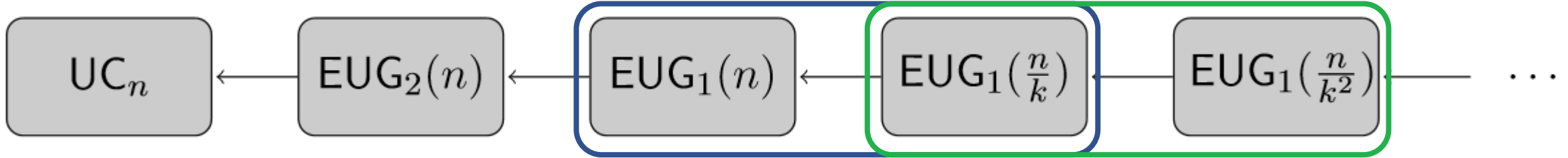
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$G = (V, E)$ with inputs and outputs

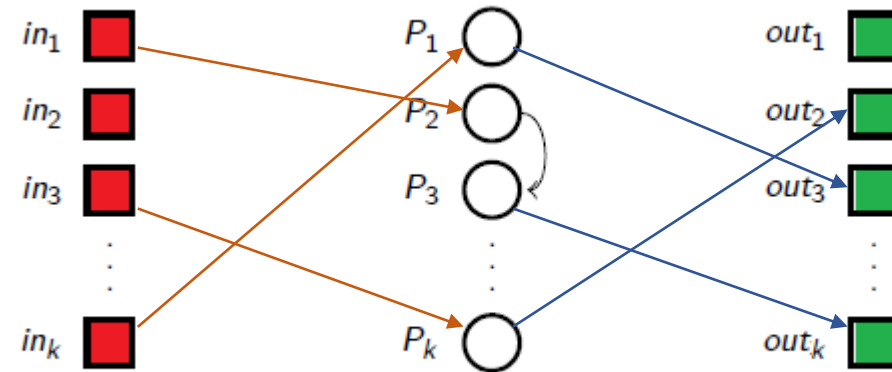


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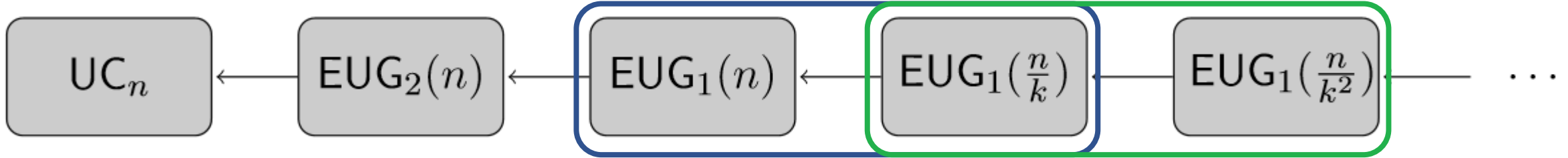
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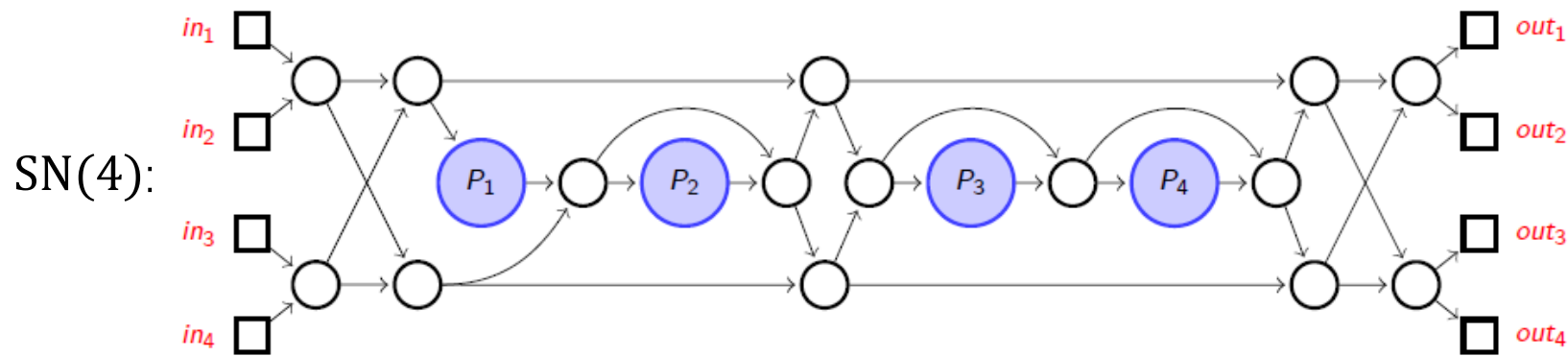
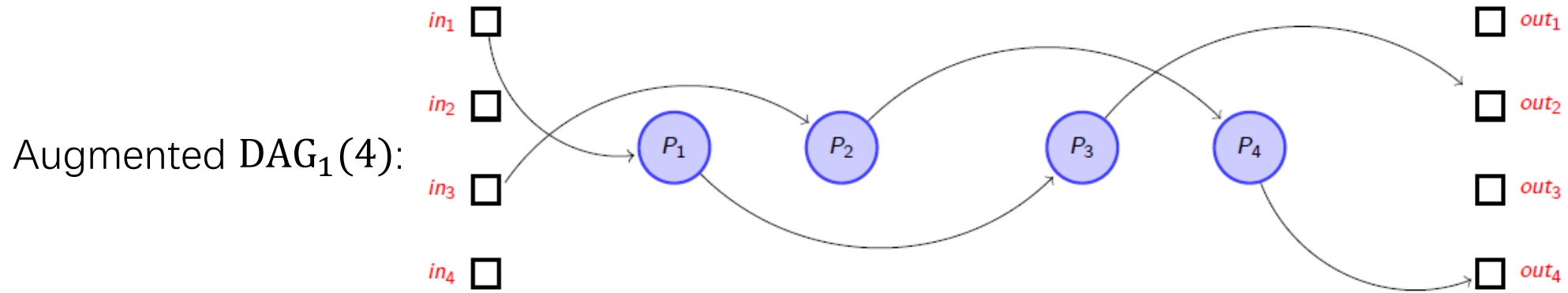
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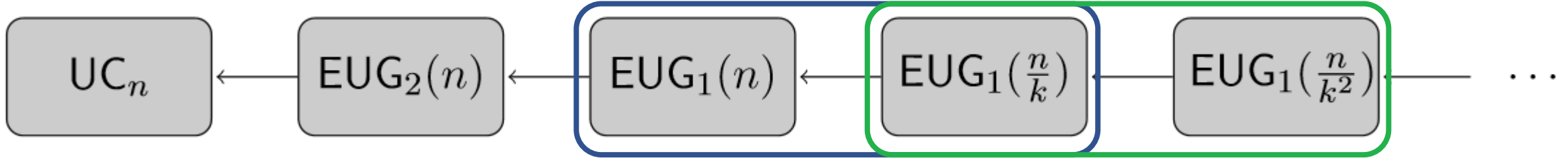


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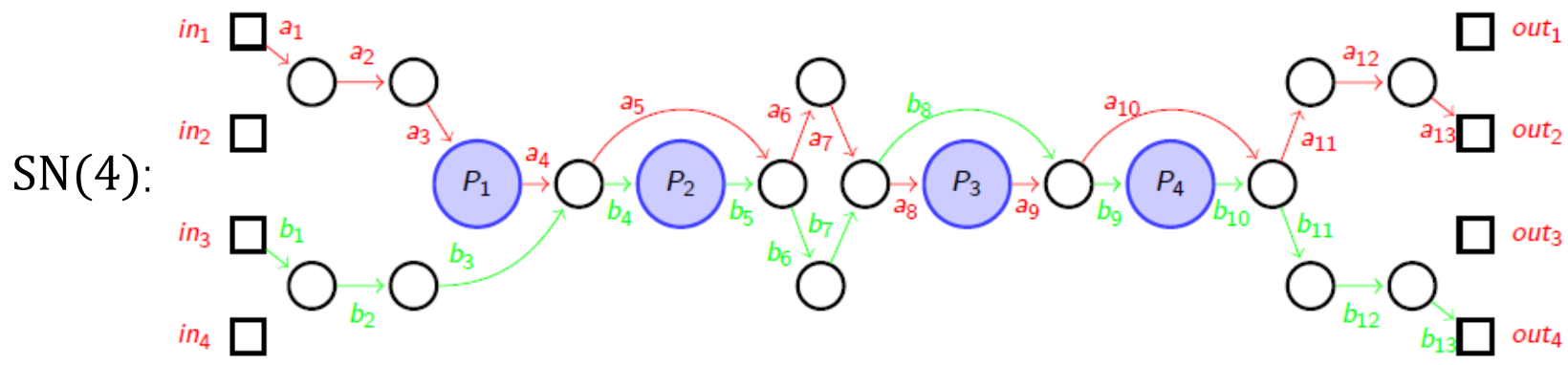
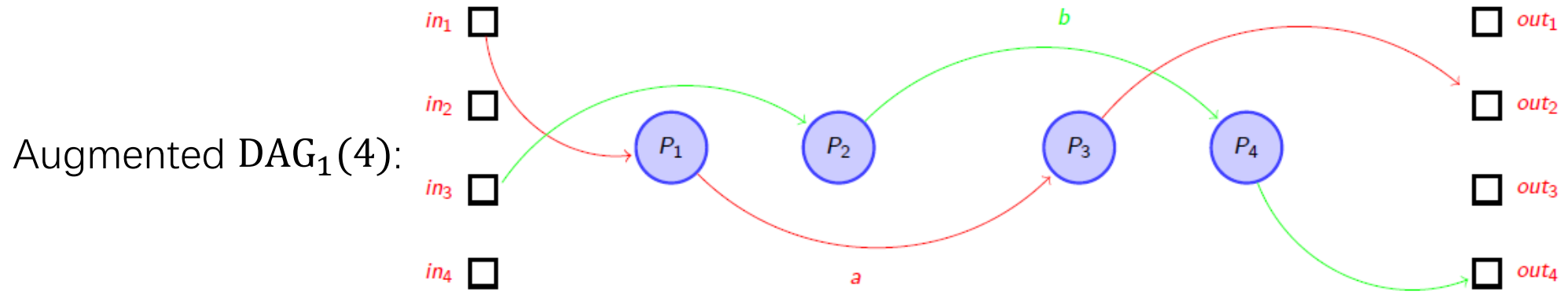


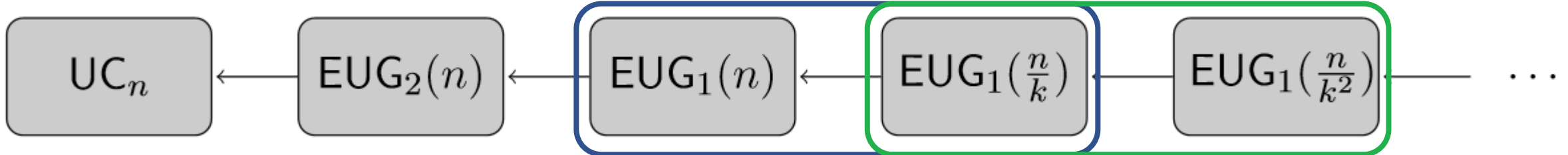
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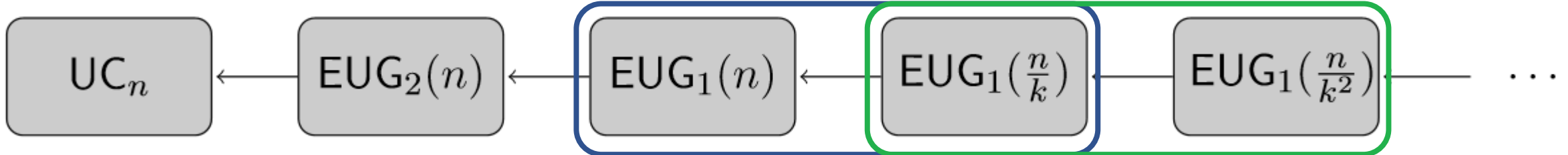
[Valiant76]: $EUG_1(\lceil n/k \rceil - 1) + k\text{-way supernode } SN(k) \rightarrow EUG_1(n)$,
 then we have $|EUG_1(n)| = k \cdot |EUG_1(\lceil n/k \rceil - 1)| + \lceil n/k \rceil \cdot |SN(k)|$

$SN(k)_1$

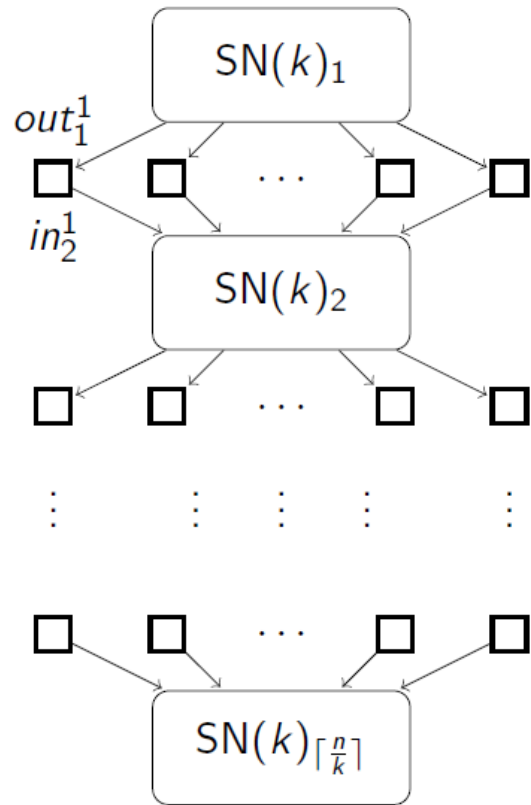
$SN(k)_2$

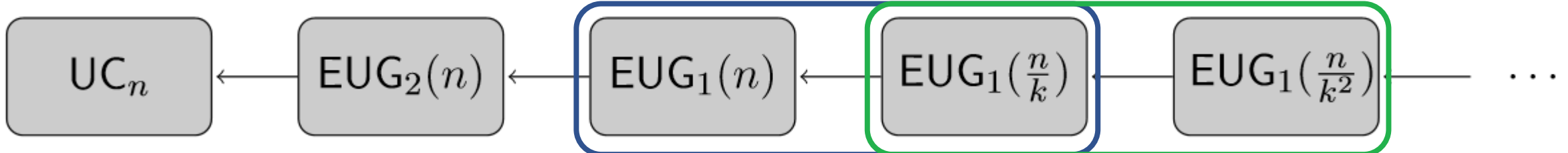
\vdots

$SN(k)_{\lceil n/k \rceil}$

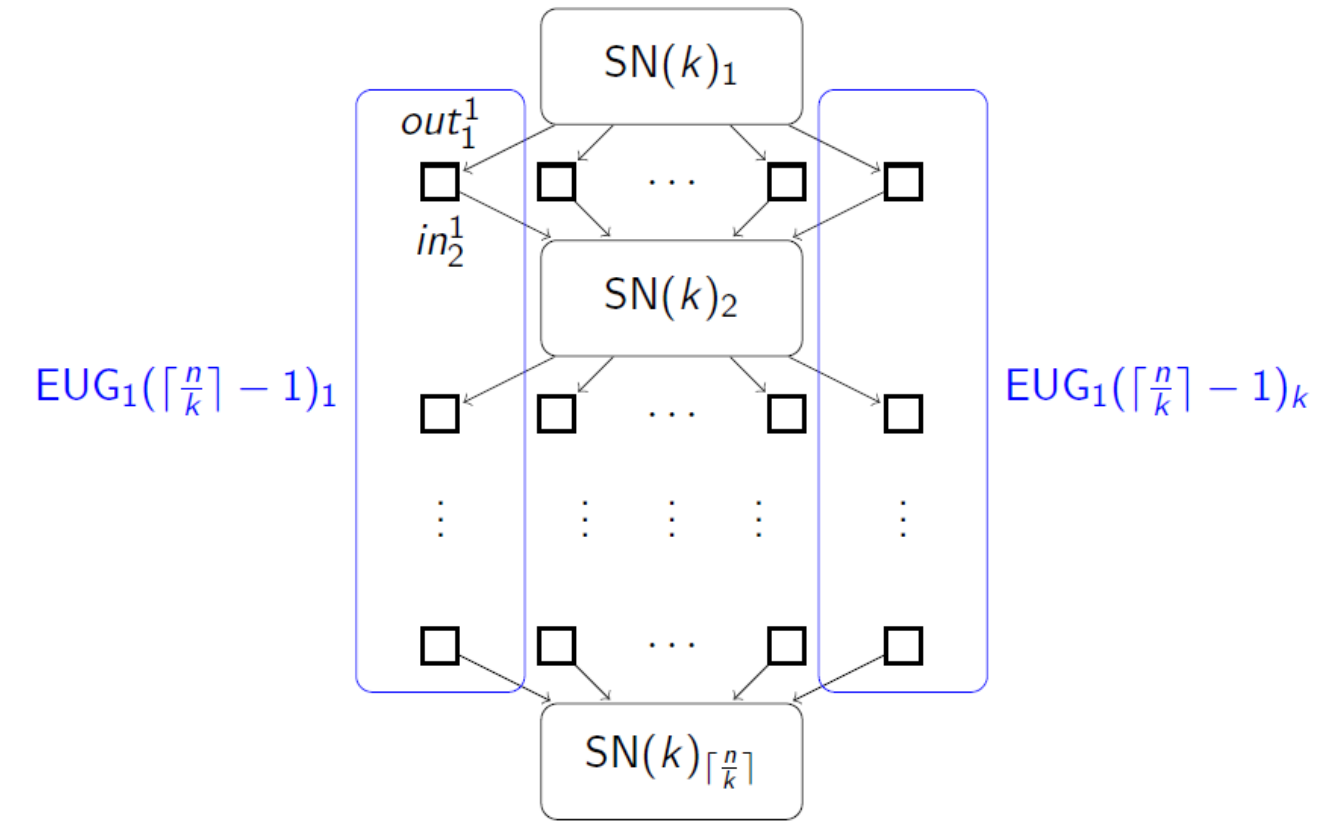


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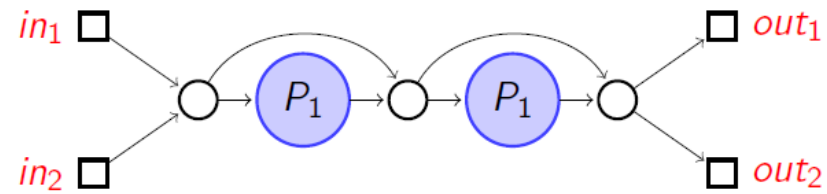




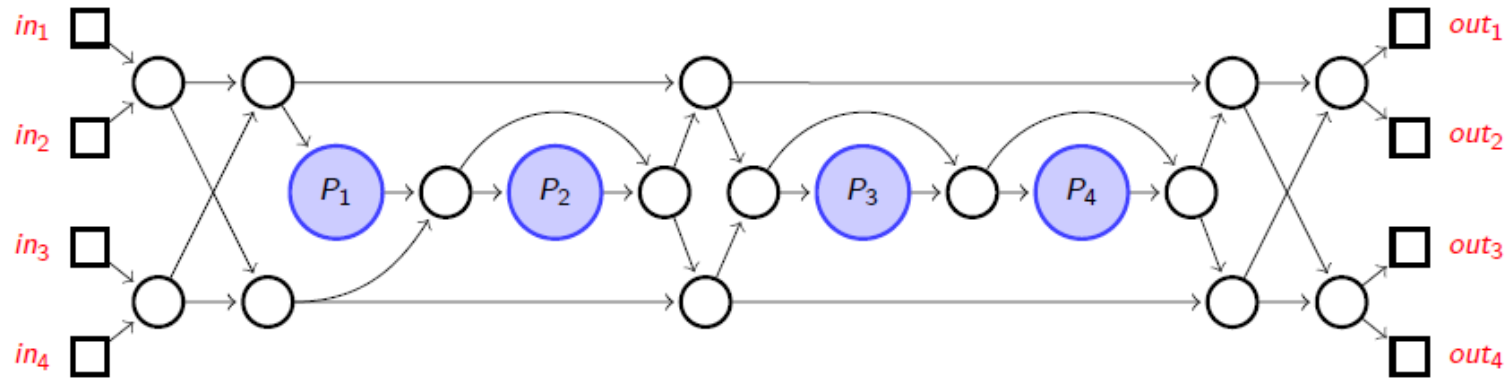
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Valiant's 2-way and 4-way Construction



$$|SN(2)| = 5, |EUG_2(n)| = 5n \log n$$



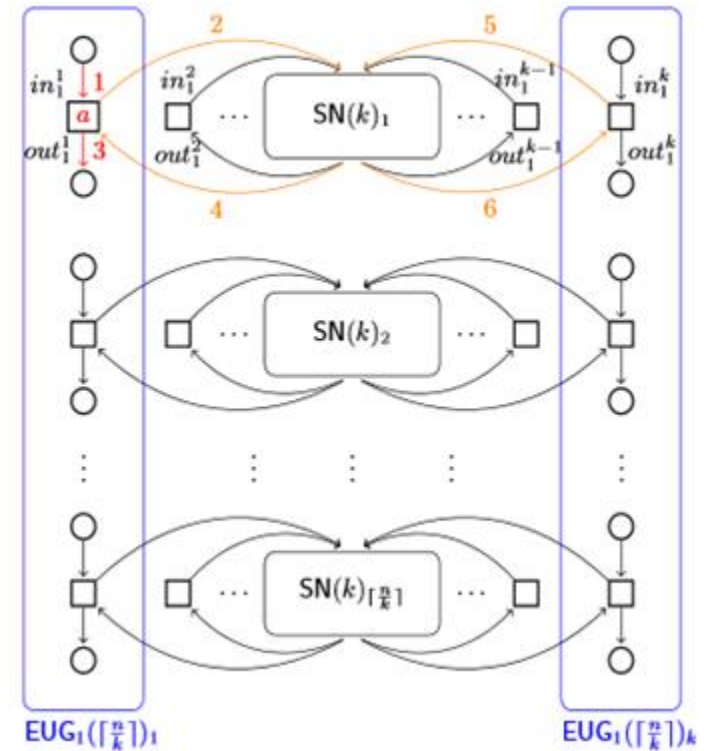
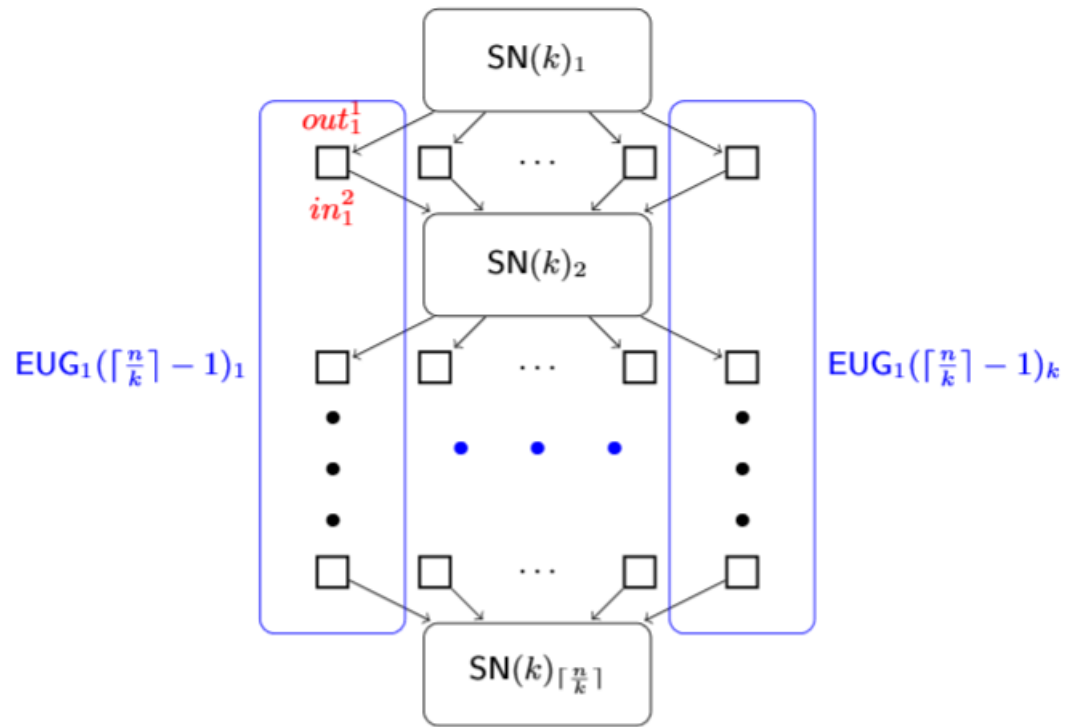
$$|SN(4)| = 19, |EUG_2(n)| = 4.75n \log n$$

Our Construction

Valiant's framework

vs.

Our intermediate construction

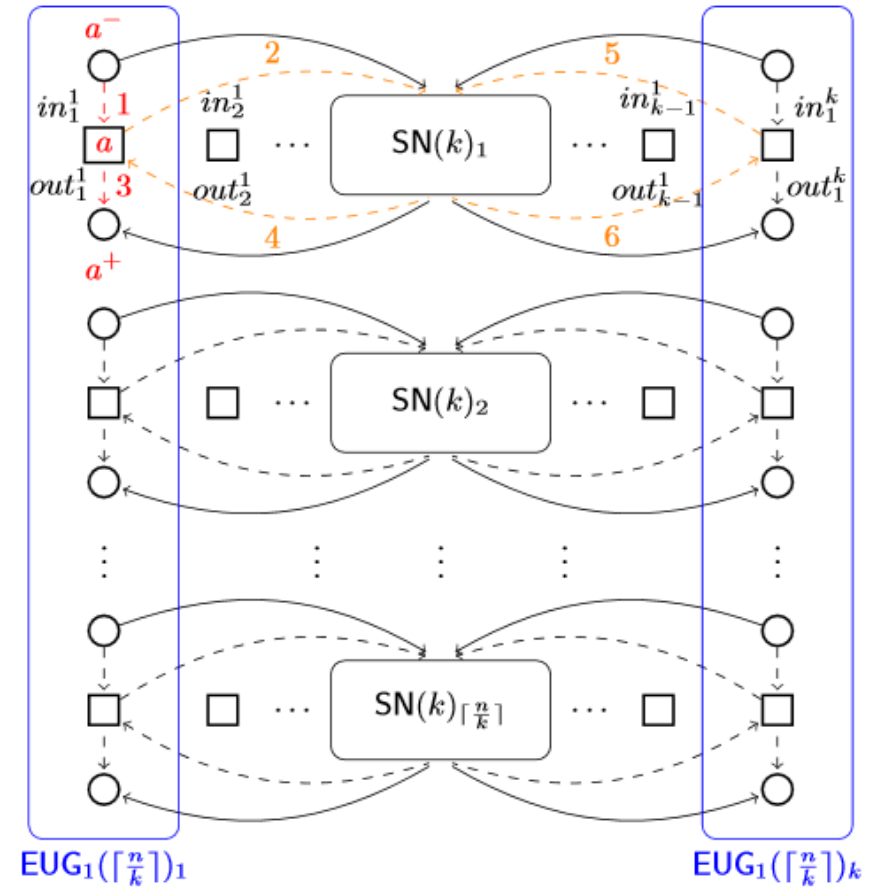
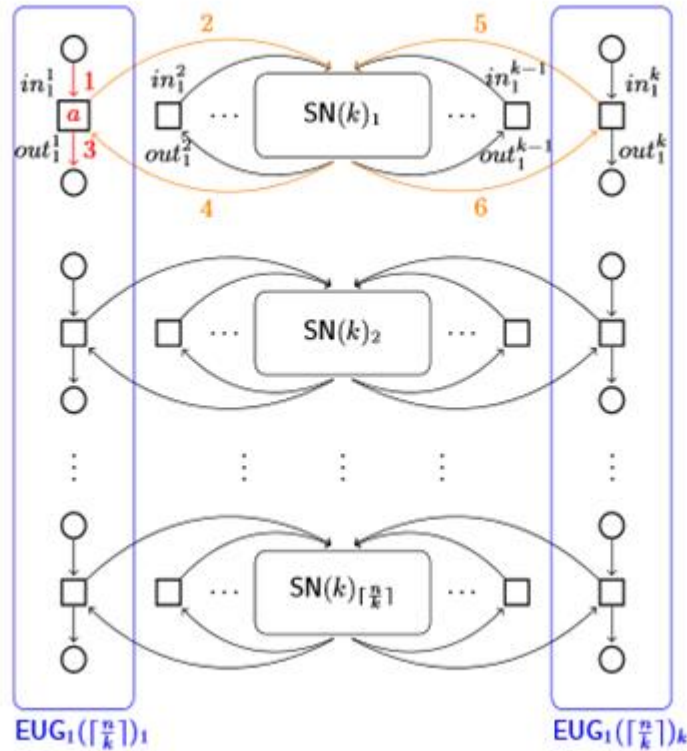


Our Construction

Our intermediate construction

vs.

Our end construction



Our End Construction: Size of Circuit

$$|\text{EUG}_2(n)| = 2|\text{EUG}_1(n)| - n$$

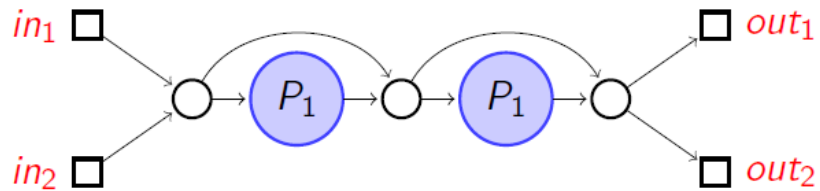
$$|\text{EUG}_1(n)| = k \cdot |\text{EUG}_1(\lceil \frac{n}{k} \rceil)| + \lceil \frac{n}{k} \rceil \cdot |\text{SN}(k)| - n .$$

Theorem 4 (Universal circuits). For any integer $k \geq 2$, there exists explicit k -way constructions of $\text{EUG}_2(n)$ and UC_n with

$$|\text{EUG}_2(n)| = \frac{2(|\text{SN}(k)| - k)}{k \log k} n \log n - O(n) \quad \square \geq 2.95$$

$$|\text{UC}_n| \leq 4|\text{EUG}_2(n)| + O(n) .$$

In particular, for $k = 2$ we have $|\text{EUG}_2(n)| = 3n \log n - O(n)$.



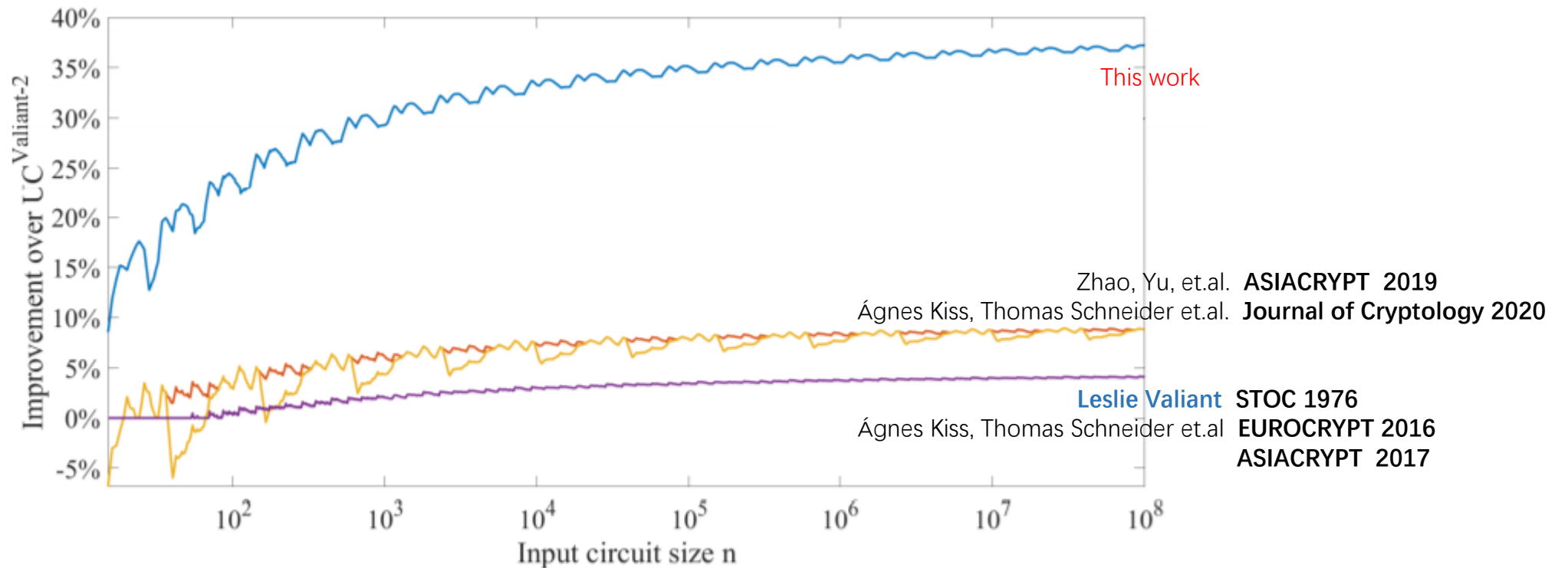
$$|\text{SN}(2)| = 5 , |\text{EUG}_2(n)| = 3n \log n - O(n) .$$

Comparison with Previous Works

Asymptotic size of $EUG_2(n)$:

Construction	Leslie Valiant STOC 1976	Günther, Kiss & Schnerider EUROCRYPT2016, ASIACRYPT 2017	Zhao Yu et al. ASIACRYPT 2019	Liu Yu et al. CRYPTO 2021
$ EUG_2(n) $	$\sim 4.75n \cdot \log n$	$\sim 4.75n \cdot \log n$	$\sim 4.5n \cdot \log n$	$\sim 3n \cdot \log n$

Close to lower bound:
 $2.95n \cdot \log n$



Related Works

- [Leslie Valiant](#), *Universal Circuit*, **STOC 1976**
- Helger Lipmaa, Payman Mohassel, Saeed Sadeghian. *Valiant's Universal Circuit: Improvements, Implementation, and Applications*, **ePrint 2016/017**
- [Ágnes Kiss](#), [Thomas Schneider](#). *Valiant's Universal Circuit is Practical*, **EUROCRYPT 2016**
- [Daniel Günther](#), [Ágnes Kiss](#), [Thomas Schneider](#). *More Efficient Universal Circuit Constructions*, **ASIACRYPT 2017**
- Shuoyao Zhao, Yu Yu, Jiang Zhang, **Hanlin Liu**. *Valiant's Universal Circuits Revisited: an Overall Improvement and a Lower Bound*, **ASIACRYPT 2019**
- Masaud Y. Alhassan, [Daniel Günther](#), [Ágnes Kiss](#), [Thomas Schneider](#). *Efficient and Scalable Universal Circuits*, **Journal of Cryptology 2020**

Thanks for Listening!