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A Novel Completeness Test for Leakage Models and its Application to Side Channel Attacks and Responsibly Engineered Simulators

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Outline

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SCA

- Attacks based on information leakage (timing, power consumption, electromagnetic emission, etc.)
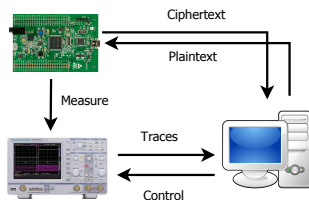- Recover the secret key potentially within a few minutes (1 — several million traces)
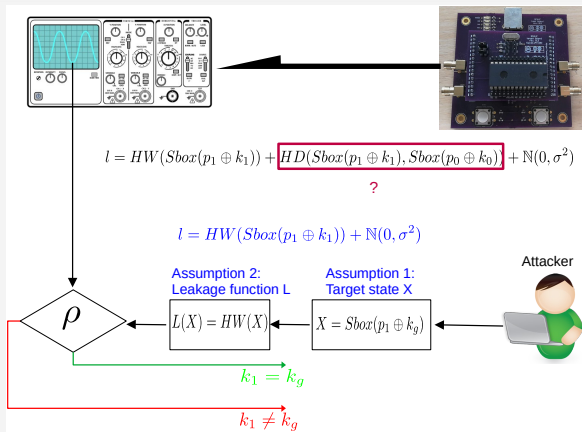

Figure: Side Channel Analysis

As a (non-profiled) attacker...

$$l = HW(Sbox(p_1 \oplus k_1)) + \boxed{HD(Sbox(p_1 \oplus k_1), Sbox(p_0 \oplus k_0))} + \mathbb{N}(0, \sigma^2)$$

?

$$l = HW(Sbox(p_1 \oplus k_1)) + \mathbb{N}(0, \sigma^2)$$

Attacker

Assumption 2:
Leakage function L

Assumption 1:
Target state X

ρ ← $L(X) = HW(X)$ ← $X = Sbox(p_1 \oplus k_g)$

$k_1 = k_g$

$k_1 \neq k_g$

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From an attacker's perspective...

Correct assumptions could be more costly than the secret key...

$$l = HW(Sbox(p_1 \oplus k_1)) + \boxed{HD(Sbox(p_1 \oplus k_1), Sbox(p_0 \oplus k_0))} + \mathbb{N}(0, \sigma^2)$$

$$l = HW(Sbox(p_1 \oplus k_1)) + \mathbb{N}(0, \sigma^2)$$

Attacker

"Your assumptions are (partly) wrong!"

"I already got the key, whatever...."

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For evaluation/certification...

Partly effective countermeasures are certainly not desirable...

$$l = HW(Sbox(p_1 \oplus k_1)) + \boxed{HD(Sbox(p_1 \oplus k_1), Sbox(p_0 \oplus k_0))} + \mathbb{N}(0, \sigma^2)$$

$$l = HW(Sbox(p_1 \oplus k_1)) + \mathbb{N}(0, \sigma^2)$$

Security Evaluation

"Your assumptions are (partly) wrong!"

"Er... then what does my results mean?"
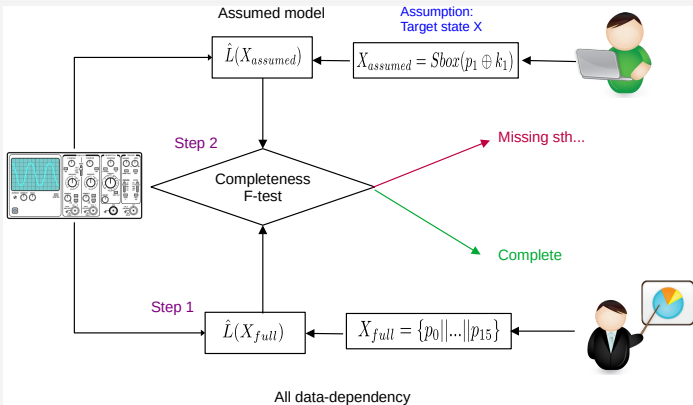
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Our contribution

We propose/clarify in this paper...

- "Leakage models" contain both X and L
  - Emphasis on X (there exist other solutions for L)
- "Completeness" test
  - "Completeness": X contains all relevant states for l
  - Using F-test to verify whether a selected X is complete (or not)
- Impacts of "completeness"
  - For attacks: revealing unexpected new leakage
  - For leakage simulators: finding leaks that would otherwise missed by overly-simplified models

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Road map

Assumed model

Defining a full model X_{full} (aka all leakage):

All-input model

For unmasked AES-128, let $\hat{X} =$ all 128-bit plaintext

- All leakage will be captured
- Requires $> 2^{128}$ traces to attack/analysis
- Collapsed models
    - Bound the inputs (as in leakage detection)
    - E.g. each byte in AES-128 takes only 11...1 or 00...0
    - Now the input space is bounded to 2^{16}

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F-test for ANOVA

$\hat{L}(p_0||...||p_{15})$ vs. $\hat{L}(Sbox(p_1 \oplus k_1))$

- Does the latter miss something?
  - $F > th$, $\hat{L}(Sbox(p_1 \oplus k_1))$ misses some factor that has significant contribution to the observed leakage
  - otherwise, complete up to the statistical power (i.e. provided number of traces)

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Put it all together,

Collapsed F-test for completeness

1 Construct a full model $\tilde{L}(X_{full})$ and an assumed model $\tilde{L}(X_{assumed})$

2 Comparison in F-test: if $F > th$, $X_{assumed}$ is not complete

Example: rejected because $HD(S(p_0 \oplus k_0), S(p_1 \oplus k_1))$ is missing

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1 Side channel analysis: achievements & challenges

2 Finding a complete set of *state*

3 Application: dissecting Attacks

4 Application: leakage simulators

5 Ethical considerations

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Not neccessarily "for the attacker"…

E.g. $Sbox(p_0 \oplus k_0) \oplus Sbox(p_1 \oplus k_1)$

- takes intensive effort to find
- 2 relevant key bytes ((k_0, k_1) vs. k_1)
- reveal unexpected μ-arch features (in a profiling setup)

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From ANSSI, @https://github.com/ANSSI-FR/SecAESSTM32
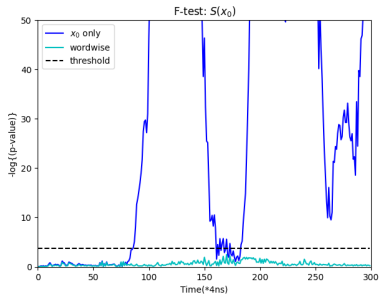
Scheme

$C(x) = rm \otimes x \oplus ra$

- One multiplicative mask rm and one additional mask ra
- Sbox input mask r_{in} and r_{out}

$$S'(rm \otimes x \oplus r_{in}) = rm \otimes S(x) \oplus r_{out}$$

- ra different for each byte
- rm, r_{in}, r_{out} shared within one encryption
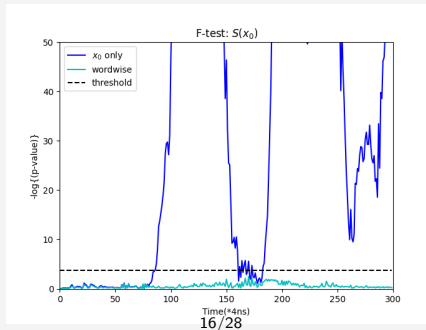
When computing masked $S'(C(x_0))$...

- Assumption: all relevant term for $S'(C(x_0))$ could leak
    - $X_{assumed} = \{x_0 = p_0 \oplus k_0 || ra_0 || r_{in} || r_{out}\}$
- $-log(pv) > th \Rightarrow$ not complete
- Not complete (blue line in the figure)
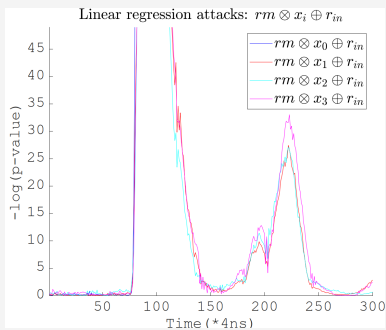

F-test: $S(x_0)$

Why?

Cortex M3 is a 32-bit core

- Load/Store bus is likely also 32-bit
- LDRB: always load word, discarding unnecessary bytes
- Word-wise load for all instructions $\Rightarrow$ complete(cyan line)

Verifying word-wise leakage

Leakage of all 4 bytes when computing the first S-box
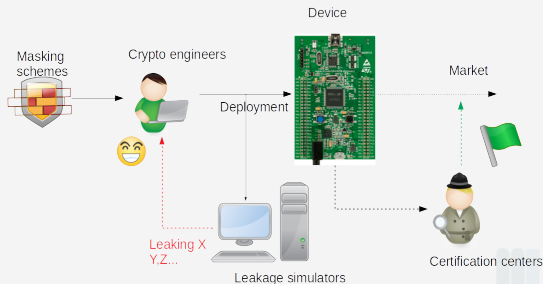
- all 4 bytes leak simultaneously (right figure)

Linear regression attacks: $rm \otimes x_i \oplus r_{in}$

Impact on attacks?

- x_i and x_j on different points on the trace
- can use the same point
- Bivariate $\Rightarrow$ Univariate
- more details in the paper...

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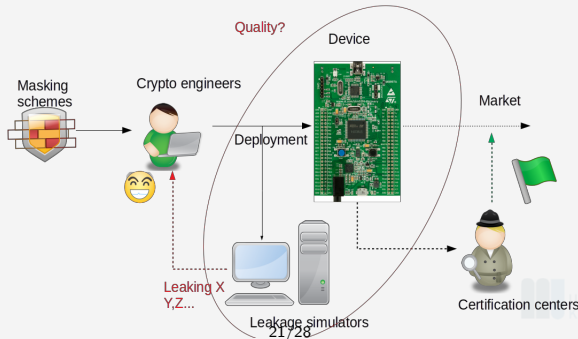
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Leakage simulators

- Early stage feedback $\Rightarrow$ Cheaper/faster development
- Leakage reasoning $\Rightarrow$ Targeted security patch

Leakage simulators

- Existing tools (Cortex M3, binary code level)
  - ELMO/ELMO*: model built from measurements
  - MAPS: RTL code from ARM

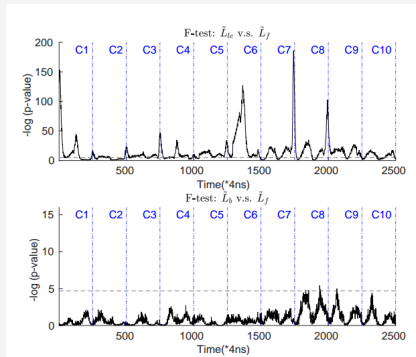- Challenge: quality? ← completeness test

Target gadget

A bitwise 2-share ISW multiplication

	Instruction	Device	ELMO	MAPS	$\tilde{L}_b$
0	$//r1 = a_{(1)}, r2 = a_{(2)}$ $//r3 = b_{(1)}, r4 = b_{(2)}, r5 = r$				
1	mov r6, r1(mov.w r6, r1 for MAPS)				
2	ands r6, r3$//r6 = a_{(1)}b_{(1)}$				
3	mov r7, r4(mov.w r7, r4 for MAPS)			✓	
4	ands r7, r2$//r7 = a_{(2)}b_{(2)}$				
5	ands r1, r4$//r1 = a_{(1)}b_{(2)}$	✓			✓
6	eors r1, r5$//r1 = a_{(1)}b_{(2)} \oplus r$	✓			✓
7	ands r2, r3$//r2 = a_{(2)}b_{(1)}$	✓	✓	✓	✓
8	eors r1, r2$//r1 = a_{(1)}b_{(2)} \oplus r \oplus a_{(2)}b_{(1)}$				
9	eors r6, r1$//c_{(1)} = a_{(1)}b_{(2)} \oplus r \oplus a_{(2)}b_{(1)} \oplus a_{(1)}b_{(1)}$	✓			✓
10	eors r7, r5$//c_2 = r \oplus a_{(2)}b_{(2)}$	✓	✓	✓	✓

Completeness test

- $\tilde{L}_{le}$: a superset for both ELMO and MAPS
  - both ELMO and MAPS fail in almost every cycle...
- $\tilde{L}_b$: recursively adding missing factors to $\tilde{L}_{le}$

Impacts on leakage detections

- both ELMO and MAPS miss leaks
- better models $\Rightarrow$ more accurate detections

	Instruction	Device	ELMO	MAPS	$\tilde{L}_b$
0	$/\!/r1 = a_{(1)}, r2 = a_{(2)}$ $/\!/r3 = b_{(1)}, r4 = b_{(2)}, r5 = r$				
1	mov r6, r1(mov.w r6, r1 for MAPS)				
2	ands r6, r3$/\!/r6 = a_{(1)}b_{(1)}$				
3	mov r7, r4(mov.w r7, r4 for MAPS)			✓	
4	ands r7, r2$/\!/r7 = a_{(2)}b_{(2)}$				
5	ands r1, r4$/\!/r1 = a_{(1)}b_{(2)}$	✓			✓
6	eors r1, r5$/\!/r1 = a_{(1)}b_{(2)} \oplus r$	✓			✓
7	ands r2, r3$/\!/r2 = a_{(2)}b_{(1)}$	✓	✓	✓	✓
8	eors r1, r2$/\!/r1 = a_{(1)}b_{(2)} \oplus r \oplus a_{(2)}b_{(1)}$				
9	eors r6, r1$/\!/c_{(1)} = a_{(1)}b_{(2)} \oplus r \oplus a_{(2)}b_{(1)} \oplus a_{(1)}b_{(1)}$	✓			✓
10	eors r7, r5$/\!/c_2 = r \oplus a_{(2)}b_{(2)}$	✓	✓	✓	✓

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Threats of proportional leakage simulators

ELMO/ELMO*

- Proportional: "close to realistic measurements"
- Pros
  - Good for attack estimation
  - Can estimate power consumption
- Cons
  - Free templates for attackers?

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Ethical considerations

Nominal leakage simulators

- Nominal: finding state X, not estimating L
- Pros
    - Good for leakage detection
    - Cannot be used as "free templates"
- Cons
    - Qualitative only

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Questions?

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