# Concrete Analysis of Quantum Lattice Enumeration 

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## Motivation

- Lattices are a popular choice for post-quantum cryptography.
- 2 popular generic attacks:
- Sieving (faster, but exponential space).
- Enumeration (slower, but polynomial space).
- Most literature is about asymptotics.
- Our work: concrete quantum look.


## History of Work

## [Bel13]

[AK17, Mon18]
[ADPS16, ABB+ 17, PLP16]
[ANS18]

## Belovs's quantum walk

Quantum tree backtracking algorithm
Quantum Enumeration is mentioned

Presenting quantum lattice enumeration and its variations are discussed at a high level without going into detail about quantum circuits and resource estimates

## Our work

Proposed a concrete implementation of Montanaro's algorithm for lattice enumeration, and its circuit resource estimates.

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- Quantum efficiency: T-gates or T-depth.


## Enumeration



Visualization idea: Thijs Laarhoven.

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## Naive tree search

Our problem can be modelled as a tree.


Where the green node corresponds to the shortest vector $-3 \mathbf{b}_{1}+2 \mathbf{b}_{2}+\mathbf{b}_{3}$.

## Backtracking: idea

We need to find some way to reduce the number of nodes in the tree.


At each level call an oracle/predicate, this is called backtracking.

## Quantum backtracking

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Montanaro '18: split the tree into odd and even distance from the root. Each step consists of going over the even distance nodes and their children. And going over the odd distance nodes and their children (and the root). Part of our paper: circuit level implementation of this step, called $R_{B} R_{A}$.


## Overall Circuit

Montanaro's algorithm uses the phase estimation of the operator $U=R_{B} R_{A}$ on the root of the substrees to detect a marked solution


Figure: General circuit for phase estimation where $U:=R_{B} R_{A}$.

## Our circuit design and its resource estimation

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The predicate $P$ will ensure the coefficients $v_{i}$ satisfy

$$
\sum_{j \geq n+1-l}\left(\sum_{i \geq j} \mu_{i, j} \cdot v_{i}\right)^{2} \cdot\left\|\mathbf{b}_{j}^{*}\right\|^{2} \leq R^{2}
$$

where $R, \mu_{i j},\left\|\mathbf{b}_{j}^{*}\right\|$ are precomputed classically.

## Predicate Design in a nutshell

$$
\underbrace{\sum_{\text {(II) } A_{k}}}_{(I V) \text { Addd }_{F F}}(\underbrace{\sum_{i \geq j}}_{(I)} \underbrace{\mu_{i, j} \cdot v_{i}}_{(I I I) M u I_{F F}})^{2} \underbrace{}_{\left(\left\|\mathbf{b}_{j}^{*}\right\|^{2}\right.} \leq R^{2}
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Figure: Components in the predicate circuit

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- Bounds:
- We know $\mathcal{T} \leq n^{n /(2 e+o(n))}$
- Better when the basis is preprocessed, e.g., in HKZ-reduced basis $d \approx n^{(\ln n) / 4}$


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## Overall Resource Estimates

## - T-depth —

$32 \sqrt{\mathcal{T} n} \cdot\left[16 n p\left(\log B+2 \log n+p^{0.158}\right)+O(n \log B)+8 d^{2} \log (d \sqrt{\mathcal{T} n})+4 d^{2} \log d+O\left(d^{2}\right)\right]$

- T-size -

$$
32 \sqrt{\mathcal{T} n} \cdot\left[8(d+1)\left(14 p n^{2}(B+1)+O\left(n^{2} B\right)\right)+8 d^{2} \log (d \sqrt{\mathcal{T} n})+16 d^{2} \log d+O\left(d^{2}\right)\right]
$$

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## Practical Parameters

For cryptographic size $n$ (e.g., $n \gtrsim 400$ ), it's reasonable to model $d \approx n, B \approx n^{2}, p \leq 3 n, \log (\mathcal{T}) \approx c n \log n$

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T-depth: $\left(128 c n^{3} \log n+O\left(n^{2.158}\right)\right) \sqrt{\mathcal{T} n}$

$$
\text { T-size: }\left(10752 n^{6}+O\left(n^{5}\right)\right) \sqrt{\mathcal{T} n}
$$

## Heuristic 1: $d \approx n, B \approx n^{2}$



Figure: Bounds $d$ and $B$, based on solved SVP Challenges.

## Heuristic 2 - Bound $B \approx n^{2}$



Figure: Bound $B$, based on BKZ experiments and simulations.

## Heuristic 3 - $\mu_{i, j}$ have similar magnitude



Figure: Experiments on the bound for $\mu_{i, j}$ in LLL/BKZ reduced basis.

## Questions

- Can we make the phase estimation process parallel to reduce the depth?
- What are the resource estimates for extreme pruning?


The oracle receives questions now. Do you have any questions?

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