# Combined Fault and Leakage Resilience: Composability, Constructions and Compiler

Sebastian Berndt Thomas Eisenbarth Sebastian Faust Marc Gourjon Maximilian Orlt Okan Seker

### Fault and Side-Channel Attacks

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Classical setting: Black-box model



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 ▶ Adversary learns Input/Output E.g. plain-text/cipher-text (M, C)



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- Side-Channel Attack
  - E.g. Power consumption



# Fault and Side-Channel Attacks

Classical setting: Black-box model

- Adversary learns Input/Output E.g. plain-text/cipher-text (M, C)
- Real Adversary is more powerful
  - Side-Channel Attack
    - E.g. Power consumption
  - Fault Attacks
    - E.g. Electromagnetic Pulses



# Security Model



Combined Fault and Leakage Resilience: Composability, Constructions and Compiler

# Security Model

#### Computational model



# Security Model

Computational model

Arithmetic circuit



Computational model

Arithmetic circuit

Adversarial model



Computational model

Arithmetic circuit

Adversarial model

Leakage model d arbitrary wires can be probed



- Computational model
  - Arithmetic circuit
- Adversarial model
- Leakage model d arbitrary wires can be probed
- Fault model
  - e arbitrary wires can be faulted



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- Combined model



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 $O(d + e) \le O(d \cdot e) \Rightarrow$  This work uses Shamir Secret Sharing



Encode the inputs *a* and *b* 

а<sub>0</sub> а1



Compiler

Encode the inputs a and b
 Only compute on encodings a<sub>0</sub> - a<sub>1</sub> - a<sub>1</sub> -







- Encode the inputs *a* and *b*
- Only compute on encodings
- Randomize the circuit







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Compiler secure against d probes and e faults

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[SFRES18]

[DN19]

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Compiler secure against d probes and e faults

[SFRES18][DN19]n = 2d + e + 1 sharesn = d + e + 1 shares



 $\triangleright > O(n^3)$  Complexity

Compiler secure against d probes and e faults

[SFRES18]  $\blacktriangleright n = 2d + e + 1$  shares Improve number of shares

• 
$$O(n^2)$$
 Complexity





Improve complexity

Compiler secure against d probes and e faults This Work [SFRES18] [DN19]  $\blacktriangleright$  n = 2d + e + 1 shares  $\blacktriangleright$  n = d + e + 1 shares  $\blacktriangleright$  n = d + e + 1 shares Improve number of shares  $\triangleright$   $O(n^2)$  Complexity  $\blacktriangleright O(n^2)$  Complexity  $\triangleright > O(n^3)$  Complexity Improve complexity



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New Gadgets: Mult., Add. & Ref.

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Leakage Resilience:	Fault Resilience:				
d probes in the circuit $d/2$ probes in each gadget	e faults in the circuit				

Leakage- Simulator Mult.,	v Gadg Add.	ets: & Ref.
Leakage Resilience: <i>d</i> probes in the circuit <i>d</i> /2 probes in each gadget		Fault Resilience: <i>e</i> faults in the circuit











#### **•** Take *n* different $\alpha_i$ with $\alpha_i \neq 0$

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 $\frac{\operatorname{Enc}(s)}{r_1 \dots r_d \leftarrow \$ \mathbb{F}}$ for  $j = 0, \dots, n-1$ :  $s_j \leftarrow \sum_{i=1}^d r_i \alpha_j^i + s$ return  $[\![s]\!]_d^n := (s_0, \dots, s_{n-1})$ 

▶ Take *n* different  $\alpha_i$  with  $\alpha_i \neq 0$ ▶  $[[s]]_d^n = (s_i)_{i \in [n]} \leftarrow \text{Enc}(s)$   $\frac{\operatorname{Enc}(s)}{r_1 \dots r_d \leftarrow \$ \mathbb{F}}$ for  $j = 0, \dots, n-1$ :  $s_j \leftarrow \sum_{i=1}^d r_i \alpha_j^i + s$ return  $[\![s]\!]_d^n := (s_0, \dots, s_{n-1})$ 

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Leakage Resilience: Any set of *d* shares s<sub>i</sub> is uniformly, independently distributed.

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s =  $f(0) \leftarrow \text{Dec}([[s]]^n_d)$ 

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- Leakage Resilience: Any set of d shares s<sub>i</sub> is uniformly, independently distributed.
- ► Fault Resilience: If only e shares of d + e + 1 shares are faulted the polynomial has a degree > d!

Addition of  $\llbracket a \rrbracket_d^n$  and  $\llbracket b \rrbracket_d^n$ 

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• Compute  $c_i \leftarrow a_i + b_i$ 

Addition of  $\llbracket a \rrbracket_d^n$  and  $\llbracket b \rrbracket_d^n$  $\blacktriangleright$  Compute  $c_i \leftarrow a_i + b_i$   $\begin{cases} \left(\sum_{i=1}^{d} r_{i} x^{i} + a\right) + \left(\sum_{i=1}^{d} r_{i} x^{i} + b\right) \\ = \sum_{i=1}^{d} (r_{i} + r'_{i}) x^{i} + (a + b) \end{cases}$ 

Addition of  $\llbracket a \rrbracket_d^n$  and  $\llbracket b \rrbracket_d^n$ 

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Refresh of  $\llbracket a \rrbracket_d^n$ 

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Refresh of  $[a]_d^n$ 

- ► Generate  $\llbracket b \rrbracket_d^n \leftarrow \text{Enc}(0)$
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Probing Attack: Our Paper Fix: *d* Refreshes in a row

# Fixed SotA Compiler with n = 2d + e + 1

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Multiplication of  $\llbracket a \rrbracket_d^n$  and  $\llbracket b \rrbracket_d^n$
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- ► Compute  $c_i \leftarrow a_i \cdot b_i$
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- ▶ Reduce  $\llbracket c \rrbracket_{2d}^n$  down to  $\llbracket c \rrbracket_d^n$

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- ▶ Reduce  $\llbracket c \rrbracket_{2d}^n$  down to  $\llbracket c \rrbracket_d^n$
- Problem: Requires n = 2d + e + 1

$$\begin{cases} \sum_{i=1}^{d} r_{i}x^{i} + a \cdot \sum_{i=1}^{d} r_{i}x^{i} + b \\ = \sum_{i=1}^{2d} (r_{i}'')x^{i} + (a \cdot b) \end{cases}$$

### Compiler with n = d + e + 1

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Reverse the steps

- ► Reduce  $\llbracket a \rrbracket_d^n$ ,  $\llbracket b \rrbracket_d^n$  down to  $\llbracket a \rrbracket_{d/2}^n$ ,  $\llbracket b \rrbracket_{d/2}^n$
- Compute  $\llbracket c \rrbracket_d^n = \llbracket a \cdot b \rrbracket_d^n$



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- Compute  $\llbracket c \rrbracket_d^n = \llbracket a \cdot b \rrbracket_d^n$
- ▶ Problem *d* probes in  $\llbracket a \rrbracket_{d/2}^n$  reveal *a*





Compiler with n = d + e + 1

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- Problem d probes in  $\llbracket a \rrbracket_{d/2}^n$  reveal a

Solution: SplitRed splits  $\llbracket a \rrbracket_d^n$  into  $\llbracket a' \rrbracket_d^n$  and  $\llbracket a'' \rrbracket_d^n$  with  $\llbracket a' \rrbracket_d^n + \llbracket a'' \rrbracket_d^n = \llbracket a \rrbracket_{d/2}^n$ 























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(a)+(b)+(c)= Combined Resilience






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