

# Actively Secure Half-Gates with Minimum Overhead under Duplex Networks

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# Background on Constant Round 2PC

- Steady improvement in the semi-honest world

Textbook [Yao86]	P&P [BMR90]	GRR3 [NPS99]	GRR2 [PSSW90]	Free-XOR [KS08]	FleXOR [KMR14]	Half-Gates [ZRE15]	Three-Halves [RR21]
XOR: $8\kappa$	XOR: $4\kappa$	XOR: $3\kappa$	XOR: $2\kappa$	XOR: 0	$\{0, 1, 2\}_\kappa$	$2\kappa$	$1.5\kappa + 5$
AND: $8\kappa$	AND: $4\kappa$	AND: $3\kappa$	AND: $2\kappa$	AND: $3\kappa$			



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- What about the malicious world?

Cut-and-Choose

[LP07,NO09,HKE13,NST17,...]

$O(\rho\kappa)$  or  $O(\frac{\rho\kappa}{\log C})$

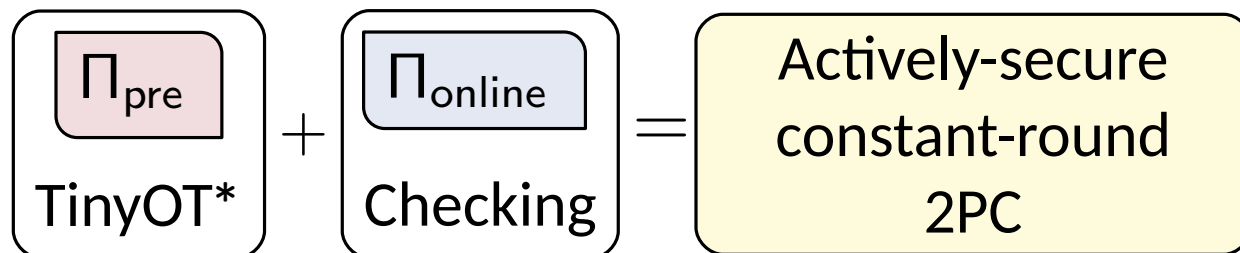
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Cut-and-Choose [LP07,NO09,HKE13,NST17,...]	Authenticated Garbling [WRK17,KRRW18]
$O(\rho\kappa)$ or $O(\frac{\rho\kappa}{\log C})$	$\Pi_{\text{pre}}: 13\kappa + 8\rho$ $\Pi_{\text{online}}: 2\kappa + 1$



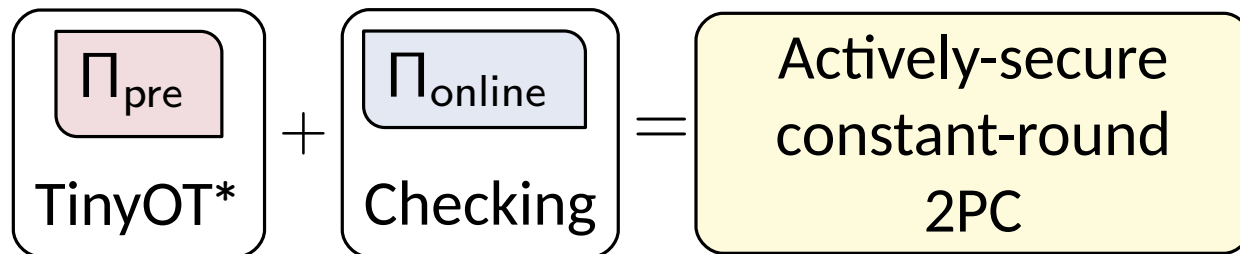
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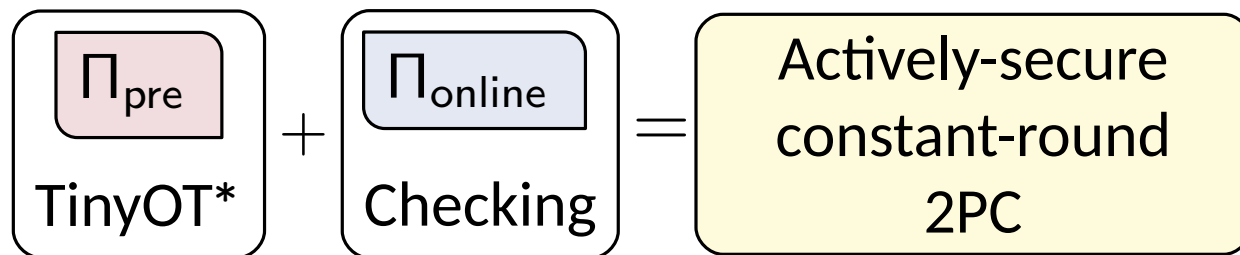
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Cut-and-Choose [LP07,NO09,HKE13,NST17,...]	Authenticated Garbling [WRK17,KRRW18]	PCGs [BCG+19, YWL+20, CRR21,...]	AG from PCG [DILO22]
$O(\rho\kappa)$ or $O(\frac{\rho\kappa}{\log C})$	$\Pi_{\text{pre}}: 13\kappa + 8\rho$ $\Pi_{\text{online}}: 2\kappa + 1$		$\mathcal{F}_{\text{VOLE-hyb.}} 2\kappa + 8\rho$ $\mathcal{F}_{\text{DAMT-hyb.}} 2\kappa + 4\rho$

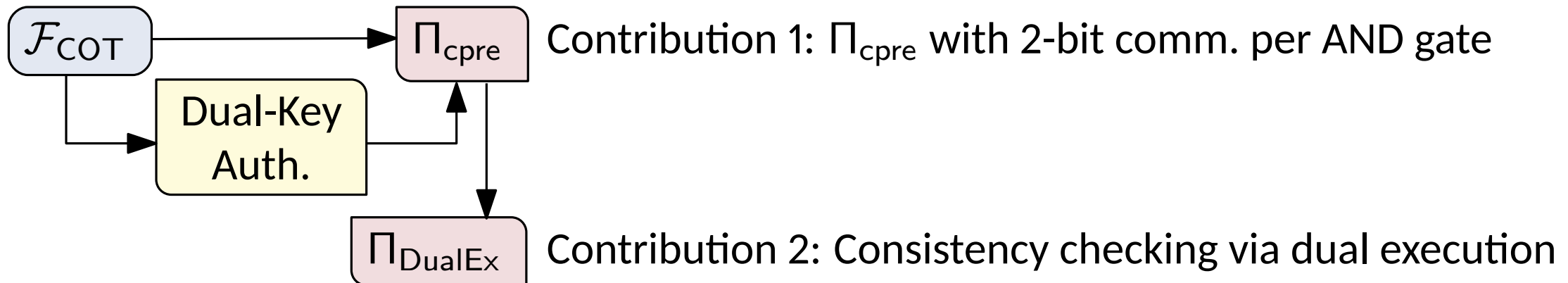


Can we close the gap?

# Our Contributions

- Authenticated garbling with one-way comm. as small as semi-honest half-gates

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	$2\kappa$	$2\kappa$
HSS-PCG	8	2	$8\kappa + 11$ (4.04 $\times$ )	$16\kappa + 22$ (8.09 $\times$ )
KRRW-PCG	4	4	$5\kappa + 7$ (2.53 $\times$ )	$8\kappa + 14$ (4.05 $\times$ )
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25 $\times$ )	$2\kappa + 8\rho + 5$ (2.27 $\times$ )
This work	8	3	$2\kappa + 5$ ( $\approx 1\times$ )	$4\kappa + 10$ (2.04 $\times$ )
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48 $\times$ )	$2\kappa + 3\rho + 4$ ( $\approx 1.48\times$ )



# Authenticated Garbling = Distributed Garbling + Checking



controls garbling so it can

- selective-failure on  $\Lambda := z \oplus \lambda \Rightarrow$  Secret share  $\lambda := a \oplus b$
- garble different logic  $\Rightarrow$  Add IT-MAC, equality check, etc.

$\Lambda_i$	$\Lambda_j$	Masked $L_k, \Lambda_k$
0	0	$L_{k,0} \oplus (\lambda_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
0	1	$L_{k,0} \oplus (\lambda_i \cdot \bar{\lambda}_j \oplus \lambda_k) \Delta_A$
1	0	$L_{k,0} \oplus (\bar{\lambda}_i \cdot \lambda_j \oplus \lambda_k) \Delta_A$
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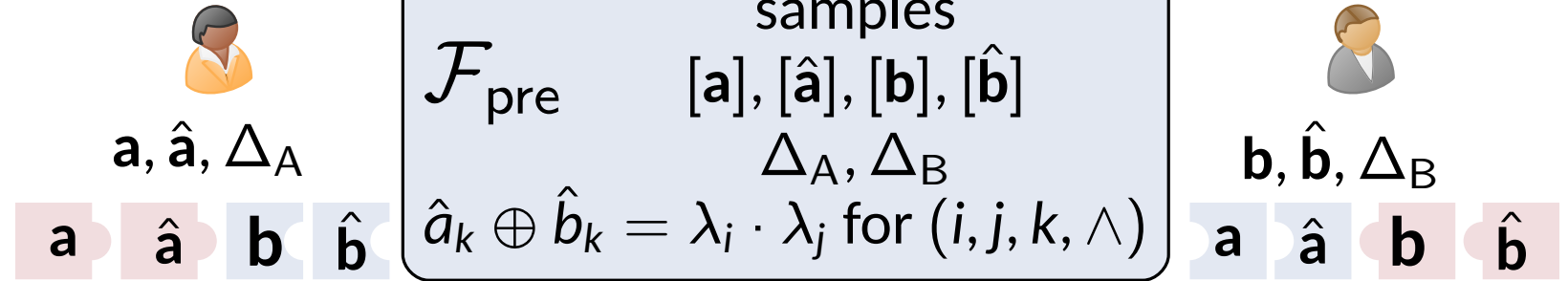


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$$\begin{array}{c} \text{a} \\ \Delta_B \end{array} \oplus \begin{array}{c} \text{a} \\ \Delta_B \end{array} = \text{a} \cdot \Delta_B$$



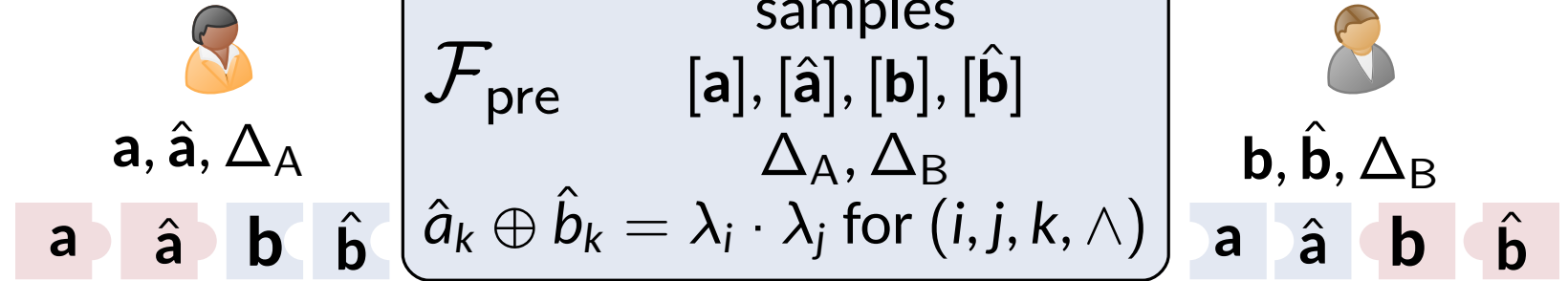
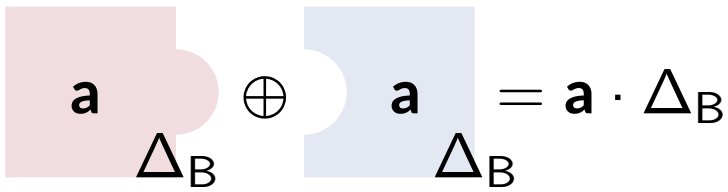
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$\Lambda_i$	$\Lambda_j$	Alice's GC	Bob's GC
0	0	$L_{k,0} \oplus K[\Lambda_{00}]$	$M[\Lambda_{00}]$
0	1	$L_{k,0} \oplus K[\Lambda_{01}]$	$M[\Lambda_{01}]$
1	0	$L_{k,0} \oplus K[\Lambda_{10}]$	$M[\Lambda_{10}]$
1	1	$L_{k,0} \oplus K[\Lambda_{11}]$	$M[\Lambda_{11}]$

$$\Lambda_k \cdot \Delta_A := \lambda_k \cdot \Delta_A \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_A$$

$$= \lambda_k \cdot \Delta_A \oplus \dots \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_A$$

Free-XOR GC  $\Rightarrow$   
 $|\Delta_A| = \kappa \approx 128$

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$$a \oplus a \cdot \Delta_B = a \cdot \Delta_B$$

$a, \hat{a}, \Delta_A$   
 $a \quad \hat{a} \quad b \quad \hat{b}$

samples  
 $\mathcal{F}_{pre} \quad [a], [\hat{a}], [b], [\hat{b}]$   
 $\Delta_A, \Delta_B$   
 $\hat{a}_k \oplus \hat{b}_k = \lambda_i \cdot \lambda_j \text{ for } (i, j, k, \Lambda)$

$b, \hat{b}, \Delta_B$   
 $a \quad \hat{a} \quad b \quad \hat{b}$

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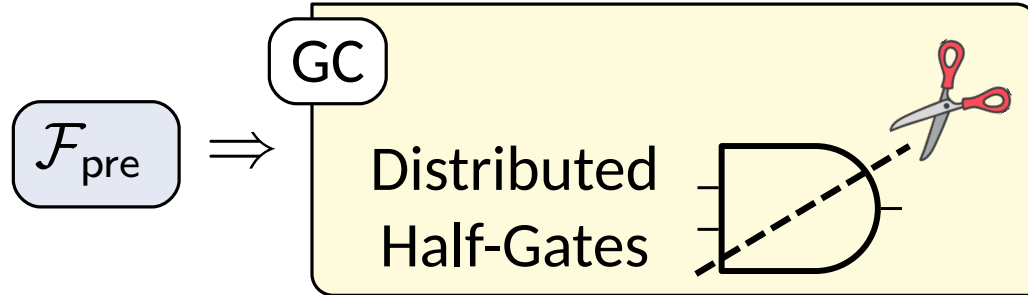
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1	1	$L_{k,0} \oplus M[\Lambda_{11}]$	$K[\Lambda_{11}]$

Free-XOR GC  $\Rightarrow$   
 $|\Delta_A| = \kappa \approx 128$

IT-MAC Soundness  $\Rightarrow$   
 $|\Delta_B| = \rho \approx 40$

# KRRW18: Distributed Half-Gates Garbling + Equality Checking

- Distributed half-gates garbling is fully compatible with  $\mathcal{F}_{\text{pre}}$

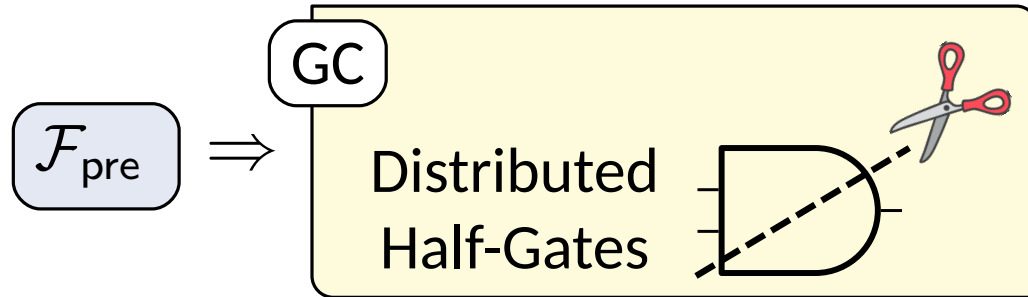


$$\begin{aligned} \Lambda_k \cdot \Delta_A &:= \lambda_k \cdot \Delta_A \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_A \\ &= \underbrace{(\lambda_k \oplus \lambda_i \lambda_j)}_{\text{already shared}} \cdot \Delta_A \oplus \underbrace{\Lambda_i \lambda_j}_{G_{k,0}} \cdot \Delta_A \oplus \underbrace{\Lambda_j (\Lambda_i \oplus \lambda_i)}_{G_{k,1}} \cdot \Delta_A \end{aligned}$$

$4\kappa$ bits/AND WRK17	$\Rightarrow$	$2\kappa + 1$ bits/AND KRRW18
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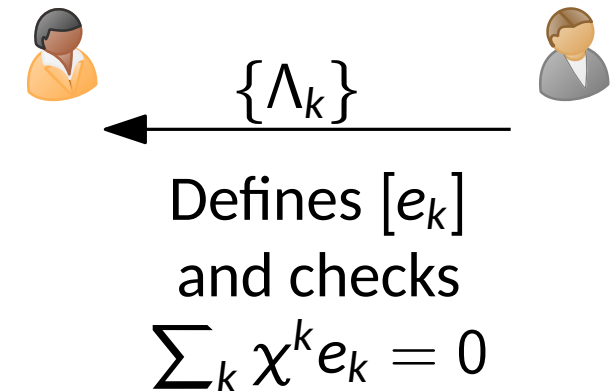
$$4\kappa \text{ bits/AND WRK17} \Rightarrow 2\kappa + 1 \text{ bits/AND KRRW18}$$

- **b**-mask removes selective failure, now only need to check correct AND correlation

$$\Lambda_k \oplus \lambda_k = (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \Leftrightarrow e_k := \Lambda_k \oplus \lambda_k \oplus \Lambda_i \Lambda_j \oplus \Lambda_i \lambda_j \oplus \lambda_i \Lambda_j \oplus \lambda_i \lambda_j = 0$$

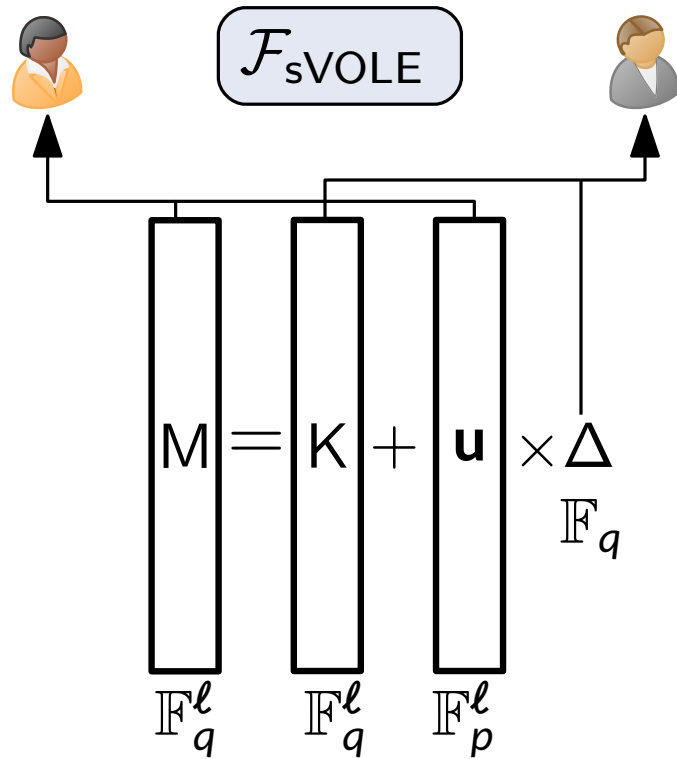
Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.



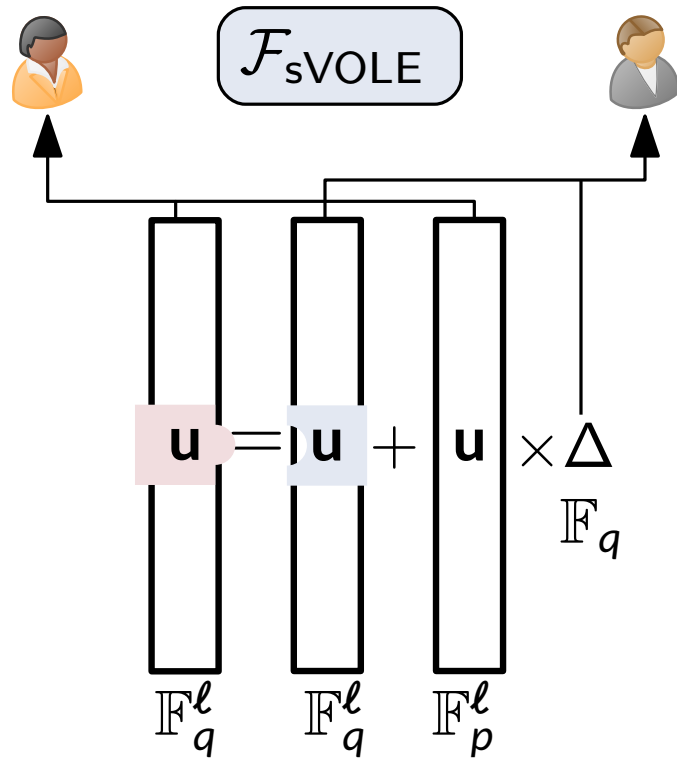
$$4\rho \text{ bits/AND WRK17} \Rightarrow 0 \text{ bits/AND KRRW18}$$

# Efficient COT/sVOLE and Designated Verifier Zero Knowledge

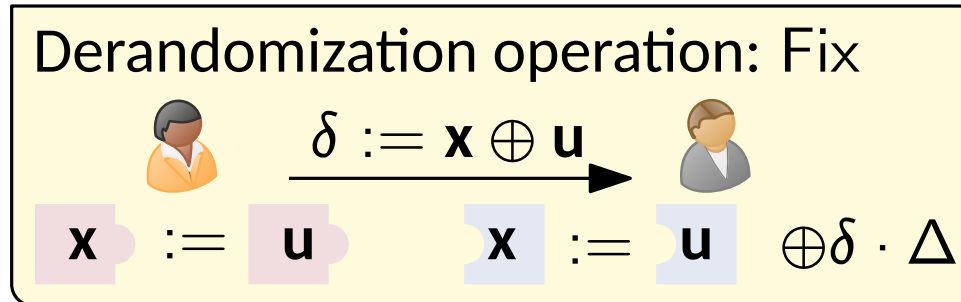


- Efficient protocol for  $\mathcal{F}_{COT}, \mathcal{F}_{sVOLE}$  with sublinear comm. and linear comp. from LPN [YWL+20, CRR21, ...]
- We refer the  $\mathbb{F}_p = \mathbb{F}_2$  variant of  $\mathcal{F}_{sVOLE}$  as  $\mathcal{F}_{COT}$

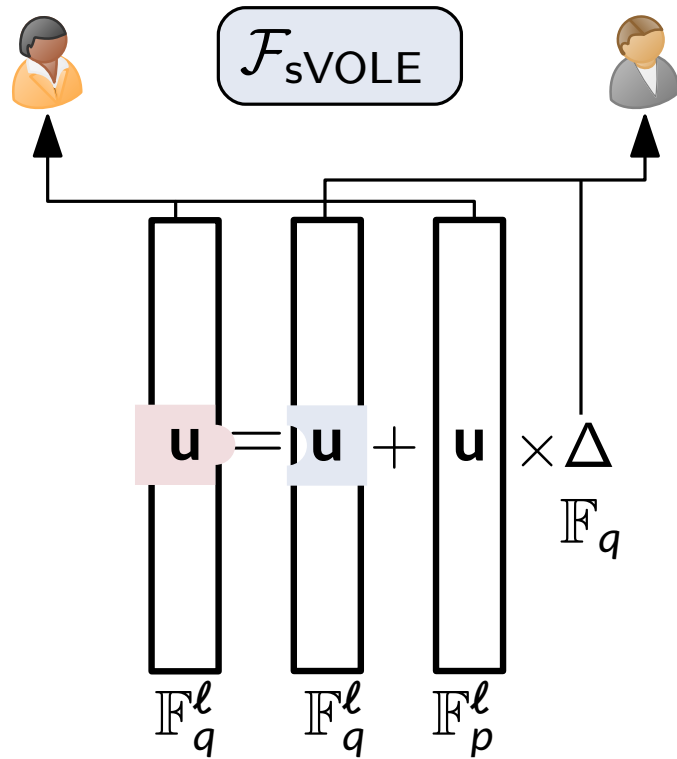
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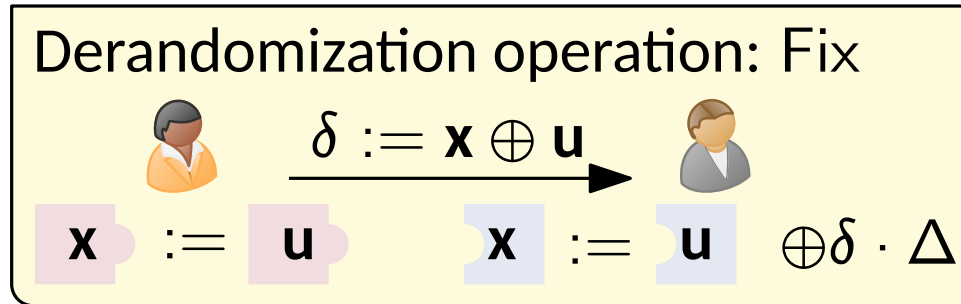
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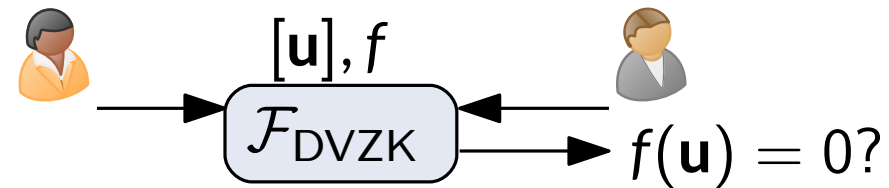
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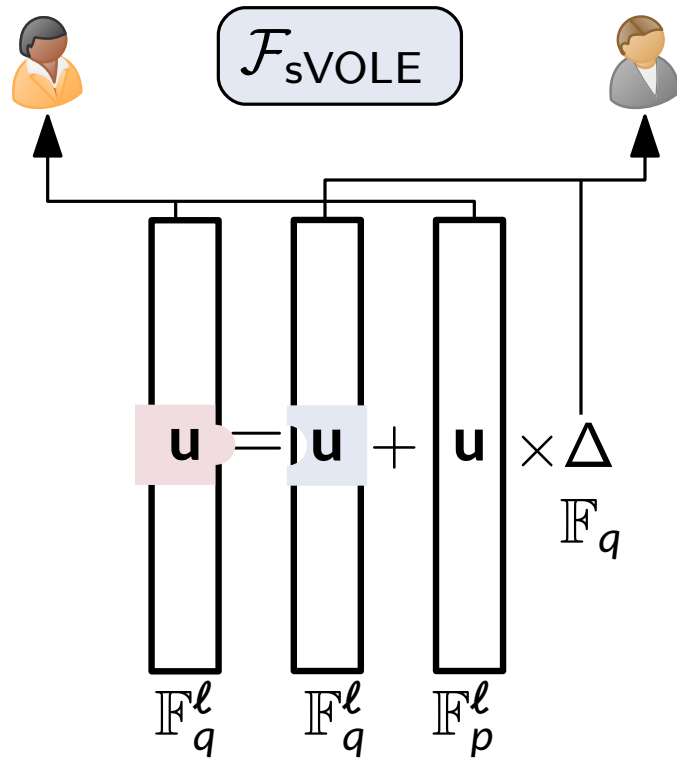


- Efficient proof for deg- $d$  relations on  $\mathbf{u}$  [DIO21, YSWW21, ...]

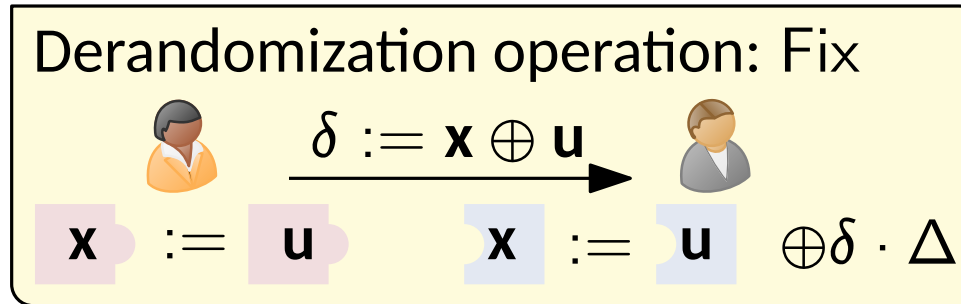




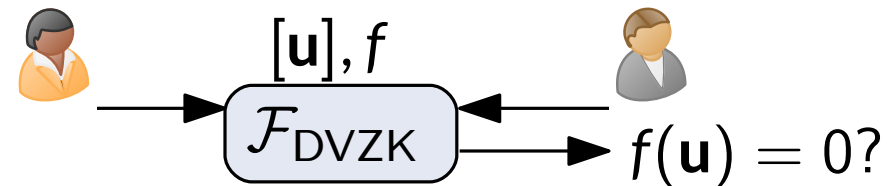
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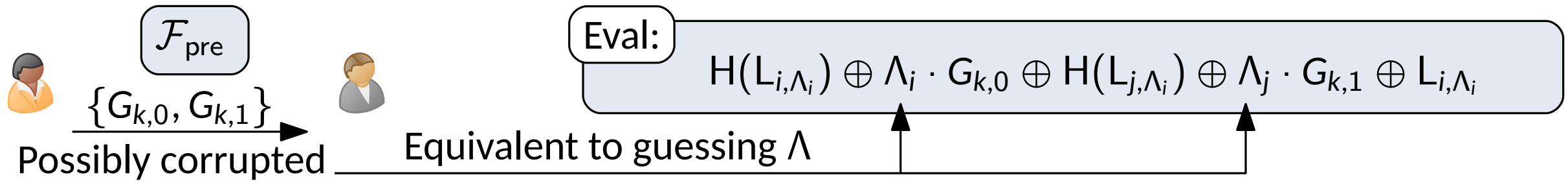


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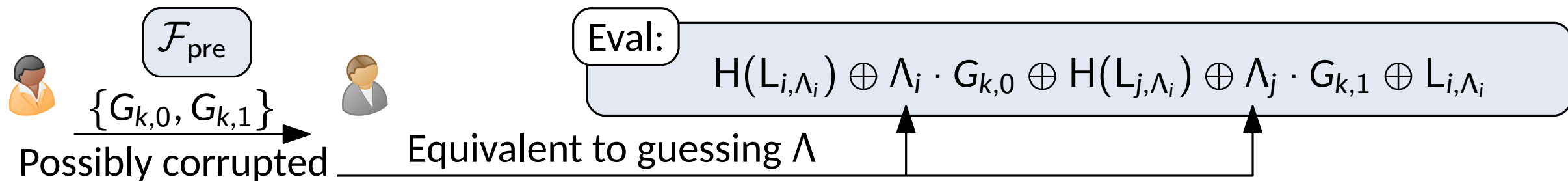
- In DILO, those PCG correlations are called “simple correlations”
- Unfortunately, we still don’t have an efficient direct  $\mathcal{F}_{\text{pre}}$  PCG construction
- The closest is the  $\mathcal{F}_{\text{DAMT}}$  correlation generated from Ring-LPN, but with  $\rho$ -time overhead

# Prior Art: DILO



- Garbler can only guess once
- If  $\mathbf{b}$  is uniformly random, then guessing leaks no information
- If #Guess is too large, then abort happens overwhelmingly, if #Guess is too little, then we don't require much entropy from  $\mathbf{b}$

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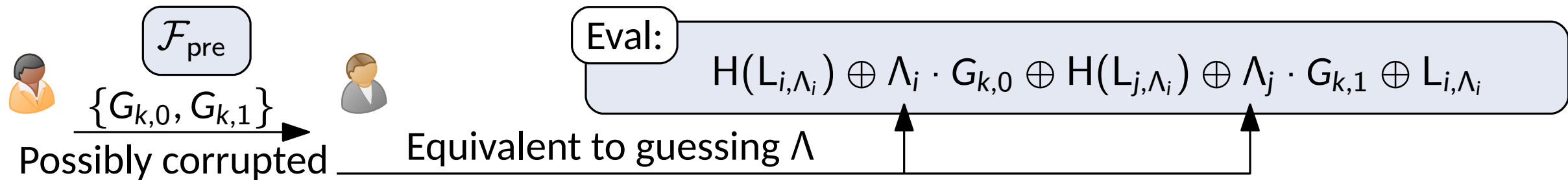
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It suffices for  $\mathbf{b}$  to be  $\rho$ -wise independent

- #Guess  $\leq \rho$ : Abort is input-independent
- #Guess  $> \rho$ : Abort is overwhelming

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## DILO Observation 2

We can construct  $\rho$ -wise independent  $\mathbf{b}$  by linear expansion

$$\mathbf{b} = \mathbf{M} \times \mathbf{b}^*$$

- For  $L = O(\rho \cdot \log(\frac{n}{\rho}))$ , a uniformly random  $\mathbf{M}$  suffices
- We can encode  $\mathbf{b}^*$  in  $\mathcal{F}_{\text{COT}}$  global keys

# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Encoding $\mathbf{b}^*$ as Global Keys

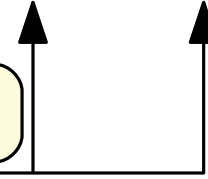
$\mathcal{F}_{\text{pre}}$

samples  
 $[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}]$   
 $\Delta_A, \Delta_B$

$$\begin{aligned} \text{s.t. } \hat{a}_k \oplus \hat{b}_k &= (a_i \oplus b_i) \cdot (a_j \oplus b_j) \\ &= a_i a_j \oplus a_i b_j \oplus a_j b_i \oplus b_i b_j \end{aligned}$$

DILO Compression:  
 $\mathbf{b} = \mathbf{M} \cdot \mathbf{b}^*, \mathbf{b}^* \in \mathbb{F}_2^L$

suffices to compute  $\mathbf{a} \otimes \mathbf{b}^*$



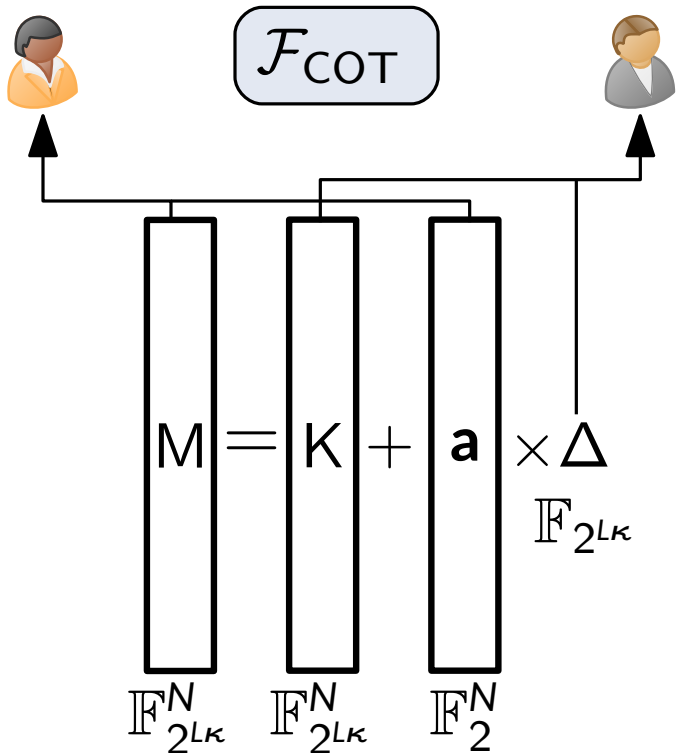
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$\mathcal{F}_{\text{pre}}$  samples  $[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}]$  s.t.  $\hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$   
 $\Delta_A, \Delta_B$   $= a_i a_j \oplus a_i b_j \oplus a_j b_i \oplus b_i b_j$

DILO Compression:  
 $\mathbf{b} = \mathbf{M} \cdot \mathbf{b}^*, \mathbf{b}^* \in \mathbb{F}_2^L$

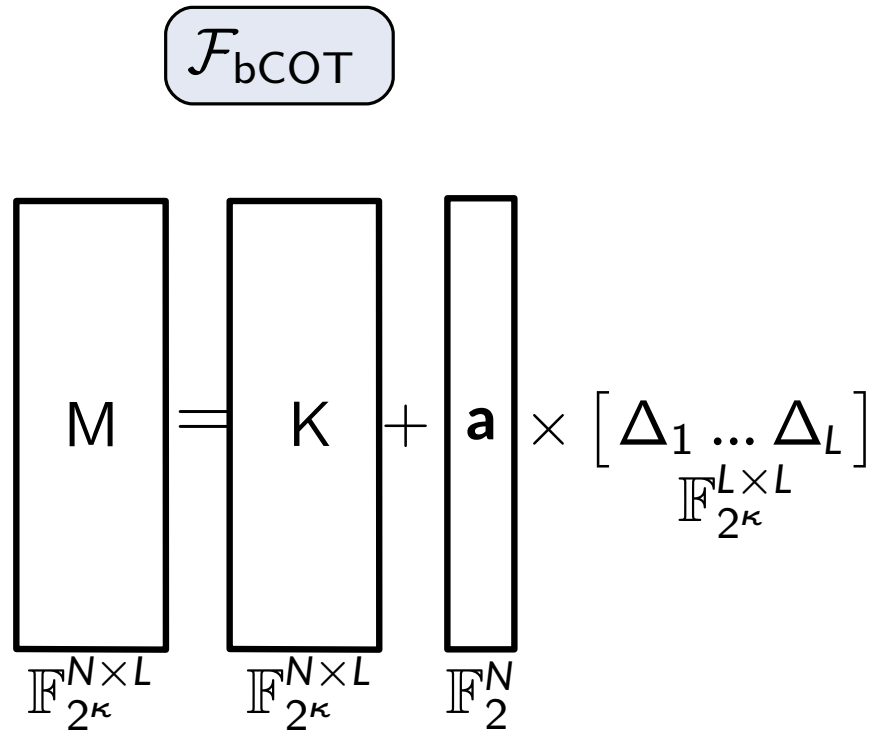
suffices to compute  $\mathbf{a} \otimes \mathbf{b}^*$

- COT can be extended to block COT, preserving PCG efficiency



Using isomorphism  
 $\mathbb{F}_{2^{\kappa L}} \equiv \mathbb{F}_{2^\kappa}^L$

$\Leftrightarrow$



# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



$\mathcal{F}_{\text{bcOT}}$

$$M = K + \begin{array}{|c|} \hline a \\ \hline \hat{a} \\ \hline \end{array}$$



$\mathcal{F}_{\text{COT}}$

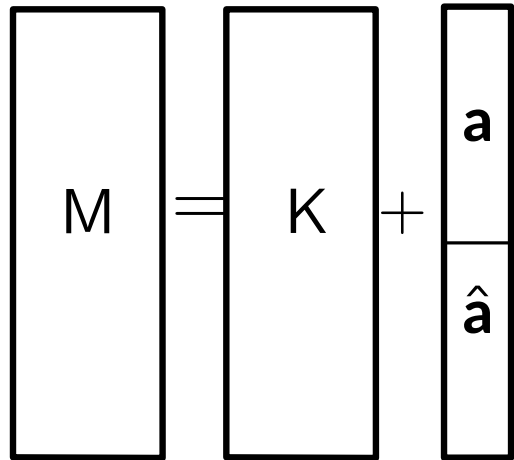
$$M = K + \mathbf{b}^* \times \Delta_A$$

$$\times [b_1^* \cdot \Delta_B, \dots, b_L^* \cdot \Delta_B, \Delta_B]$$

# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$

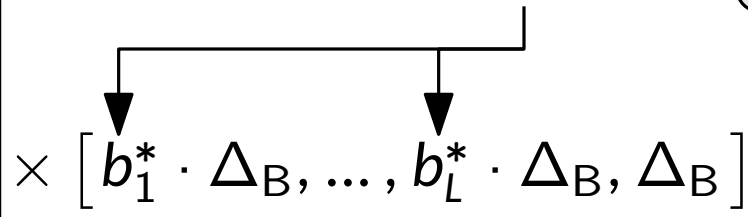


$\mathcal{F}_{\text{bcOT}}$



$\mathcal{F}_{\text{COT}}$

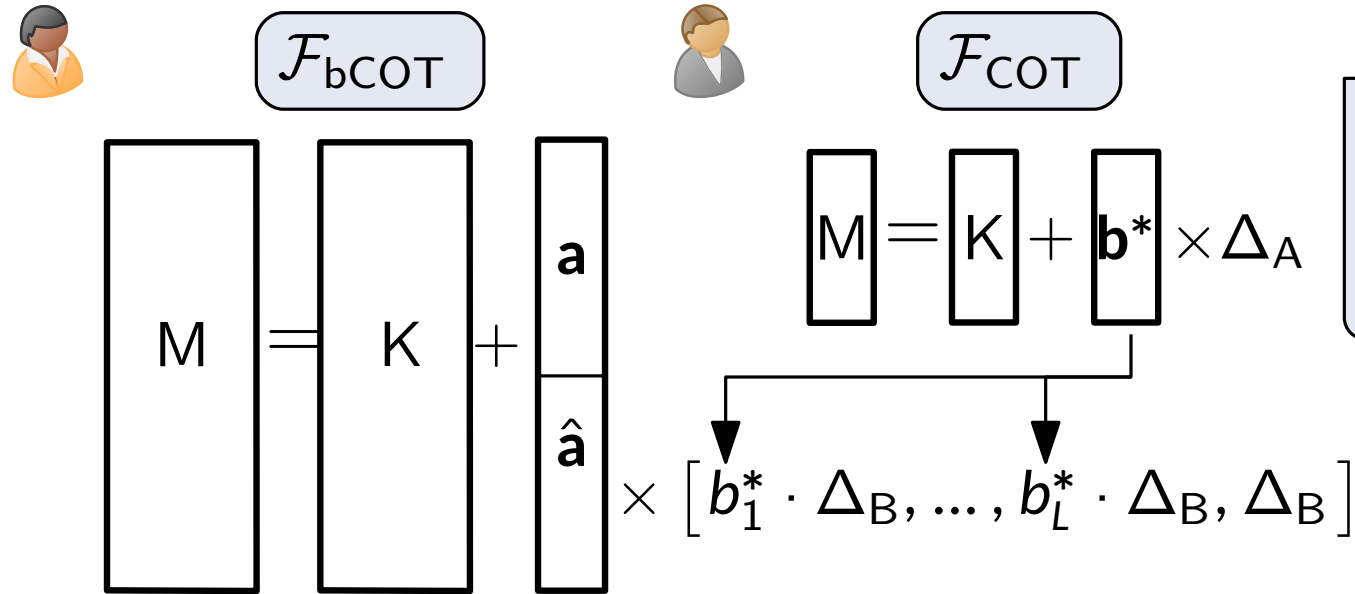
$$M = K + \mathbf{b}^* \times \Delta_A$$



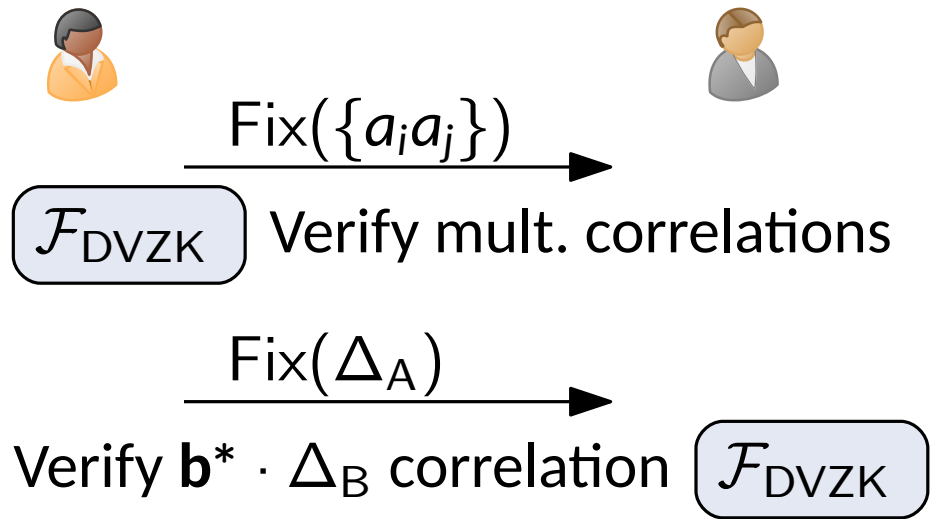
- $[\mathbf{a}]_{b_j^* \Delta_B} \equiv [\mathbf{a}b_j^*]_{\Delta_B}$
- By linearity on IT-MAC, we can get  $[a_i b_j]_{\Delta_B}$  for any  $i, j$



# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



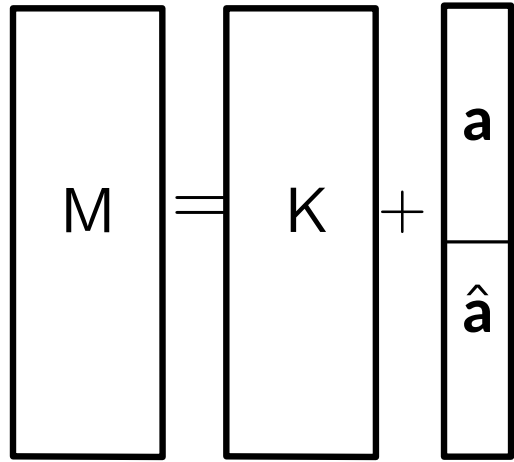
- $[\mathbf{a}]_{b_j^* \Delta_B} \equiv [\mathbf{a}b_j^*]_{\Delta_B}$
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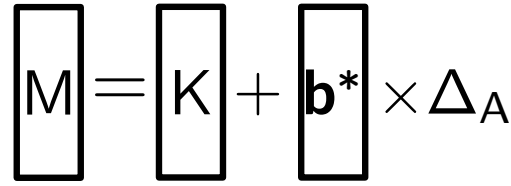
# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



$\mathcal{F}_{\text{bcOT}}$



$\mathcal{F}_{\text{COT}}$



$$\times [b_1^* \cdot \Delta_B, \dots, b_L^* \cdot \Delta_B, \Delta_B]$$

- $[a]_{b_j^* \Delta_B} \equiv [ab_j^*]_{\Delta_B}$
- By linearity on IT-MAC, we can get  $[a_i b_j]_{\Delta_B}$  for any  $i, j$



$\text{Fix}(\{a_i a_j\})$

$\mathcal{F}_{\text{DVZK}}$

Verify mult. correlations



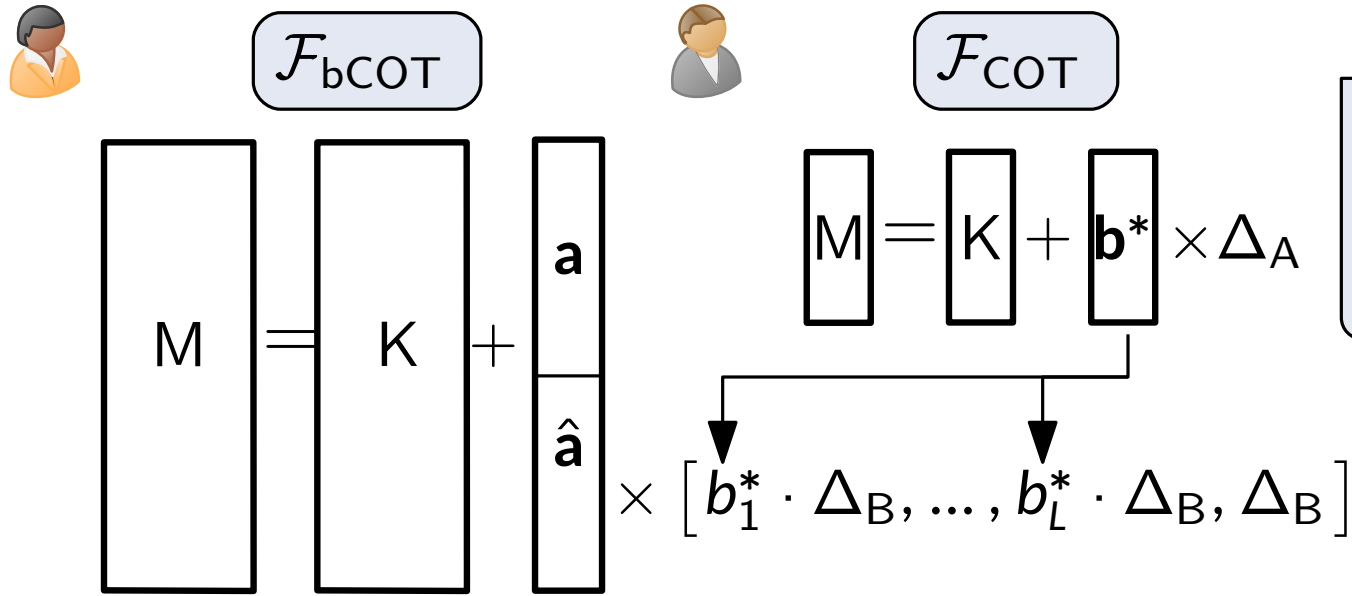
Define  $[\tilde{b}_k]_{\Delta_B} := [\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i]_{\Delta_B}$

$\text{Fix}(\Delta_A)$

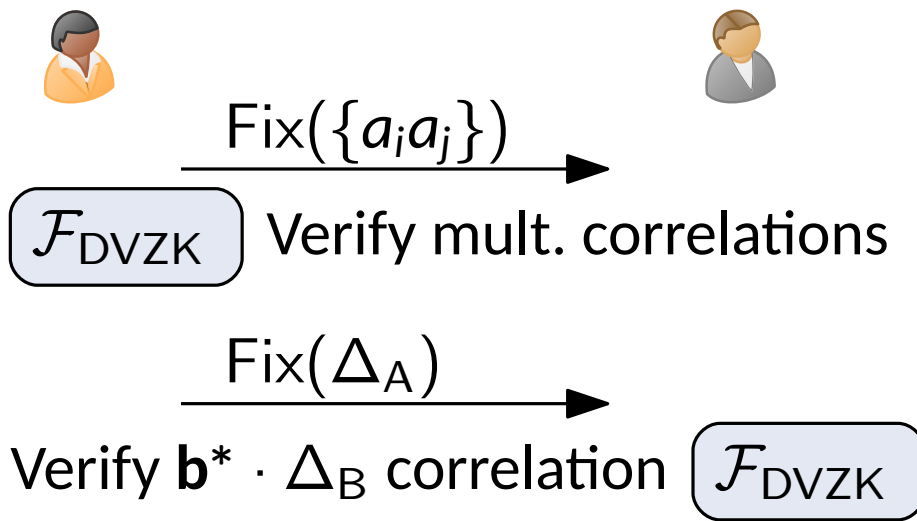
Verify  $b^* \cdot \Delta_B$  correlation

$\mathcal{F}_{\text{DVZK}}$

# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Computing $\hat{b}_k$



- $[a]_{b_j^* \Delta_B} \equiv [ab_j^*]_{\Delta_B}$
- By linearity on IT-MAC, we can get  $[a_i b_j]_{\Delta_B}$  for any  $i, j$

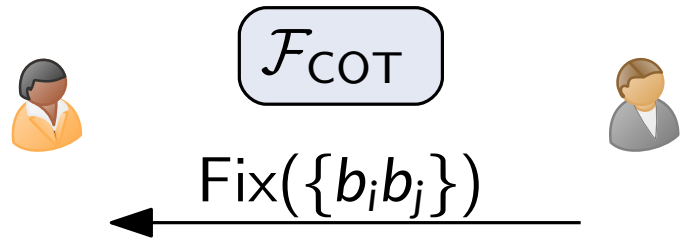


Define  $[\tilde{b}_k]_{\Delta_B} := [\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i]_{\Delta_B}$

$m_{k,1} := \tilde{b}_k$ 
  
 ■  $\tilde{b}_k := (\tilde{b}_k \oplus \tilde{b}_k) \cdot \Delta_B^{-1}$ 
  
 ■  $\hat{b}_k = b_i b_j \oplus \tilde{b}_k$

# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Authenticating $\hat{b}_k$ (Under $\Delta_A$ )

- It suffices to compute  $\tilde{b}_k$  since  $[\hat{b}_k]_{\Delta_A} = [\tilde{b}_k]_{\Delta_A} \oplus [b_i b_j]_{\Delta_A}$

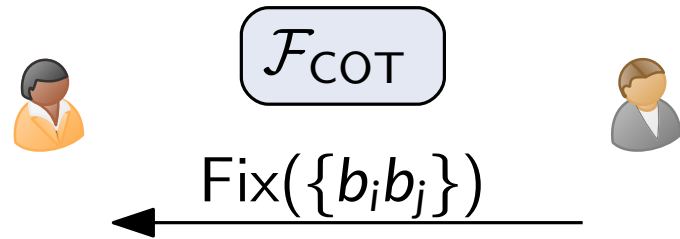


- $\tilde{b}_k = \hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i$
- $\tilde{b}_k \oplus \tilde{b}_k = (\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i) \cdot \Delta_A$

Verify mult. correlation  $\mathcal{F}_{\text{DVZK}}$

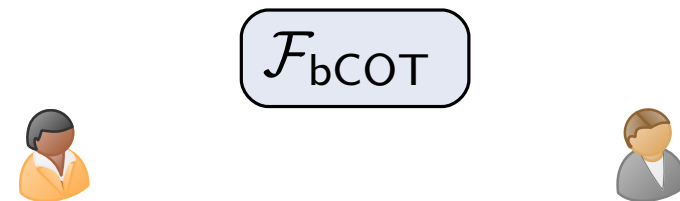
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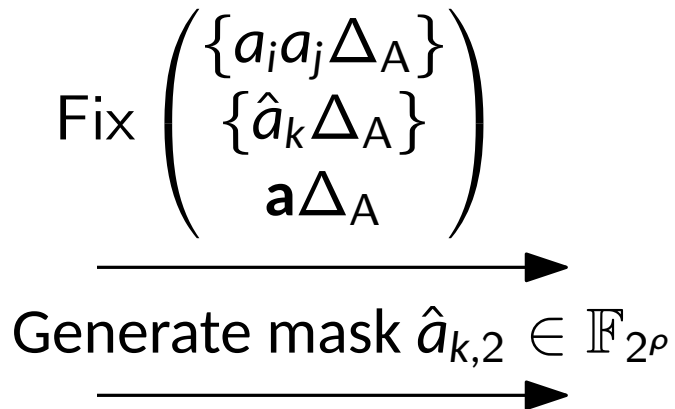


- $\tilde{b}_k = \hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i$
- $\tilde{b}_k \oplus \tilde{b}_k = (\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i) \cdot \Delta_A$

Verify mult. correlation  $\mathcal{F}_{\text{DVZK}}$

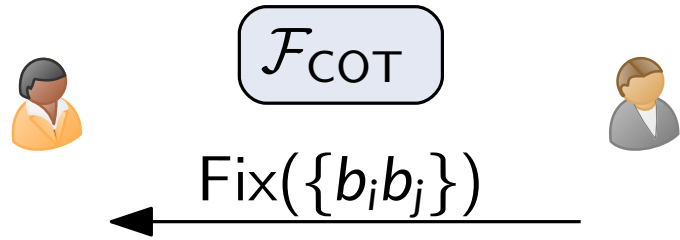


Locally compute  $[v_k]_{\Delta_B} := [\tilde{b}_k \cdot \Delta_A \oplus \hat{a}_{k,2}]_{\Delta_B}$



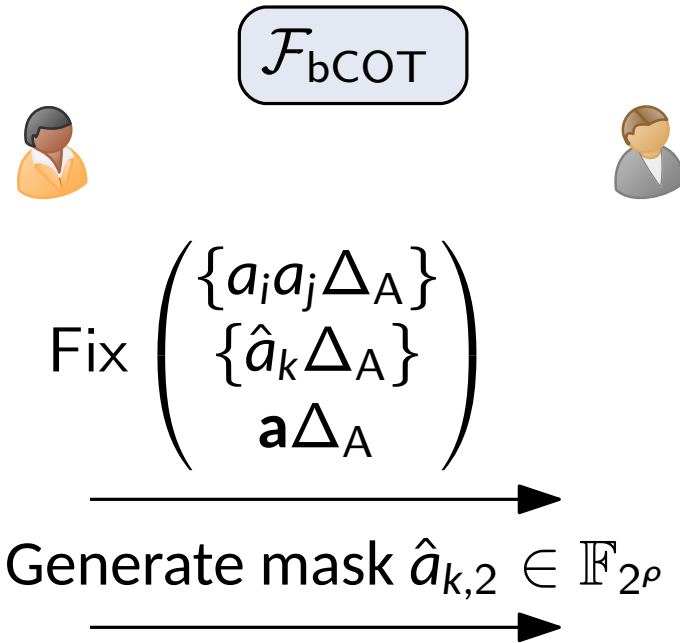
# DILO Implementation of $\mathcal{F}_{\text{cpre}}$ : Authenticating $\hat{b}_k$ (Under $\Delta_A$ )

- It suffices to compute  $\tilde{b}_k$  since  $[\hat{b}_k]_{\Delta_A} = [\tilde{b}_k]_{\Delta_A} \oplus [b_i b_j]_{\Delta_A}$

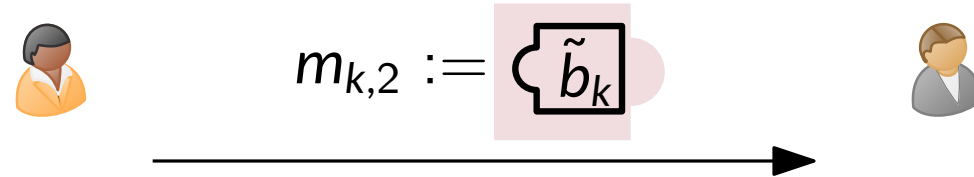


- $\tilde{b}_k = \hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i$
- $[\tilde{b}_k]_{\Delta_A} \oplus [b_i b_j]_{\Delta_A} = (\hat{a}_k \oplus a_i a_j \oplus a_i b_j \oplus a_j b_i) \cdot \Delta_A$

Verify mult. correlation  $\mathcal{F}_{\text{DVZK}}$



Locally compute  $[v_k]_{\Delta_B} := [\tilde{b}_k \cdot \Delta_A \oplus \hat{a}_{k,2}]_{\Delta_B}$

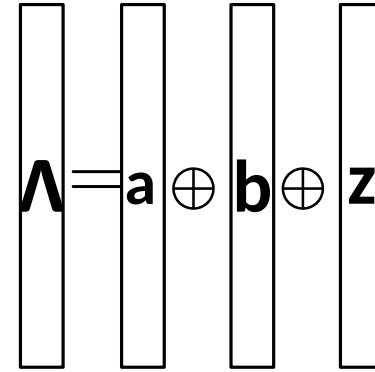


- $[\tilde{b}_k]_{\Delta_B} := ([\tilde{b}_k]_{\Delta_B} \oplus [b_i b_j]_{\Delta_B}) \cdot \Delta_B^{-1}$
- $[\hat{b}_k]_{\Delta_B} = [\tilde{b}_k]_{\Delta_B} \oplus [b_i b_j]_{\Delta_B}$

# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.



The diagram illustrates the KRRW check equation. It features four vertical bars representing variables. The first bar contains the symbol  $\Lambda$ , the second contains  $a$ , the third contains  $b$ , and the fourth contains  $z$ . A horizontal line with an equals sign connects the first bar to the second. This is followed by a plus sign with a circle inside ( $\oplus$ ), then the third bar, another plus sign with a circle inside ( $\oplus$ ), and finally the fourth bar. The entire equation is  $\Lambda = a \oplus b \oplus z$ .

# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates ~~X~~
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.

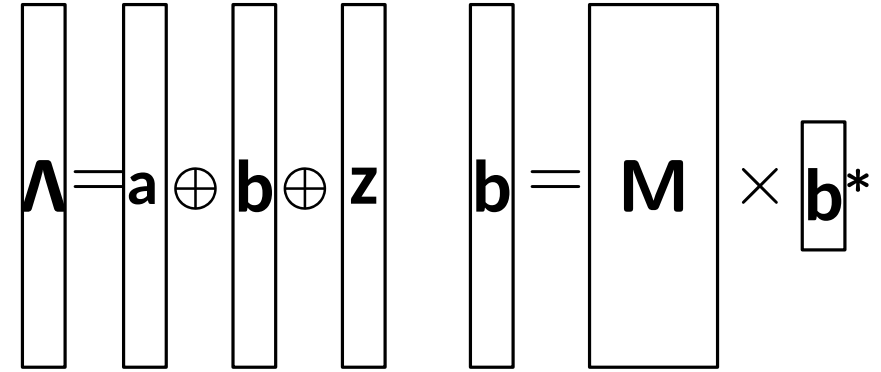
$$\Lambda = a \oplus b \oplus z \quad b = M \times b^*$$



# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates ~~X~~
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.



## DILO-WRK Check

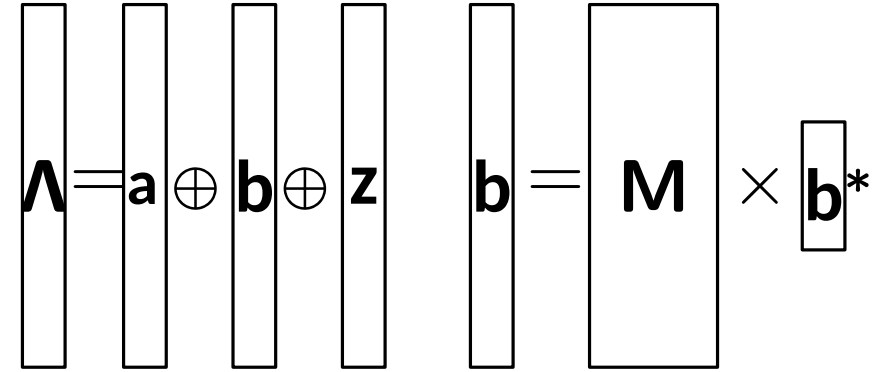
$$\Lambda_k \cdot \Delta_B := \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B$$

$$= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates ~~X~~
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.



## DILO-WRK Check


$$\Lambda_k \cdot \Delta_B := \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B$$

$$\Lambda_i(a_j \oplus b_j)\Delta_B = \Lambda_i b_j \Delta_B \oplus \Lambda_i K[a_j] \oplus \Lambda_i M[a_j]$$

$$= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

# The Online Protocol

## KRRW Check:

- Evaluator sends  $\{\Lambda_w\}$  for all AND gates 
- The checking equation reduces to equality check
- Use random linear combination to reduce comm.

$$\Lambda = a \oplus b \oplus z \quad b = M \times b^*$$

## DILO-WRK Check

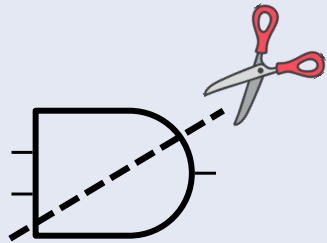
$$\Lambda_k \cdot \Delta_B := \lambda_k \cdot \Delta_B \oplus (\Lambda_i \oplus \lambda_i) \cdot (\Lambda_j \oplus \lambda_j) \cdot \Delta_B$$

$$\Lambda_i(a_j \oplus b_j)\Delta_B = \Lambda_i b_j \Delta_B \oplus \Lambda_i K[a_j] \oplus \Lambda_i M[a_j]$$

$$= \lambda_k \cdot \Delta_B \oplus \Lambda_i \Lambda_j \cdot \Delta_B \oplus \Lambda_i \lambda_j \cdot \Delta_B \oplus \Lambda_j \lambda_i \cdot \Delta_B \oplus (\hat{a}_k \oplus \hat{b}_k) \cdot \Delta_B$$

## GC

Distributed  
Half-Gates



$2\kappa$  bits/AND

## AuthGC

$\Lambda_i$	$\Lambda_j$	Alice's AuthGC	Bob's AuthGC
0	0	$L_{k,0} \oplus M[\Lambda_{00}]$	$K[\Lambda_{00}]$
0	1	$L_{k,0} \oplus M[\Lambda_{01}]$	$K[\Lambda_{01}]$
<del>1</del>	<del>0</del>	<del><math>L_{k,0} \oplus M[\Lambda_{10}]</math></del>	<del><math>K[\Lambda_{10}]</math></del>
1	1	$L_{k,0} \oplus M[\Lambda_{11}]$	$K[\Lambda_{11}]$

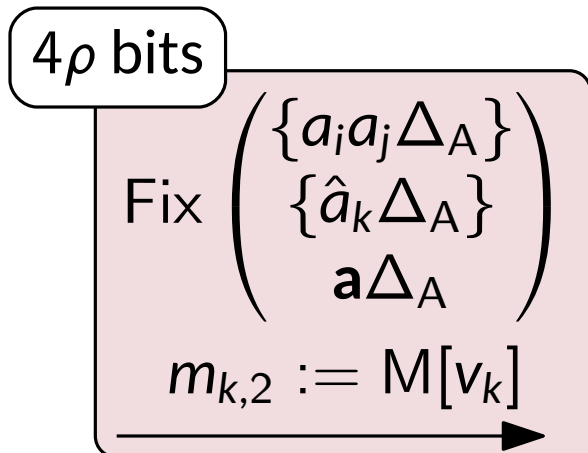
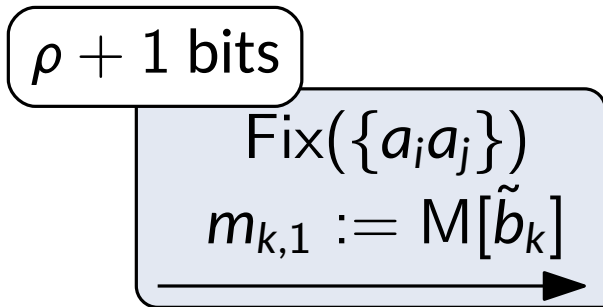
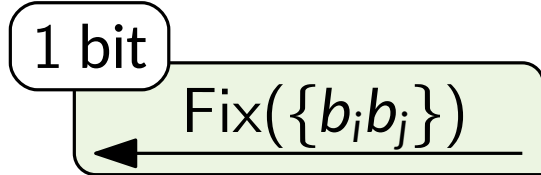
+

$3\rho$  bits/AND

$= 2\kappa + 3\rho$  bits/AND

# Optimizing the Compressed Preprocessing Protocol

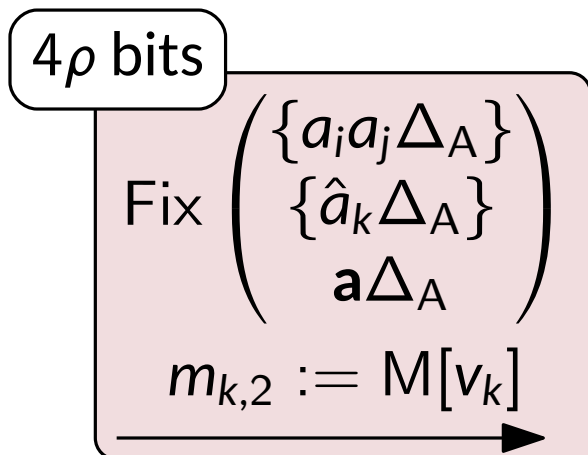
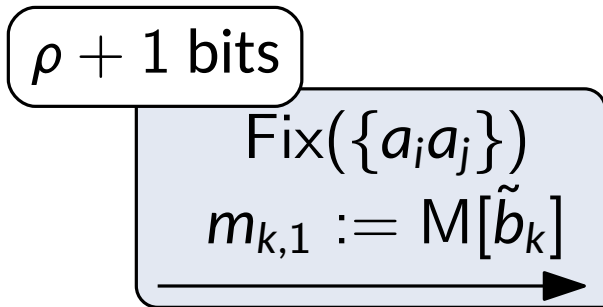
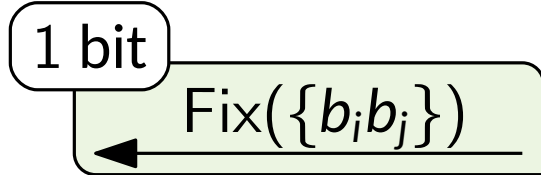
The overhead of DILO is  
 $5\rho + 2$  bits per AND gate



# Optimizing the Compressed Preprocessing Protocol

The overhead of DILO is  $5\rho + 2$  bits per AND gate

- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
- We need to detect against dishonest  $\text{Fix}()$  input



# Optimizing the Compressed Preprocessing Protocol

The overhead of DILO is  $5\rho + 2$  bits per AND gate



1 bit

$\text{Fix}(\{b_i b_j\})$

$\rho + 1$  bits

$\text{Fix}(\{a_i a_j\})$

$m_{k,1} := M[\tilde{b}_k]$

$4\rho$  bits

$\text{Fix} \left( \begin{array}{c} \{a_i a_j \Delta_A\} \\ \{\hat{a}_k \Delta_A\} \\ \mathbf{a} \Delta_A \end{array} \right)$

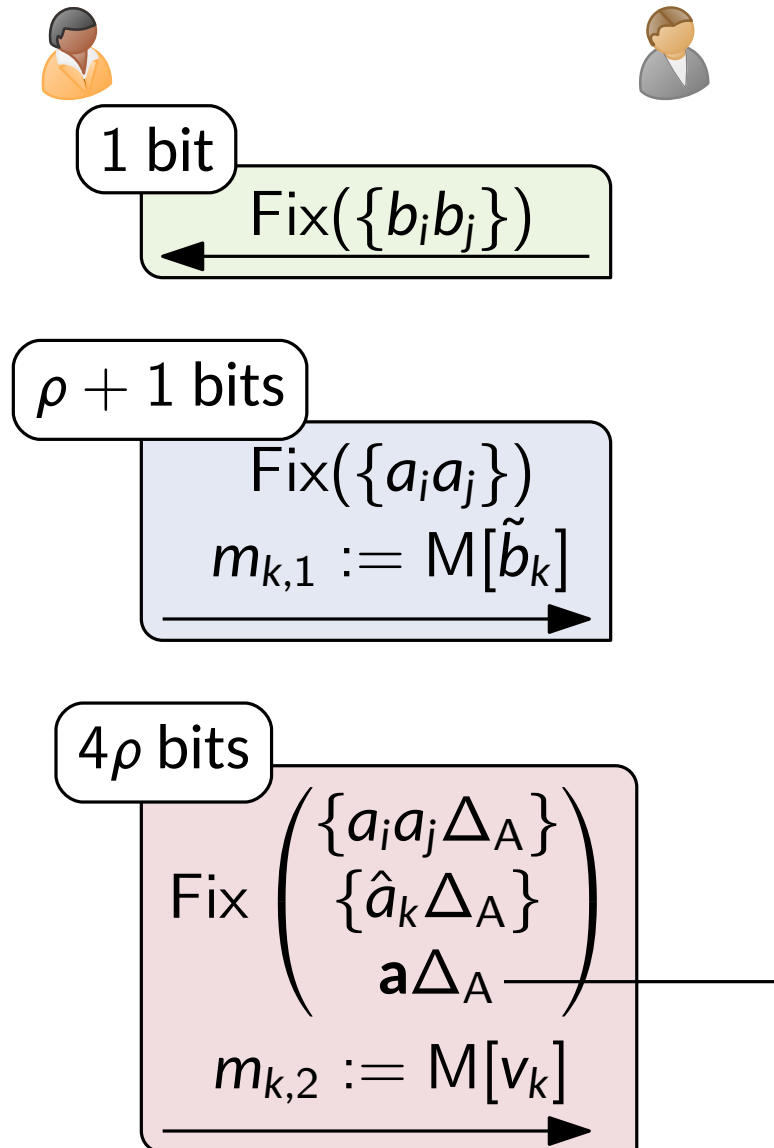
$m_{k,2} := M[v_k]$

- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
- We need to detect against dishonest  $\text{Fix}()$  input
- $[\mathbf{a} \Delta_A]_{\Delta_B} \equiv [\mathbf{a}]_{\Delta_A \cdot \Delta_B}$
- $M[\mathbf{a} \Delta_A] \oplus K[\mathbf{a} \Delta_A] = \mathbf{a} \Delta_A \Delta_B$
- We denote it as  $\langle \mathbf{a} \rangle$

Dual Key Authentication

# Optimizing the Compressed Preprocessing Protocol

The overhead of DILO is  $5\rho + 2$  bits per AND gate



- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
- We need to detect against dishonest  $\text{Fix}()$  input

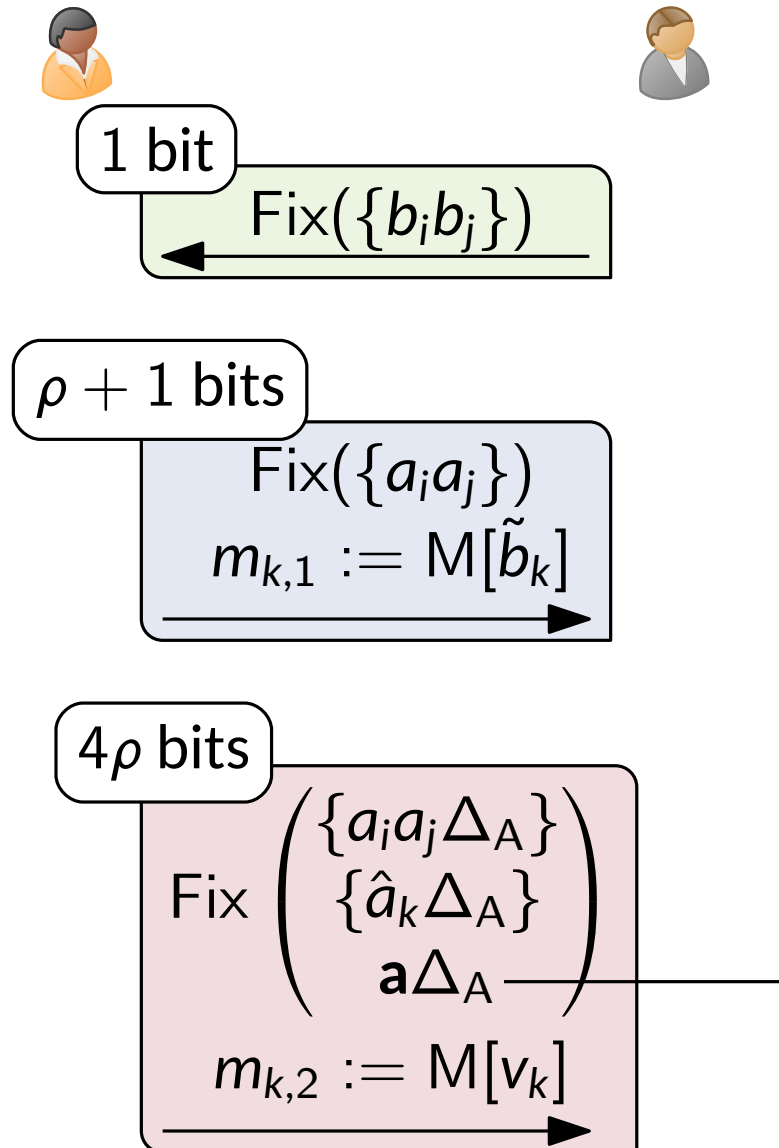
Dual Key Authentication

- $[\mathbf{a} \Delta_A]_{\Delta_B} \equiv [\mathbf{a}]_{\Delta_A \cdot \Delta_B}$
- $M[\mathbf{a} \Delta_A] \oplus K[\mathbf{a} \Delta_A] = \mathbf{a} \Delta_A \Delta_B$
- We denote it as  $\langle \mathbf{a} \rangle$

- Suppose we generate  $\langle \tilde{b}_k \rangle$  and  $\langle r \rangle, [r]_B$  (mask for Bob)
- Bob can open  $y := \sum_k \chi^k \cdot \tilde{b}_k \oplus r$  and convince Alice
- Bob calls  $\text{Fix}(\tilde{b}_k)$  and checks  $\sum_k \chi^k [\tilde{b}_k] \oplus [r] \oplus y = 0$

# Optimizing the Compressed Preprocessing Protocol





The overhead of DILO is  $5\rho + 2$  bits per AND gate



- Why not call  $\text{Fix}(\tilde{b}_k)$  directly?
- We need to detect against dishonest  $\text{Fix}()$  input

## Dual Key Authentication

- $[\mathbf{a}\Delta_A]_{\Delta_B} \equiv [\mathbf{a}]_{\Delta_A \cdot \Delta_B}$
- $M[\mathbf{a}\Delta_A] \oplus K[\mathbf{a}\Delta_A] = \mathbf{a}\Delta_A \Delta_B$
- We denote it as  $\langle \mathbf{a} \rangle$

- Suppose we generate  $\langle \tilde{b}_k \rangle$  and  $\langle r \rangle, [r]_B$  (mask for )
-  can open  $y := \sum_k \chi^k \cdot \tilde{b}_k \oplus r$  and convince 
-  calls  $\text{Fix}(\tilde{b}_k)$  and checks  $\sum_k \chi^k [\tilde{b}_k] \oplus [r] \oplus y = 0$

If so we can reduce  $4\rho$  bits to 1 bit

Our goal is to generate  $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$



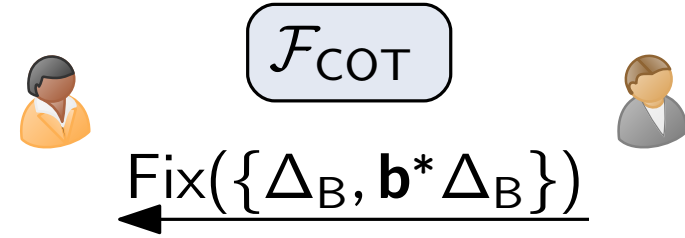
# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{\mathbf{b}}_k \rangle := \langle \hat{\mathbf{a}}_k \rangle \oplus \langle \mathbf{a}_i \mathbf{a}_j \rangle \oplus \langle \mathbf{a}_i \mathbf{b}_j \rangle \oplus \langle \mathbf{a}_j \mathbf{b}_i \rangle$

- $D_A[\hat{\mathbf{a}}_k] \oplus D_B[\hat{\mathbf{a}}_k] = \hat{\mathbf{a}}_k \Delta_A \Delta_B$

- $D_A[\mathbf{a}_i \mathbf{b}_j] \oplus D_B[\mathbf{a}_i \mathbf{b}_j] = \mathbf{a}_i \mathbf{b}_j \Delta_A \Delta_B$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bCOT}}$  global keys



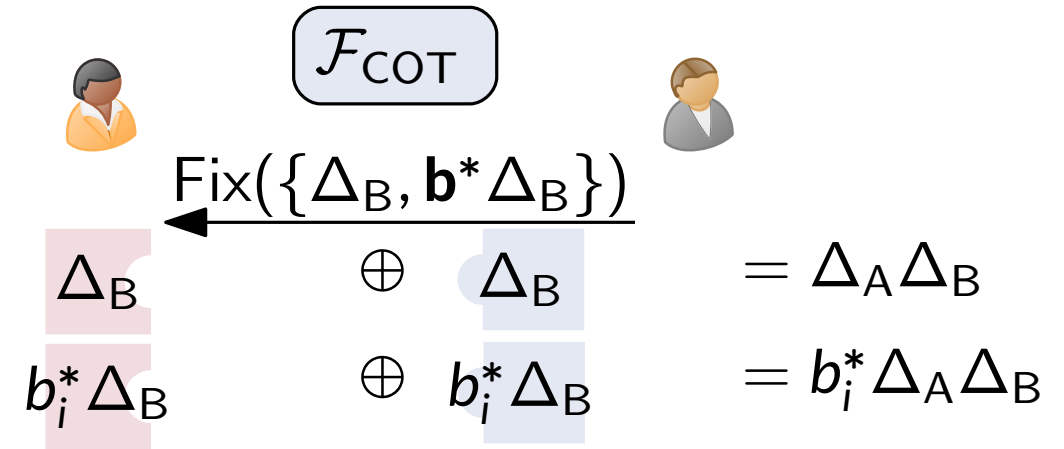
# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bcOT}}$  global keys



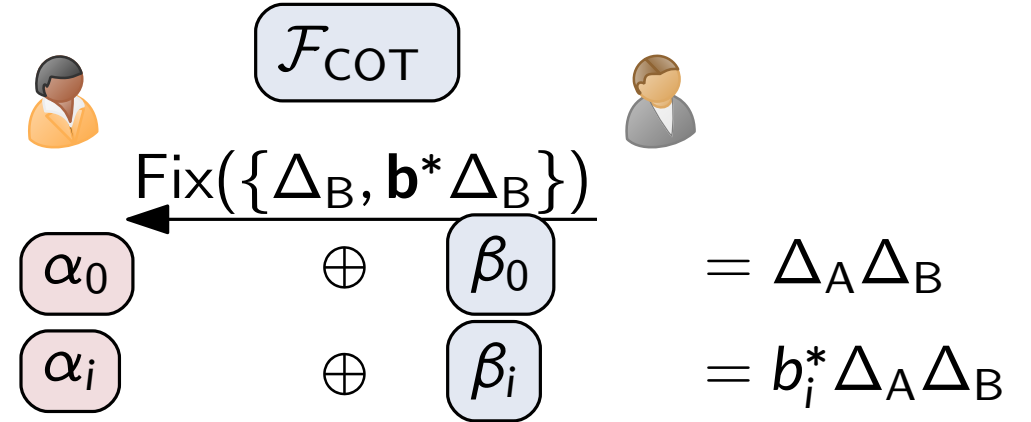
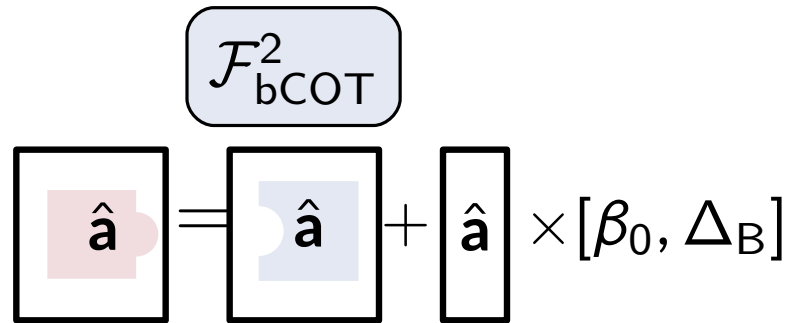
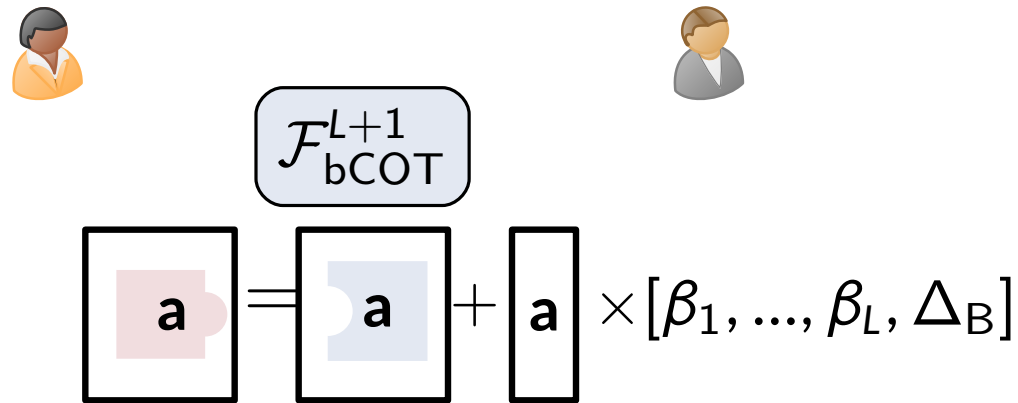
# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bcOT}}$  global keys



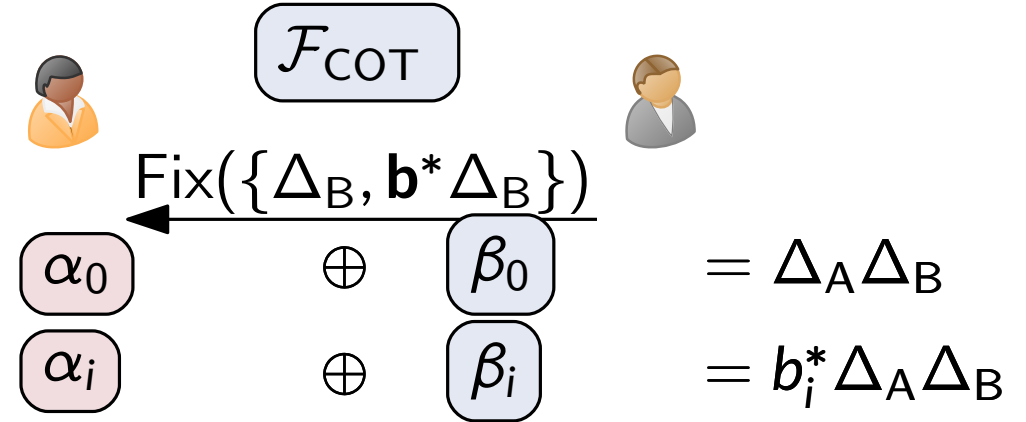
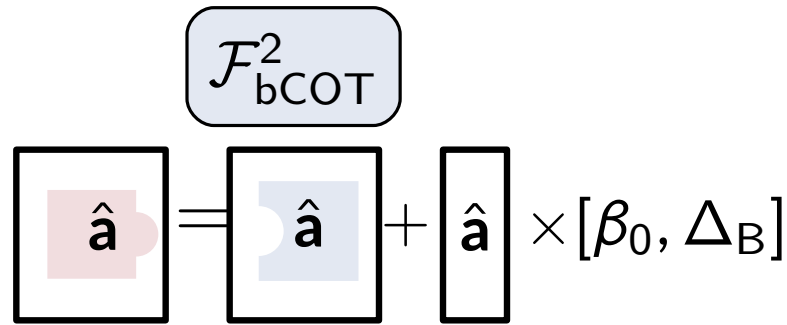
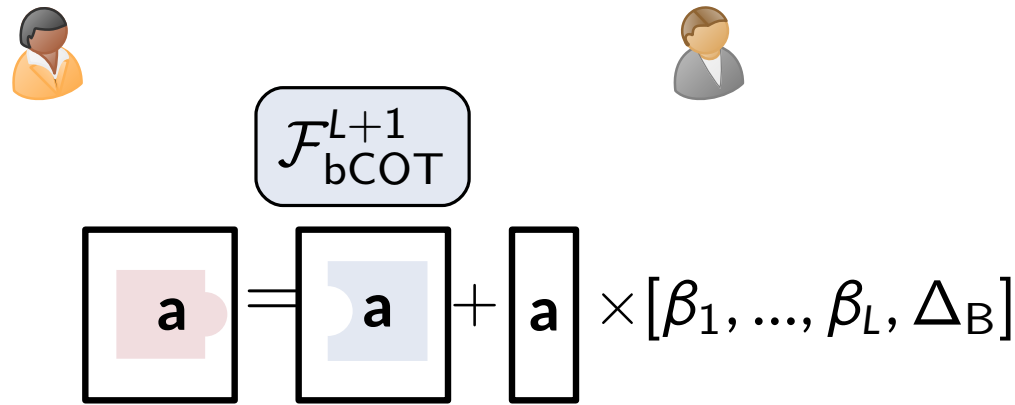
# Optimizing the Compressed Preprocessing Protocol, Continued

- $\langle \tilde{b}_k \rangle := \langle \hat{a}_k \rangle \oplus \langle a_i a_j \rangle \oplus \langle a_i b_j \rangle \oplus \langle a_j b_i \rangle$

- $D_A[\hat{a}_k] \oplus D_B[\hat{a}_k] = \hat{a}_k \Delta_A \Delta_B$

- $D_A[a_i b_j] \oplus D_B[a_i b_j] = a_i b_j \Delta_A \Delta_B$

The compression technique allows encoding  $\mathbf{b}$  in  $\mathcal{F}_{\text{bcOT}}$  global keys



$$\Leftrightarrow \begin{cases} \alpha_0 \cdot \Delta_A^{-1} \oplus \beta_0 \cdot \Delta_A^{-1} = \Delta_B \\ \alpha_i \cdot \Delta_A^{-1} \oplus \beta_i \cdot \Delta_A^{-1} = b_i^* \Delta_B \end{cases}$$

By  $\text{Fix}(\Delta'_A)$

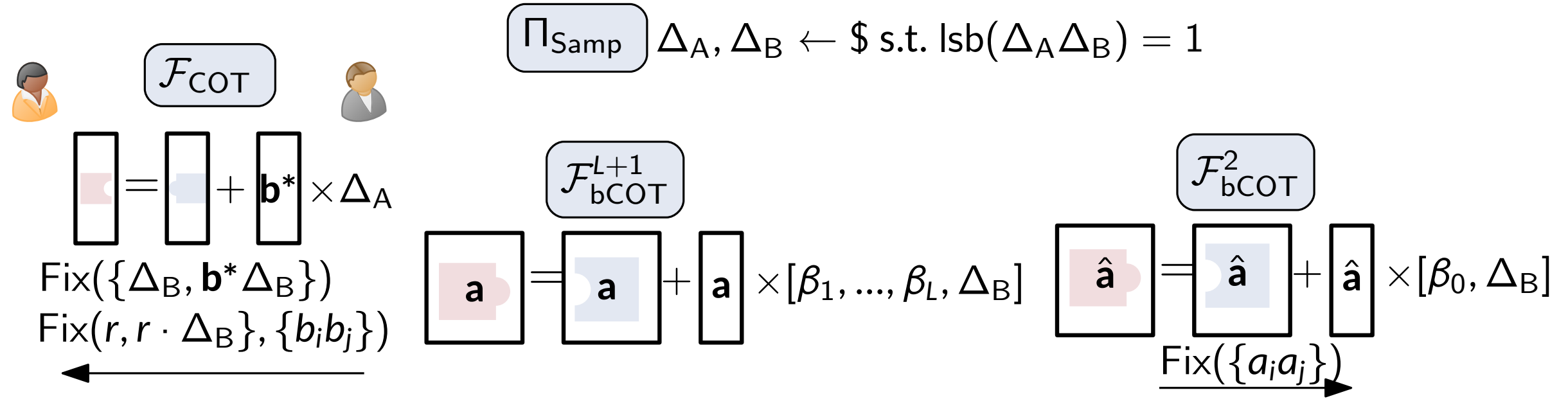
$$\begin{cases} K[\beta_0] \oplus \beta_0 \cdot \Delta'_A = M[\beta_0] \\ K[\beta_i] \oplus \beta_i \cdot \Delta'_A = M[\beta_i] \end{cases}$$

[DIO21] gives a modular way of proving equality under independent keys

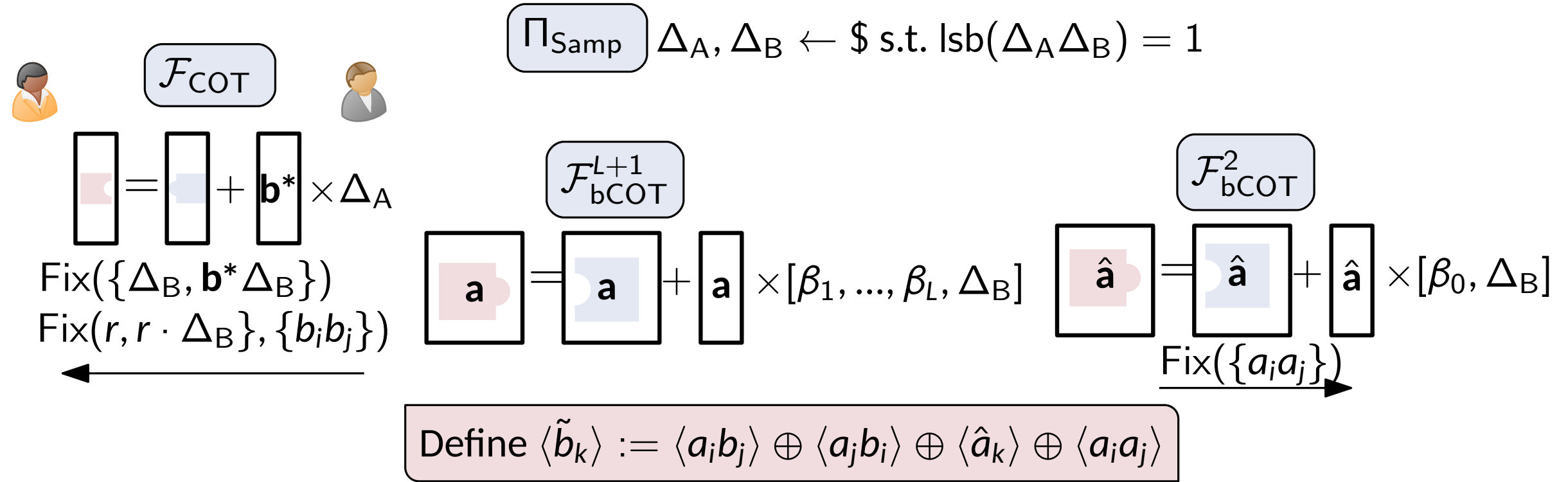
# Optimizing the Compressed Preprocessing Protocol, Completed

$$\prod_{\text{Samp}} \Delta_A, \Delta_B \leftarrow \$ \text{ s.t. } \text{lsb}(\Delta_A \Delta_B) = 1$$

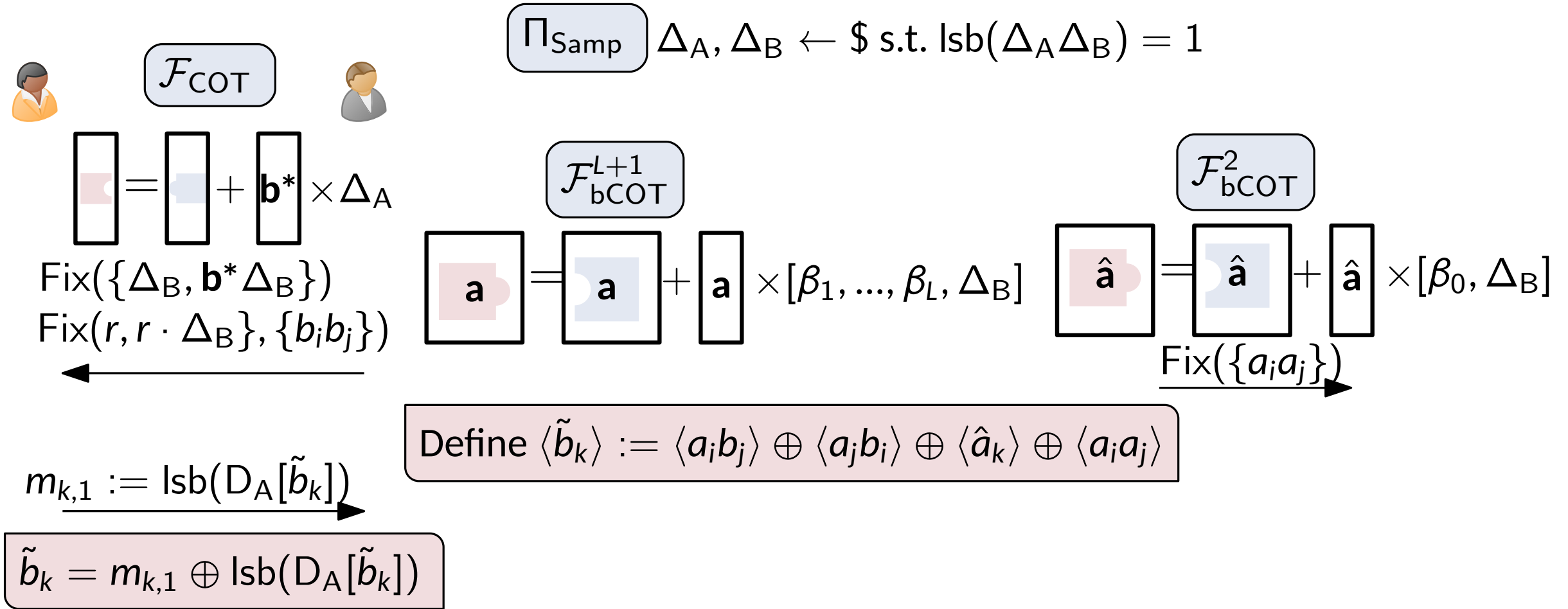
# Optimizing the Compressed Preprocessing Protocol, Completed



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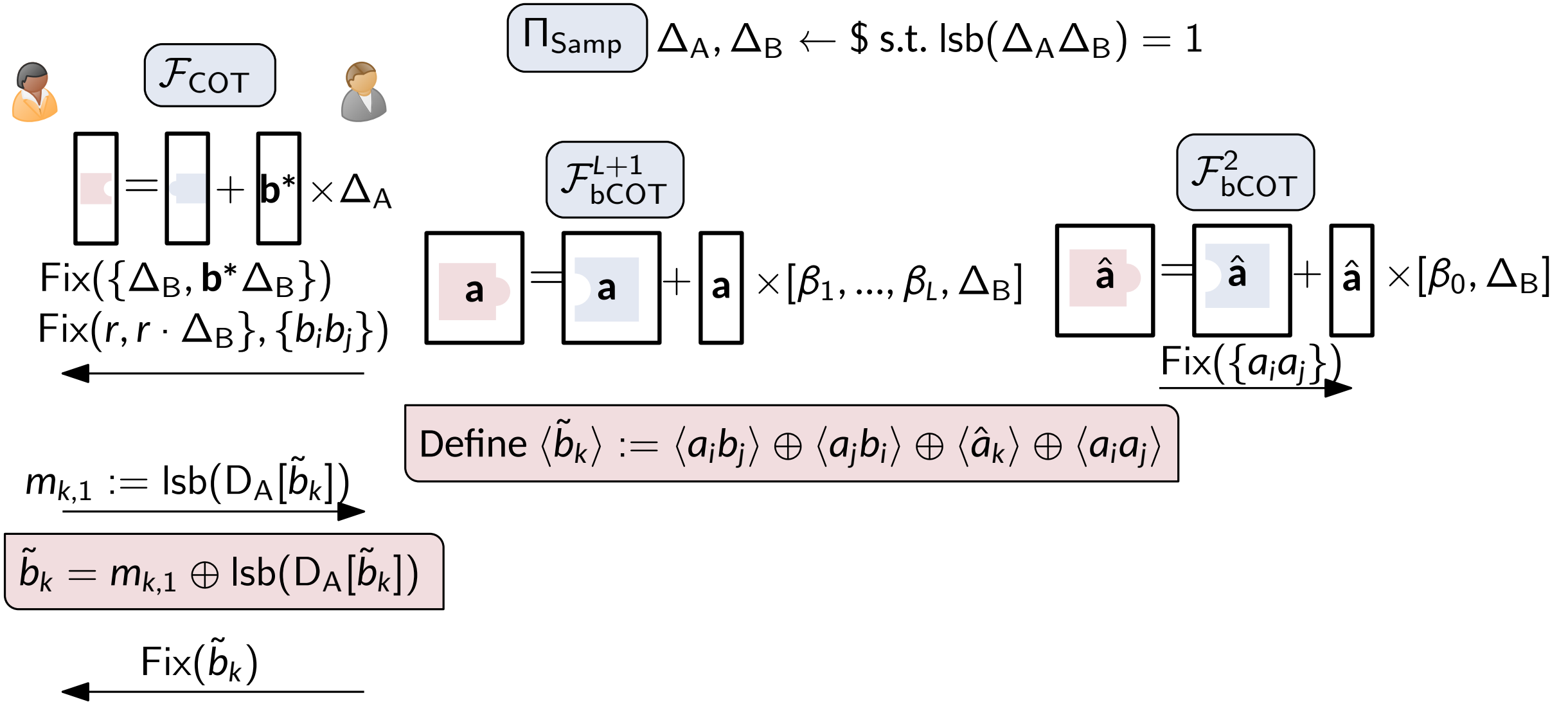


# Optimizing the Compressed Preprocessing Protocol, Completed

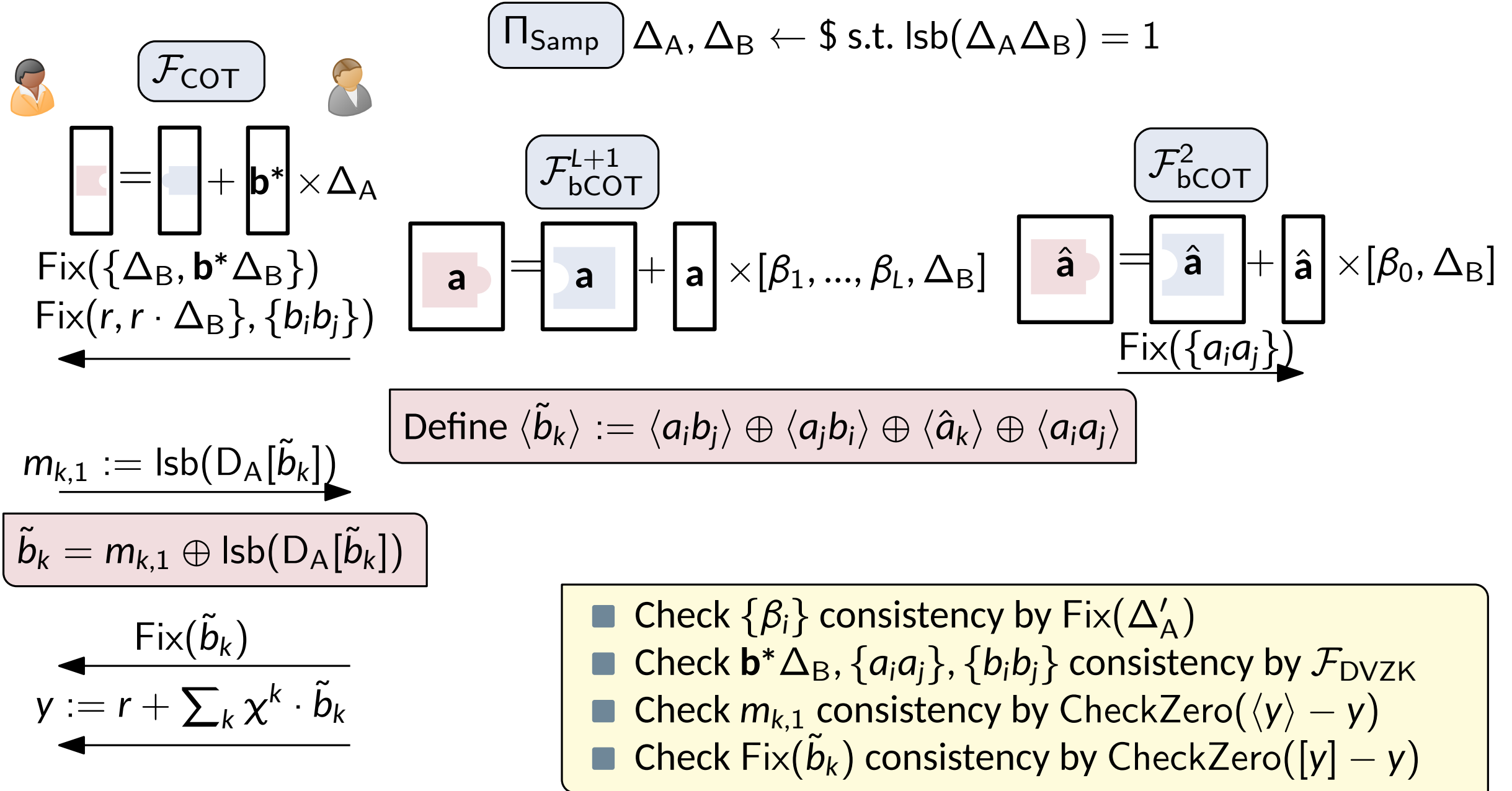








# Optimizing the Compressed Preprocessing Protocol, Completed







# Optimizing the Compressed Preprocessing Protocol, Completed

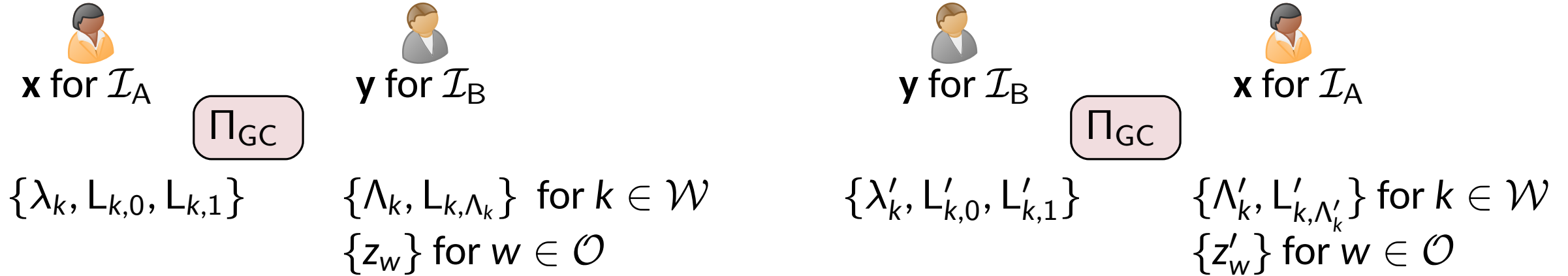


# Optimizing the One-way Communication Via Dual Execution

- Optimized  $\mathcal{F}_{\text{cpre}}$  + DILO-WRK =   $\rightarrow$  :  $2\kappa + 3\rho + 2$  bits,   $\rightarrow$  : 2 bits
- How about optimizing one-way communication? Maybe dual execution?

# Optimizing the One-way Communication Via Dual Execution

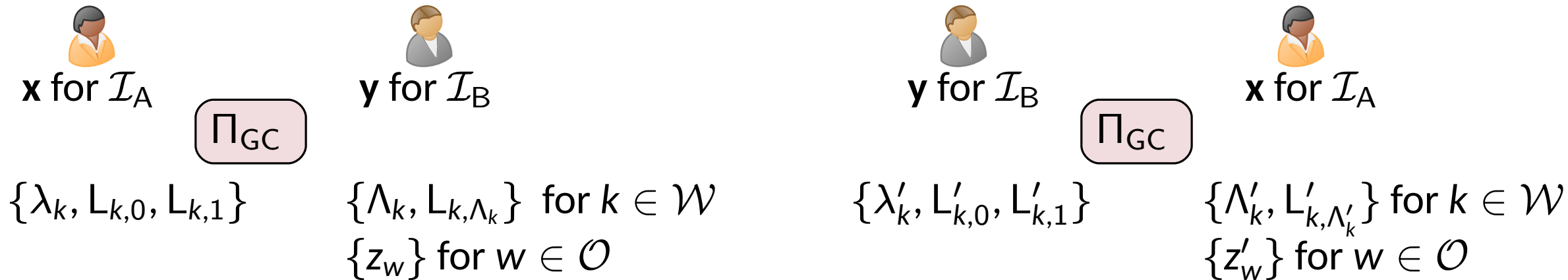
- Optimized  $\mathcal{F}_{\text{cpre}}$  + DILO-WRK =   $\rightarrow$   :  $2\kappa + 3\rho + 2$  bits,   $\rightarrow$   : 2 bits
- How about optimizing one-way communication? Maybe dual execution?



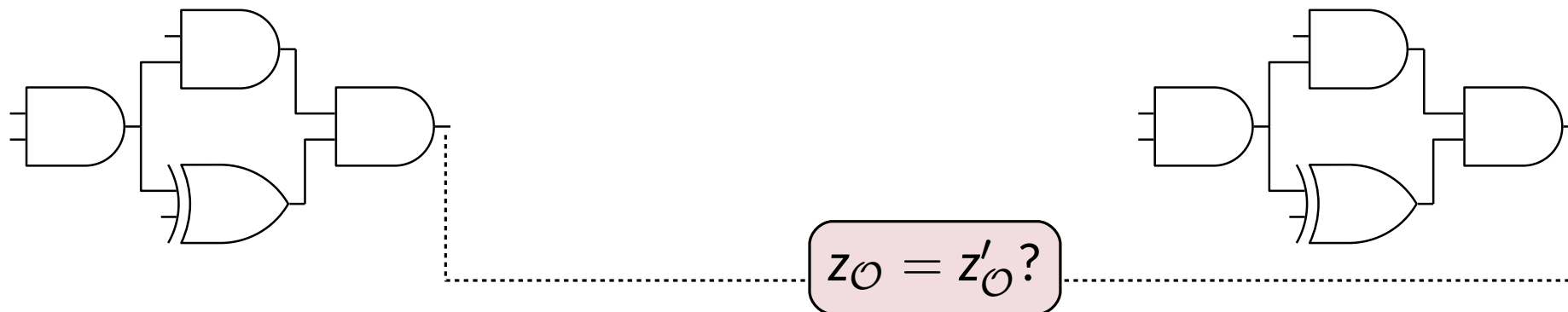
# Optimizing the One-way Communication Via Dual Execution

■ Optimized  $\mathcal{F}_{\text{cpre}} + \text{DILO-WRK} = \text{Person A} \rightarrow \text{Person B} : 2\kappa + 3\rho + 2 \text{ bits}, \text{Person B} \rightarrow \text{Person A} : 2 \text{ bits}$

■ How about optimizing one-way communication? Maybe dual execution?



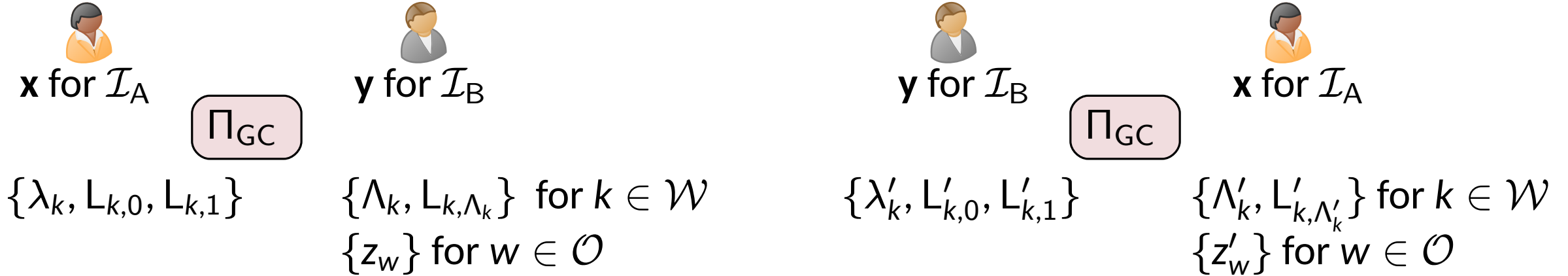
■ [HEK12, HsV20]: Check for equality in circuit outputs



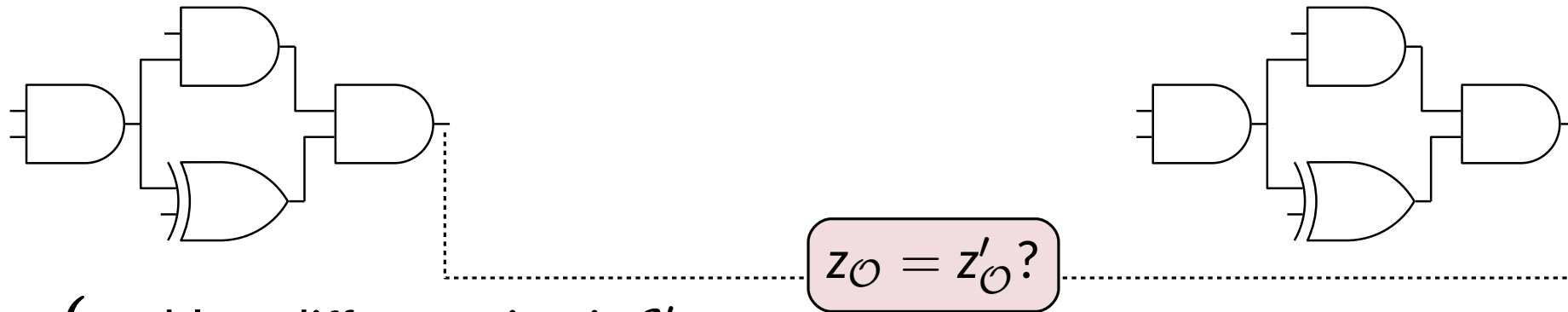
# Optimizing the One-way Communication Via Dual Execution

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■ How about optimizing one-way communication? Maybe dual execution?



■ [HEK12, HsV20]: Check for equality in circuit outputs



$A$  may  $\left\{ \begin{array}{l} \text{garble a different circuit } \mathcal{C}' \\ \text{use different input } \mathbf{x} \text{ or } \mathbf{y} \\ \text{launch selective failure} \end{array} \right.$

$\Rightarrow$

- Even if we use a secure  $\mathcal{F}_{\text{EQ}}$
- $A$  can still gain 1-bit leakage

# Optimizing the One-way Communication Via Dual Execution



$$\mathcal{F}_{\text{cpre}}$$

$$[\mathbf{a}], [\hat{\mathbf{a}}], [\mathbf{b}], [\hat{\mathbf{b}}], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}_k \oplus \hat{b}_k = (a_i \oplus b_i) \cdot (a_j \oplus b_j)$$

$$\Pi_{\text{DG}}$$

$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$



$$\mathcal{F}_{\text{cpre}}$$

$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

$$\Pi_{\text{DG}}$$

$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

# Optimizing the One-way Communication Via Dual Execution



$\mathcal{F}_{\text{cpre}}$



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$$\{\lambda_k, L_{k,0}, L_{k,1}\} \quad \{\Lambda_k, L_{k,\Lambda_k}\} \text{ for } k \in \mathcal{W}$$

$$L_{k,\Lambda_k} = L_{k,0} \oplus \Lambda_k \cdot \Delta_A$$



$\mathcal{F}_{\text{cpre}}$



$$[\mathbf{a}'], [\hat{\mathbf{a}}'], [\mathbf{b}'], [\hat{\mathbf{b}}'], \Delta_A, \Delta_B \leftarrow \$$$

$$\text{s.t. } \hat{a}'_k \oplus \hat{b}'_k = (a'_i \oplus b'_i) \cdot (a'_j \oplus b'_j)$$

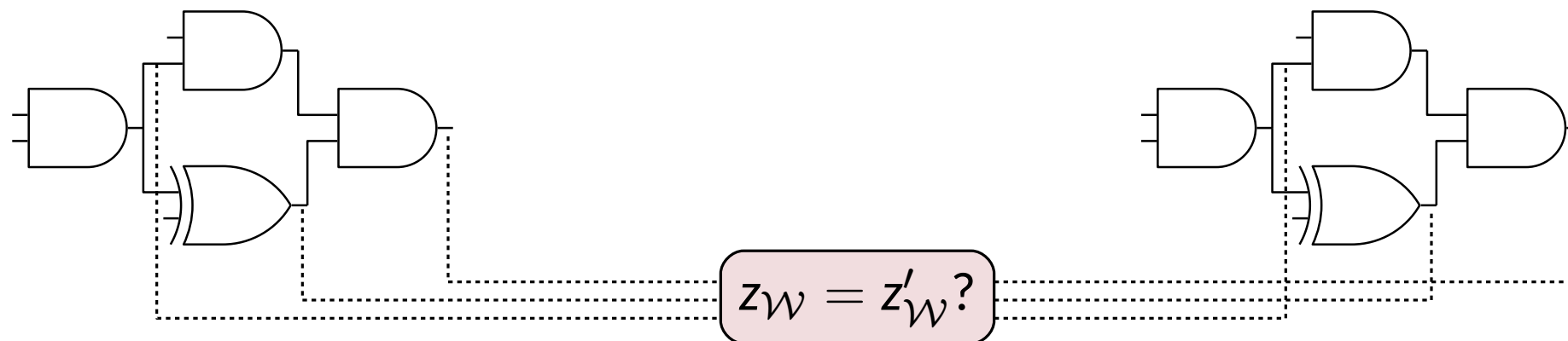
$\Pi_{\text{DG}}$

$$\{\lambda'_k, L'_{k,0}, L'_{k,1}\} \quad \{\Lambda'_k, L'_{k,\Lambda'_k}\} \text{ for } k \in \mathcal{W}$$

$$L'_{k,\Lambda'_k} = L'_{k,0} \oplus \Lambda'_k \cdot \Delta_B$$

- Color bits and wire masks are authenticated for every wire
- This enables checking equality for every wire across two executions

[HK21] Garbled Sharing





# Conclusion

- Further optimization on the compression technique of [DILO22]
- Dual-key authentication and efficient generation
- Dual execution upon distribution garbling eliminates 1-bit leakage
- Malicious 2PC with one-way comm. of  $2\kappa + 5$  bits, two way comm. of  $2\kappa + 3\rho + 4$  bits

2PC	Rounds		Communication per AND gate	
	Prep.	Online	one-way (bits)	two-way (bits)
Half-gates	1	2	$2\kappa$	$2\kappa$
HSS-PCG	8	2	$8\kappa + 11$ (4.04×)	$16\kappa + 22$ (8.09×)
KRRW-PCG	4	4	$5\kappa + 7$ (2.53×)	$8\kappa + 14$ (4.05×)
DILO	7	2	$2\kappa + 8\rho + 1$ (2.25×)	$2\kappa + 8\rho + 5$ (2.27×)
This work	8	3	$2\kappa + 5$ ( $\approx \mathbf{1}$ ×)	$4\kappa + 10$ (2.04×)
This work+DILO	8	2	$2\kappa + 3\rho + 2$ (1.48×)	$2\kappa + 3\rho + 4$ ( $\approx \mathbf{1.48}$ ×)

Thanks for your listening

*Merci beaucoup*