

Discretization Error Reduction for High Precision Torus Fully Homomorphic Encryption

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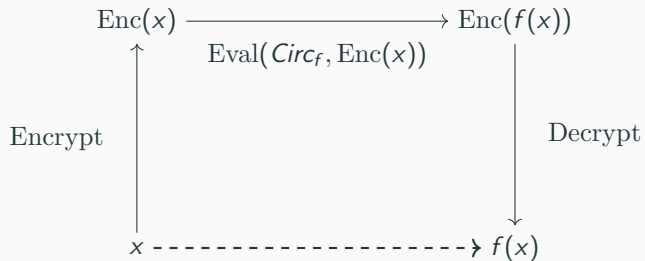
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Introduction

Fully Homomorphic Encryption

- Encryption scheme that allows to operate with encrypted data



- Usually comes with a *reryption* procedure, called **bootstrapping**.

Notations

- $n \in \mathbb{Z}$: TLWE dimension
- N : power of 2. (Ring Dimension)
- $\mathbb{T} = \mathbb{R}/\mathbb{Z} = [-\frac{1}{2}, \frac{1}{2})$
- $\mathbb{B} = \{0, 1\}$
- $\mathbb{Z}_N[X] = \mathbb{Z}[X]/\langle X^N + 1 \rangle$
- $\mathbb{T}_N[X] = \mathbb{T}[X]/\langle X^N + 1 \rangle$
- $\mathbf{s} \in \mathbb{B}^n$
- $\mathcal{K} \in \mathbb{B}_N[X]$
- ν : Extension Parameter

FHE scheme introduced in [CGGI20]¹, based on the LWE (Learning With Errors) problem.

Works with three ciphertext spaces:

- TLWE : $\text{TLWE}_{\mathbf{s}}(m) = (\mathbf{a}, b = \mathbf{a} \cdot \mathbf{s} + m + e) \in \mathbb{T}^{n+1}$
- TRLWE : $\text{TRLWE}_{\mathcal{K}}(m(X)) = (a(X), b(X) = a(X)\mathcal{K}(X) + m(X) + e(X)) \in \mathbb{T}_N[X]^2$
- TRGSW :

$$\text{TRGSW}_{\mathcal{K}}(z(X)) = \left[\begin{array}{c} \text{TRLWE}_{\mathcal{K}}(z(X)/B) \\ \vdots \\ \text{TRLWE}_{\mathcal{K}}(z(X)/B^d) \\ \text{---} \\ \text{TRLWE}_{\mathcal{K}}(z(X) \cdot (-\mathcal{K})/B) \\ \vdots \\ \text{TRLWE}_{\mathcal{K}}(z(X) \cdot (-\mathcal{K})/B^d) \end{array} \right], z(X) \in \mathbb{Z}_N[X].$$

¹Illaria Chillotti, Nicolas Gama, Mariya Georgieva, and Malika Izabachène. TFHE: fast fully homomorphic encryption over the torus. In: *Journal of Cryptology* 33.1 (2020), pp. 34–91

TFHE (continued)

TFHE supports *external product* \boxtimes , a multiplication between TRGSW and TRLWE ciphertext:

$$\text{TRGSW}_{\mathcal{K}}(z(X)) \boxtimes \text{TRLWE}_{\mathcal{K}}(m(X)) = \text{TRLWE}_{\mathcal{K}}(z(X)m(X)).$$

Bootstrapping homomorphically decrypts the TLWE ciphertext by calculating **CMux** circuit for $i \in \llbracket 0, n-1 \rrbracket$:

CMux : Homomorphic Selection

$$\mathbf{CMux}(\text{BSK}_i = \text{TRGSW}_{\mathcal{K}}(\mathbf{s}_i), X^{\bar{a}_i} \text{ACC}, \text{ACC}) = \text{TRGSW}_{\mathcal{K}}(\mathbf{s}_i) \boxtimes ((X^{\bar{a}_i} - 1) \cdot \text{ACC}) + \text{ACC}$$

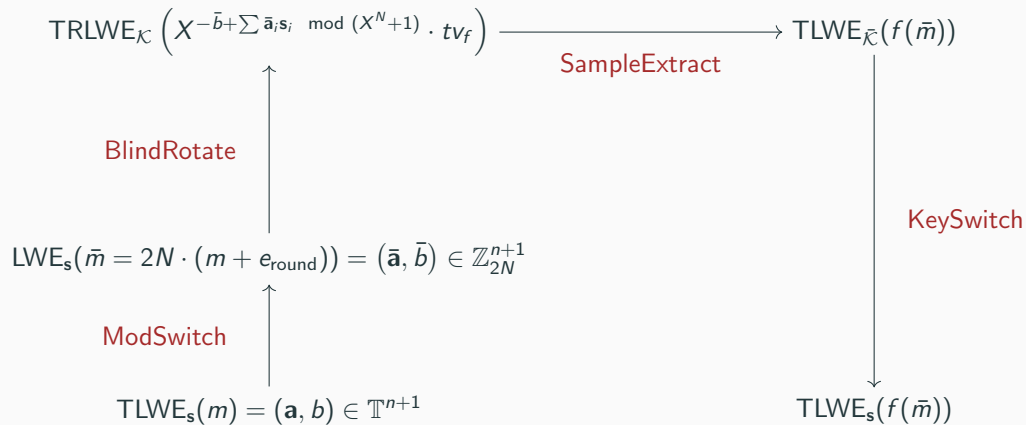
which gives

$$X^{-\bar{b} + \sum \bar{a}_i \mathbf{s}_i \bmod (X^N + 1)} \cdot \text{ACC} = \text{TRLWE}_{\mathcal{K}}\left(X^{-\bar{b} + \sum \bar{a}_i \mathbf{s}_i \bmod (X^N + 1)} \cdot m(X)\right).$$

Extended BootStrapping for TFHE

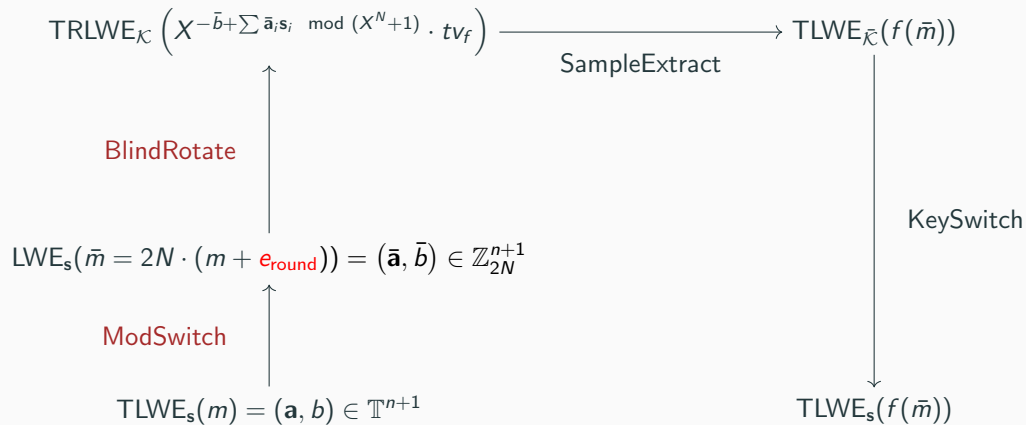
TFHE (Functional) Bootstrapping

Given a function $f : \mathbb{T} \rightarrow \mathbb{T}$, the (functional) TFHE bootstrapping is a series of algorithms:

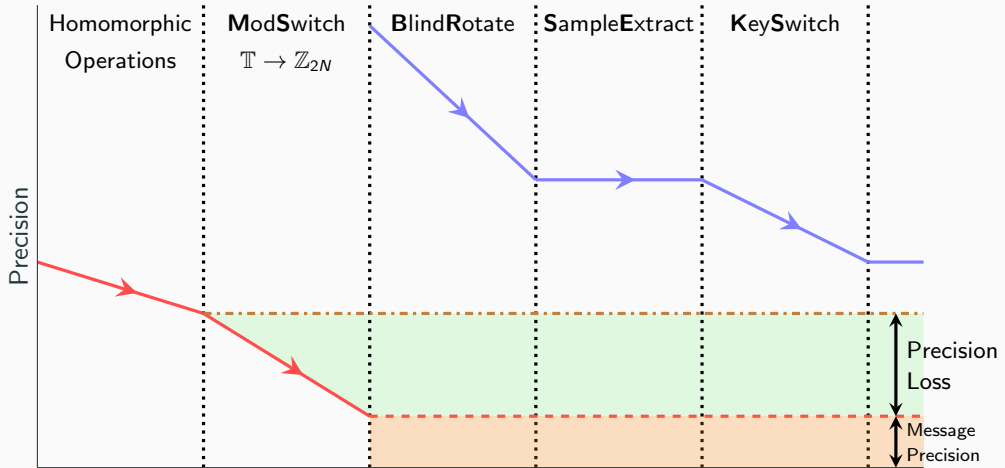


TFHE (Functional) Bootstrapping

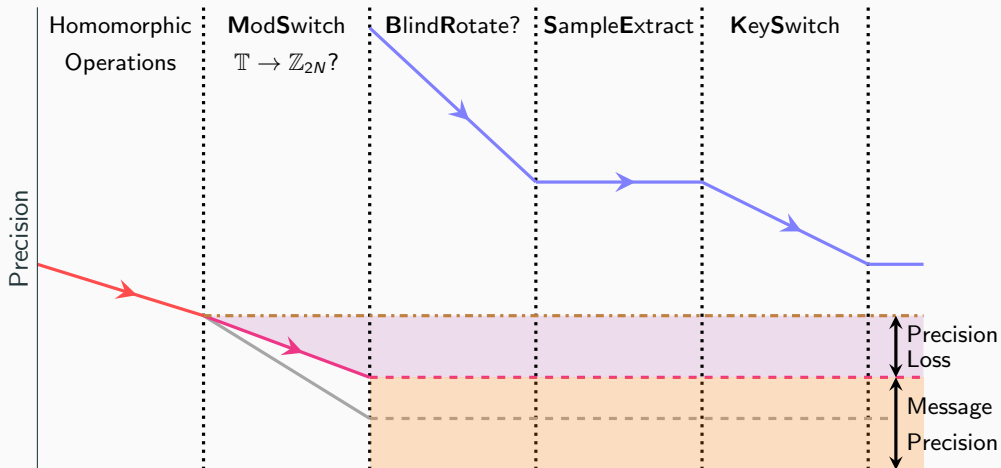
Given a function $f : \mathbb{T} \rightarrow \mathbb{T}$, the (functional) TFHE bootstrapping is a series of algorithms:



Precision in TFHE Bootstrapping

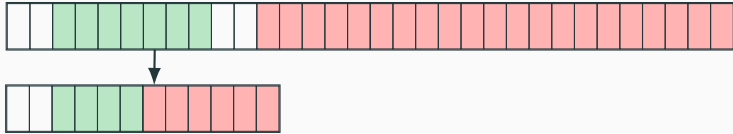


High Precision TFHE : Reduce the ModSwitch (Discretization) Error



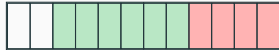
High Precision TFHE : Reduce the ModSwitch (Discretization) Error

ModSwitch to \mathbb{Z}_{2N} adds error:

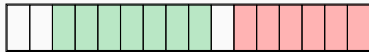


To reduce this, there are some approaches:

- Reduce Hamming weight of secret key \mathbf{s} , $\text{Ham}(\mathbf{s})$.



- **Increase** $N \rightarrow 2^k N$



Enlarging $N \rightarrow N' = 2^k N$

Advantages

- Can use small TRLWE/TRGSW error \Rightarrow BlindRotate noise decreases
- Error from ModSwitch decreases \Rightarrow precision increases by k bits

Disadvantages

- Quasilinear growth for polynomial multiplication complexity : $O(N' \log N')$
- (Possibly) Larger keyswitch noise
- (Possibly) Larger public key / ciphertext size

Our Work 1 - Ciphertext Extension

Module homomorphism $\iota : \mathbb{T}_N[X] \rightarrow \mathbb{T}_{2^\nu N}[X]$

$$\begin{aligned} \iota : \mathbb{T}_N[X] &\longrightarrow \mathbb{T}_{2^\nu N}[X], \\ p(x) = \sum_{i=0}^{N-1} p_i X^i &\longmapsto p_{\text{ext}}(X) = \sum_{i=0}^{N-1} p_i X^{2^\nu i} \end{aligned}$$

Toy Example: $\mathbb{T}_2[X] \rightarrow \mathbb{T}_8[X]$

$$\iota(0.5X + 0.7) = 0.5X^4 + 0.7$$

Ciphertext Extension by Zero Padding

$$\begin{aligned} \iota \left(\text{TRLWE}_{\mathcal{K}}^N(p(X)) \right) &= \text{TRLWE}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(p_{\text{ext}}(X)) \text{ for } p(X) \in \mathbb{T}_N[X], \\ \iota \left(\text{TRGSW}_{\mathcal{K}}^N(q(X)) \right) &= \text{TRGSW}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(q_{\text{ext}}(X)) \text{ for } q(X) \in \mathbb{Z}_N[X], \end{aligned}$$

Our Work 1 - Extended BlindRotate

The external product \boxtimes on ring dimension $2^\nu N$ naturally follows:

$$\text{TRGSW}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(q_{\text{ext}}(X)) \boxtimes \text{TRLWE}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(p(X)) = \text{TRLWE}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(p(X) \cdot q_{\text{ext}}(X)).$$

Which enables to evaluate the **CMux** circuit on ring dimension $2^\nu N$ while original ring dimension stays in N :

$$\text{ACC} \leftarrow \text{TRGSW}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(\mathbf{s}_i) \boxtimes ((X^{\bar{a}_i} - 1) \cdot \text{ACC}) + \text{ACC}.$$

Thus, we **ModSwitch** to $\mathbb{Z}_{2^{\nu+1}N}$, gaining additional ν bits of precision.

Our Work 2 - Parallel External Product

Module isomorphism $\tau : \mathbb{T}_{2^\nu N}[X] \rightarrow \mathbb{T}_N^{2^\nu}[X]$

$$\begin{aligned} p(x) = \sum_{i=0}^{2^\nu N-1} p_i X^i &\mapsto \left(p^{(0)}(X), \dots, p^{(2^\nu-1)}(X) \right) \\ &= \left(\sum_{i=0}^{N-1} p_{2^\nu i} X^i, \dots, \sum_{i=0}^{N-1} p_{2^\nu i + 2^\nu - 1} X^i \right). \end{aligned}$$

Toy Example: $\mathbb{T}_8[X] \rightarrow \mathbb{T}_2^4[X]$

$$\tau(0.2X^7 + 0.1X^6 + 0.5X^4 + 0.3X + 0.7) = (0.7 + 0.5X, 0.3, 0.1X, 0.2X)$$

Ciphertext Folding

$$\tau \left(\text{TRLWE}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(m) \right) = \left(\text{TRLWE}_{\mathcal{K}}^N(m_0), \dots, \text{TRLWE}_{\mathcal{K}}^N(m_{2^\nu-1}) \right) \text{ for } m(X) \in \mathbb{T}_N[X].$$

Our Work 2 - Parallel External Product

For special case, we can parallelize the external product over dimension $2^\nu N$ into 2^ν external products in dimension N :

Special case of External Product \square

- **C1.** \mathcal{K}_{ext} should be an extended key of \mathcal{K} .
- **C2.** $\text{TRGSW}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(z)$ is an extension of $\text{TRGSW}_{\mathcal{K}}^N(z)$, and $z \in \mathbb{Z}$.

$$\begin{aligned} \text{TRGSW}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(z) \square \text{TRLWE}_{\mathcal{K}_{\text{ext}}}^{2^\nu N}(m) &\cong \begin{bmatrix} \text{TRGSW}_{\mathcal{K}}^N(z) \square \text{TRLWE}_{\mathcal{K}}^N(m_0) \\ \text{TRGSW}_{\mathcal{K}}^N(z) \square \text{TRLWE}_{\mathcal{K}}^N(m_1) \\ \vdots \\ \text{TRGSW}_{\mathcal{K}}^N(z) \square \text{TRLWE}_{\mathcal{K}}^N(m_{2^\nu-1}) \end{bmatrix} \\ &\cong \left(\text{TRLWE}_{\mathcal{K}}^N(z \cdot m_0), \dots, \text{TRLWE}_{\mathcal{K}}^N(z \cdot m_{2^\nu-1}) \right) \end{aligned}$$

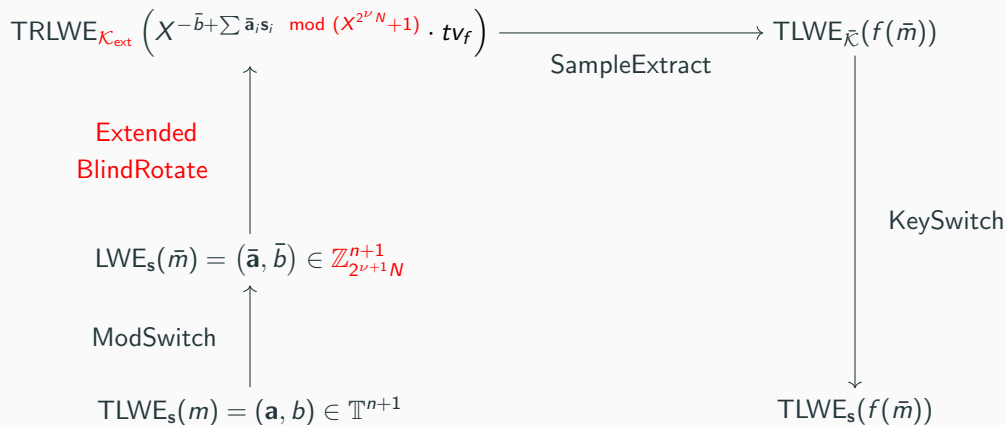
Our Work 2 - Parallel CMux / BlindRotate

With the parallel external product, we can build parallel CMux circuit:

$$\vec{\overline{ACC}} \leftarrow \left[\begin{array}{l} \text{BSK}_i \boxtimes \left(\tau \left(X^{\bar{a}_i} \cdot \tau^{-1} \left(\vec{\overline{ACC}} \right) \right)_0 - \vec{\overline{ACC}}_0 \right) + \vec{\overline{ACC}}_0 \\ \text{BSK}_i \boxtimes \left(\tau \left(X^{\bar{a}_i} \cdot \tau^{-1} \left(\vec{\overline{ACC}} \right) \right)_1 - \vec{\overline{ACC}}_1 \right) + \vec{\overline{ACC}}_1 \\ \vdots \\ \text{BSK}_i \boxtimes \left(\tau \left(X^{\bar{a}_i} \cdot \tau^{-1} \left(\vec{\overline{ACC}} \right) \right)_{2^\nu - 1} - \vec{\overline{ACC}}_{2^\nu - 1} \right) + \vec{\overline{ACC}}_{2^\nu - 1} \end{array} \right]$$

Equivalent to running 2^ν CMux circuits!

Summary : Extended BootStrapping



- Gains ν bit of precision
- Able to keep small ring dimension N
- Parallelized bootstrapping

Experimental Results

We implemented our **Extended BootStrapping** on top of the TFHE library²

We also implemented three state-of-the-art full-domain bootstrapping algorithms ([KS21], [YXSCZ21], [CZBSG22]), which all uses TFHE bootstrapping as sub-algorithm. The codes are all available at our github page³.

Experimental Environment

- CPU : Intel i9-13900K 5.8GHz 24 core, 32 threads
- RAM : DDR5 128GB
- Ubuntu 22.04

²<https://tfhe.github.io/tfhe/>

³<https://github.com/Stirling75/Extended-BootStrapping>

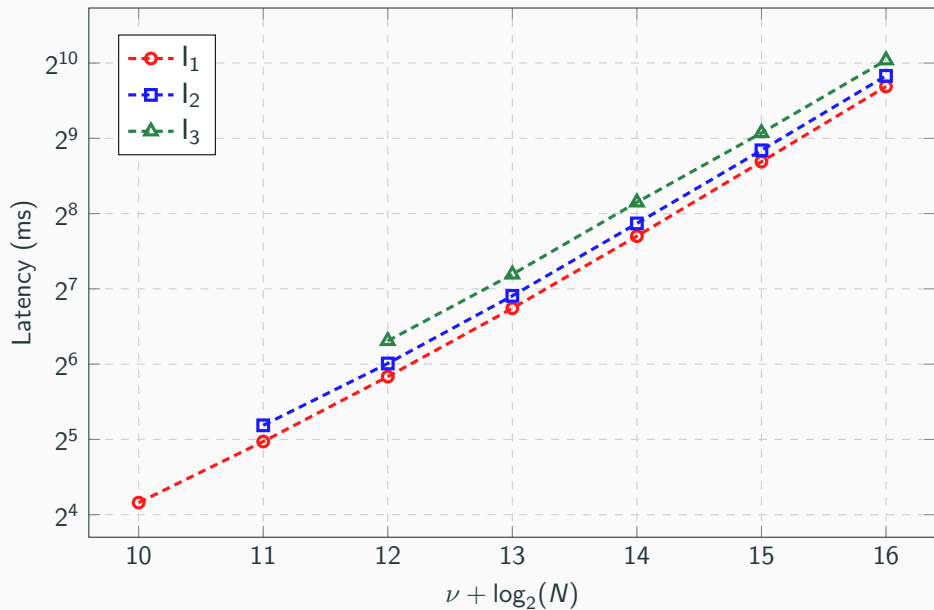
Parameters

Table 1: TFHE parameter sets. λ indicates the security level of given parameter set.

Param Set	λ	TLWE		TRLWE			KSK		BSK	
		n	$\sigma_{\text{TLWE}} (\log_2)$	N	k	$\sigma_{\text{TRLWE}} (\log_2)$	l_{KS}	B_{KS}	l_{BS}	B_{BS}
I ₁	80	750	-21.2	1024	1	-29.3	3	2 ⁸	7	2 ⁴
I ₂	80	750	-21.2	2048	1	-32	3	2 ⁸	7	2 ⁴
I ₃	80	750	-21.2	4096	1	-32	3	2 ⁸	7	2 ⁴
II	80	900	-25.7	2048	1	-32	5	2 ⁶	7	2 ⁴
III ₁	128	670	-12.4	1024	1	-20.1	3	2 ⁵	8	2 ³
III ₂	128	670	-12.4	2048	1	-32	3	2 ⁵	8	2 ³
III ₃	128	670	-12.4	4096	1	-32	3	2 ⁵	8	2 ³
IV	128	1300	-26.1	2048	1	-32	5	2 ⁶	7	2 ⁴

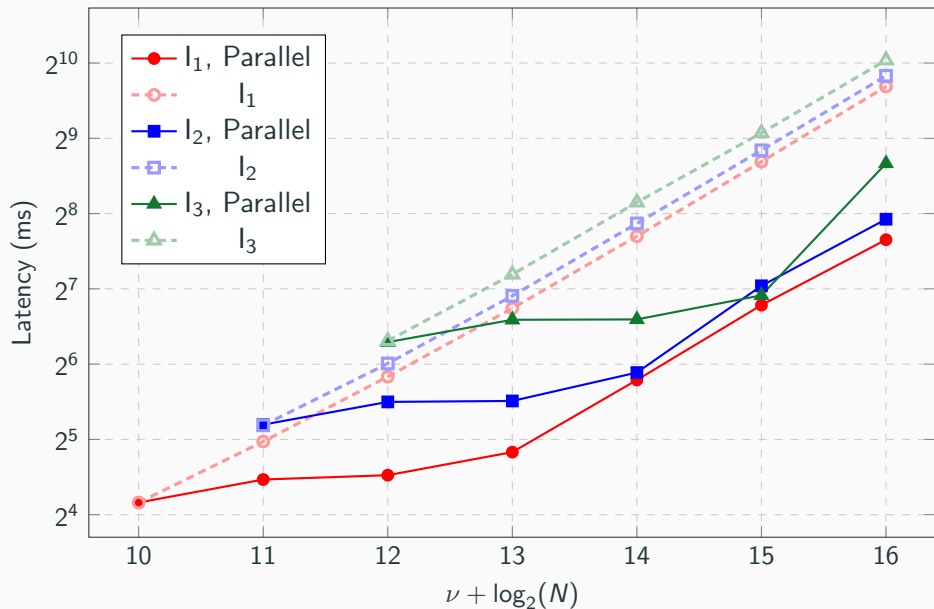
EBS : Performance (Non-Parallelized)

Parameter set l_1, l_2, l_3

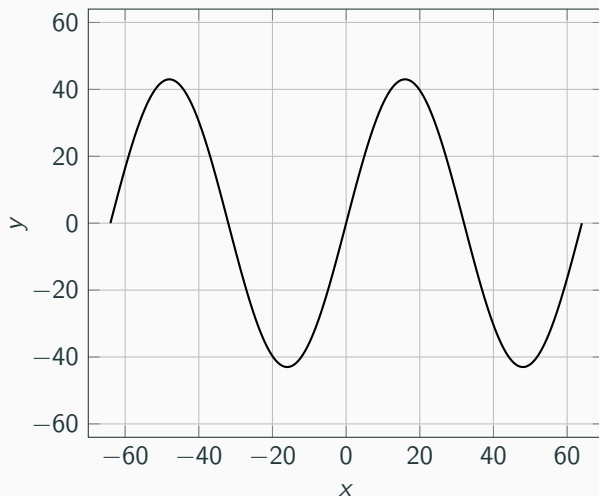


EBS : Performance (Parallelized)

Parameter set l_1, l_2, l_3

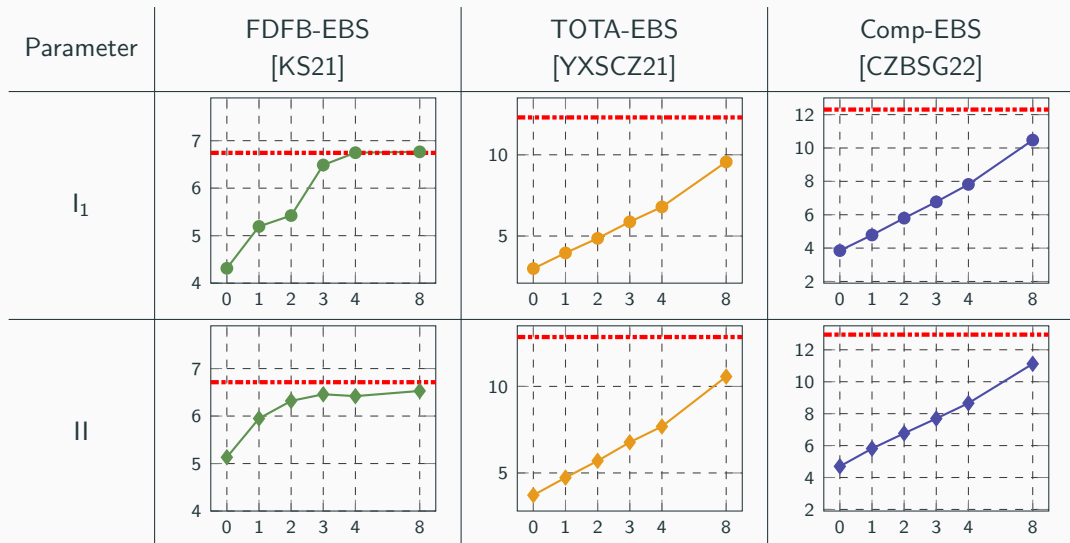


$$43 \sin\left(\frac{\pi}{32}x\right)$$



Homomorphic evaluation of function $f(x) = 43 \sin\left(\frac{\pi}{32}x\right)$ on domain $[-64, 64]$.

EBS : Function Evaluation



Thank you!

Thank You

- [CGGI20] Ilaria Chillotti, Nicolas Gama, Mariya Georgieva, and Malika Izabachène. **TFHE: fast fully homomorphic encryption over the torus**. In: *Journal of Cryptology* 33.1 (2020), pp. 34–91 (cit. on p. 6).
- [CZBSG22] Pierre-Emmanuel Clet, Martin Zuber, Aymen Boudguiga, Renaud Sirdey, and Cédric Gouy-Pailler. **Putting up the swiss army knife of homomorphic calculations by means of TFHE functional bootstrapping**. In: *Cryptology ePrint Archive* (2022) (cit. on pp. 22, 27).
- [KS21] Kamil Klucznik and Leonard Schild. **FDFB: Full domain functional bootstrapping towards practical fully homomorphic encryption**. In: *arXiv preprint arXiv:2109.02731* (2021) (cit. on pp. 22, 27).
- [YXSCZ21] Zhaomin Yang, Xiang Xie, Huajie Shen, Shiyong Chen, and Jun Zhou. **TOTA: Fully Homomorphic Encryption with Smaller Parameters and Stronger Security**. In: *Cryptology ePrint Archive* (2021) (cit. on pp. 22, 27).