

Polymath: Groth16 Is Not The Limit

Helger Lipmaa, University of Tartu, Estonia

Crypto 2024 Presentation



ZK-SNARKs

Computation: f

Public input (statement) X

Private input (witness) W



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SRS

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Proof π that $f(\mathbb{X}, \mathbb{W}) = 1$



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- Completeness

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- Knowledge-soundness

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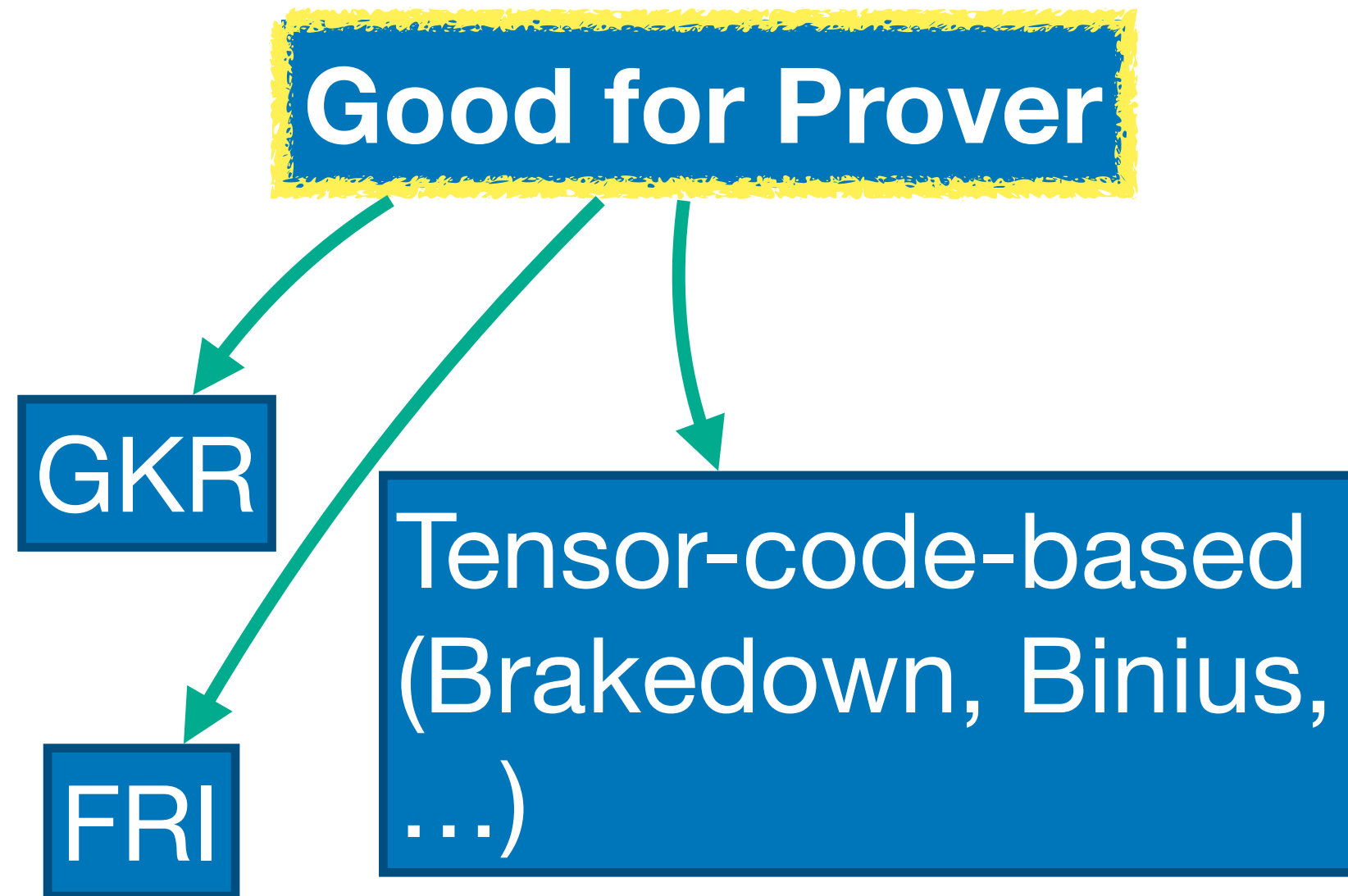
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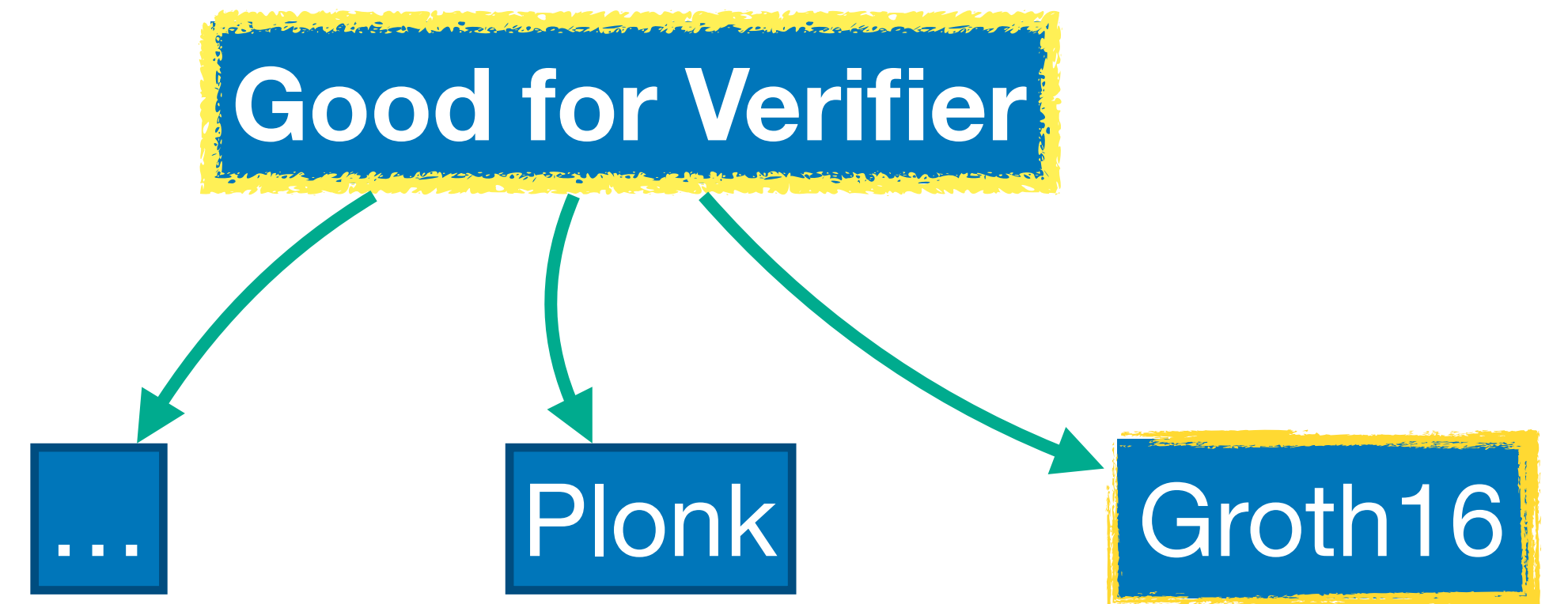
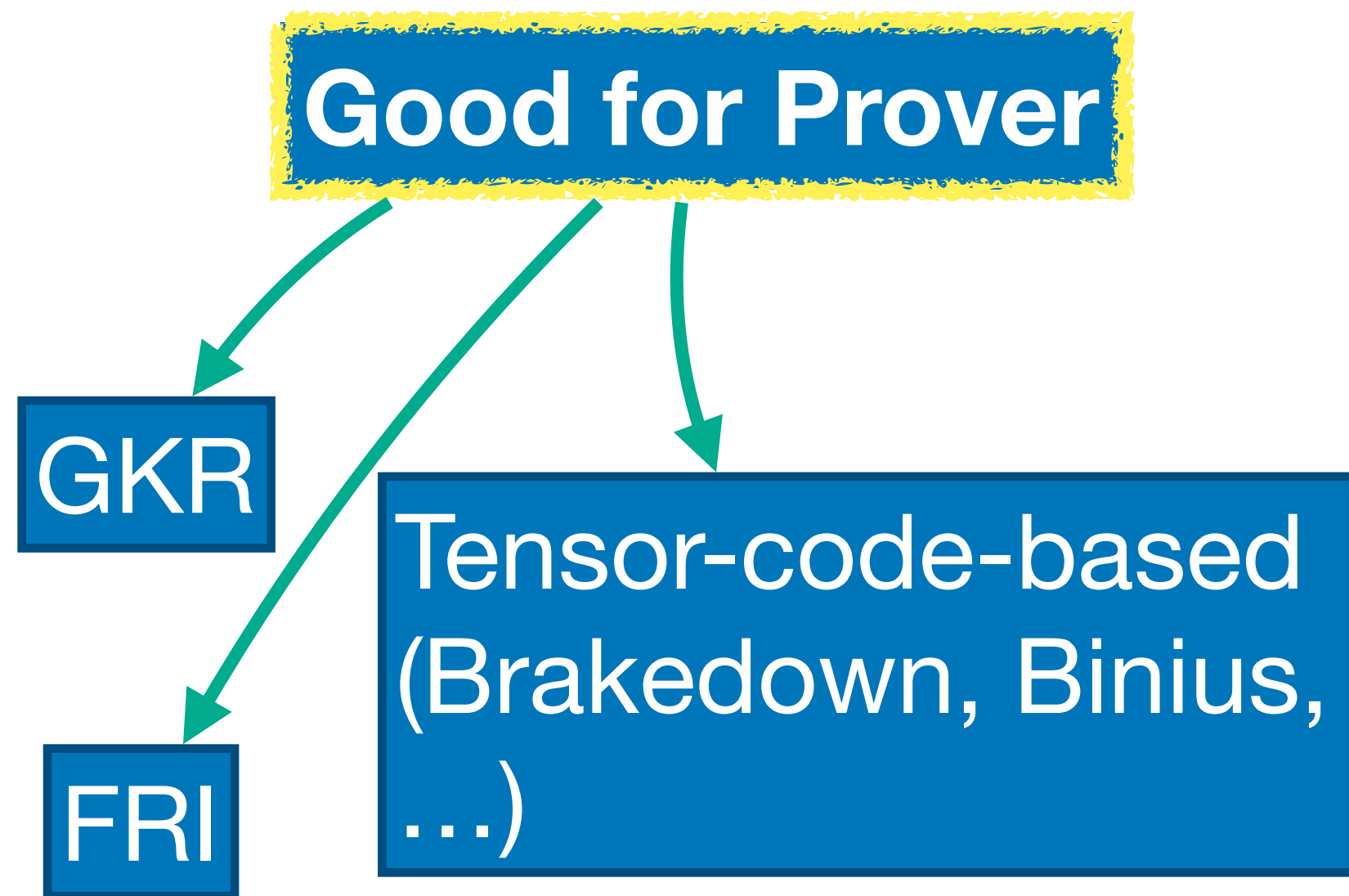
- Completeness
- Knowledge-soundness
- Zero-knowledge
- Succinct arguments

Landscape

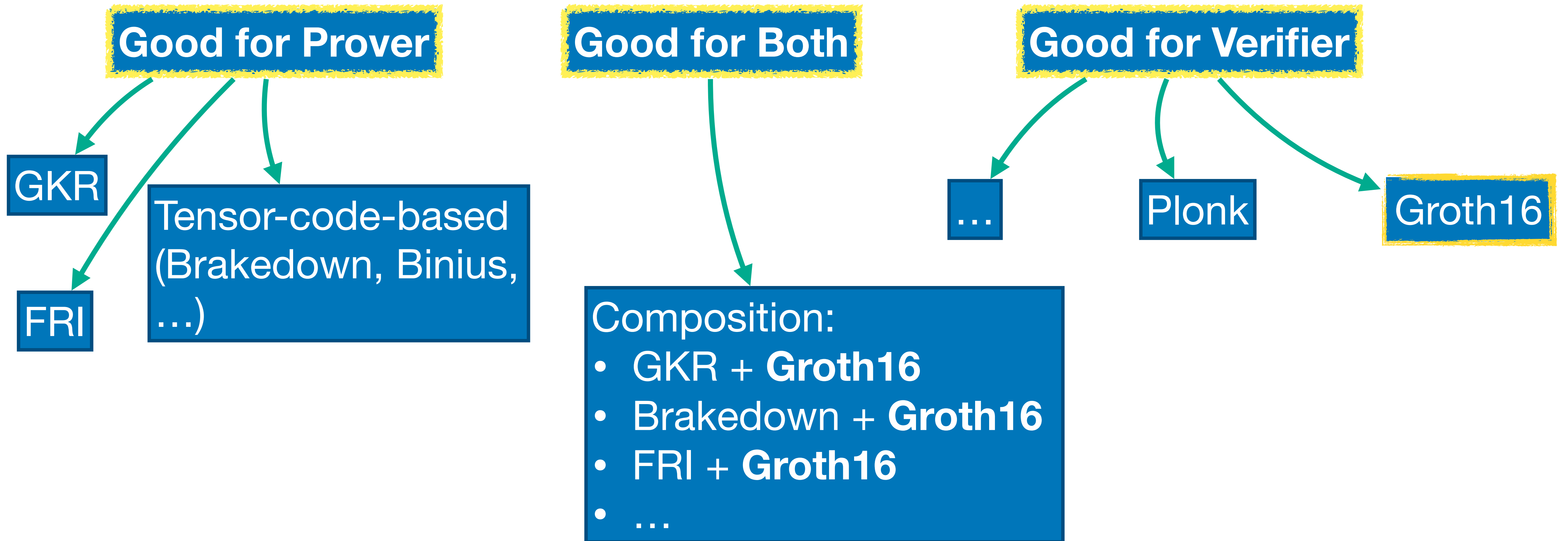
Landscape



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Landscape

Huge progress in zk-SNARK land in last 5 years
Groth16 still lands supreme after 8 years

- Shortest argument
- Fastest verifier

Good for Prover

GKR

FRI

Tensor-code-based
(Brakedown, Binius,
...)

Good for Both

Composition:

- GKR + Groth16
- Brakedown + Groth16
- FRI + Groth16
- ...

Good for Verifier

...

Plonk

Groth16



Pairings

For Muggles

- $pp = (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, [1]_1, [1]_2, \hat{e})$

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Groth16: Bird's-Eye View



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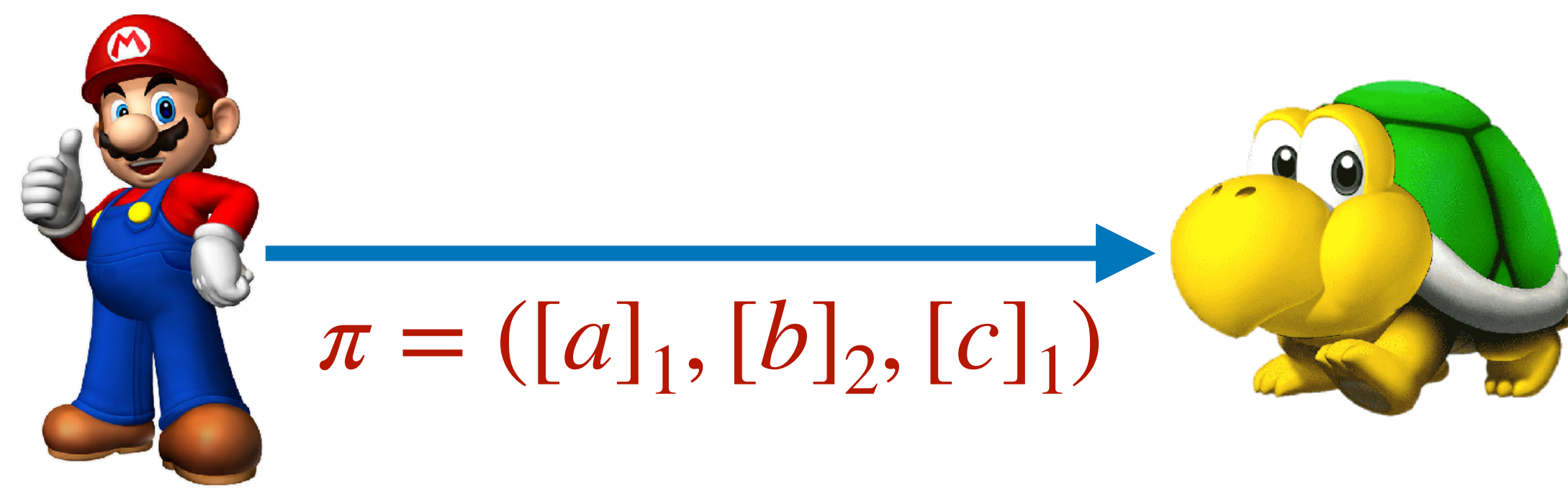


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- Verifier executes three pairings and $|\mathbb{X}|$ group ops

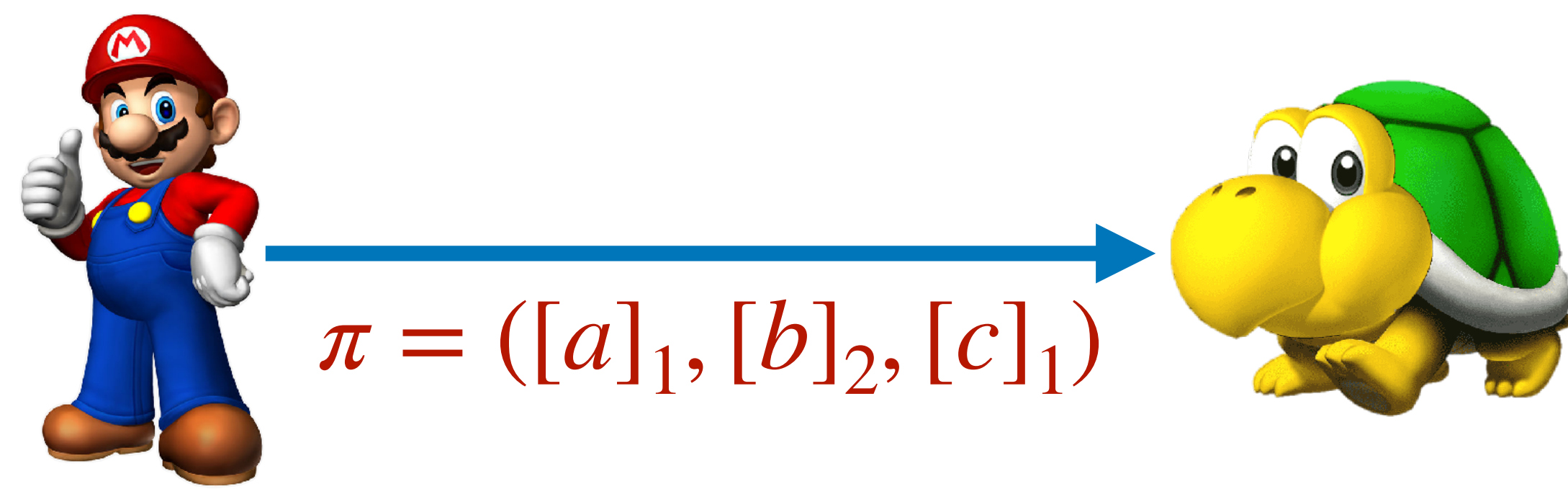
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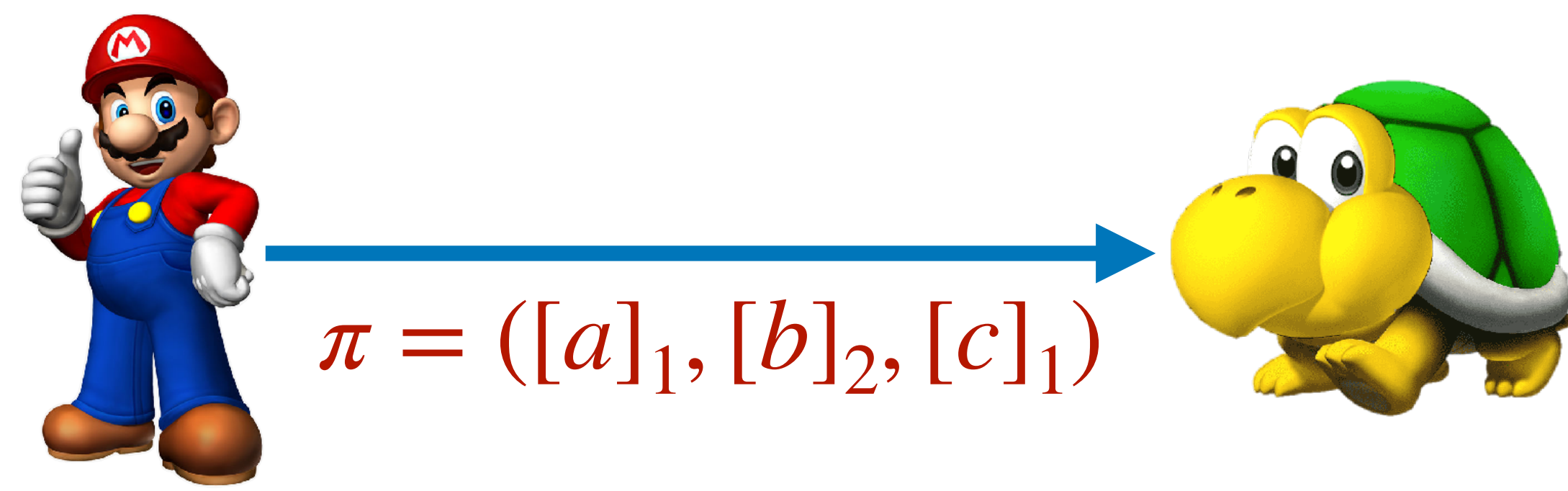
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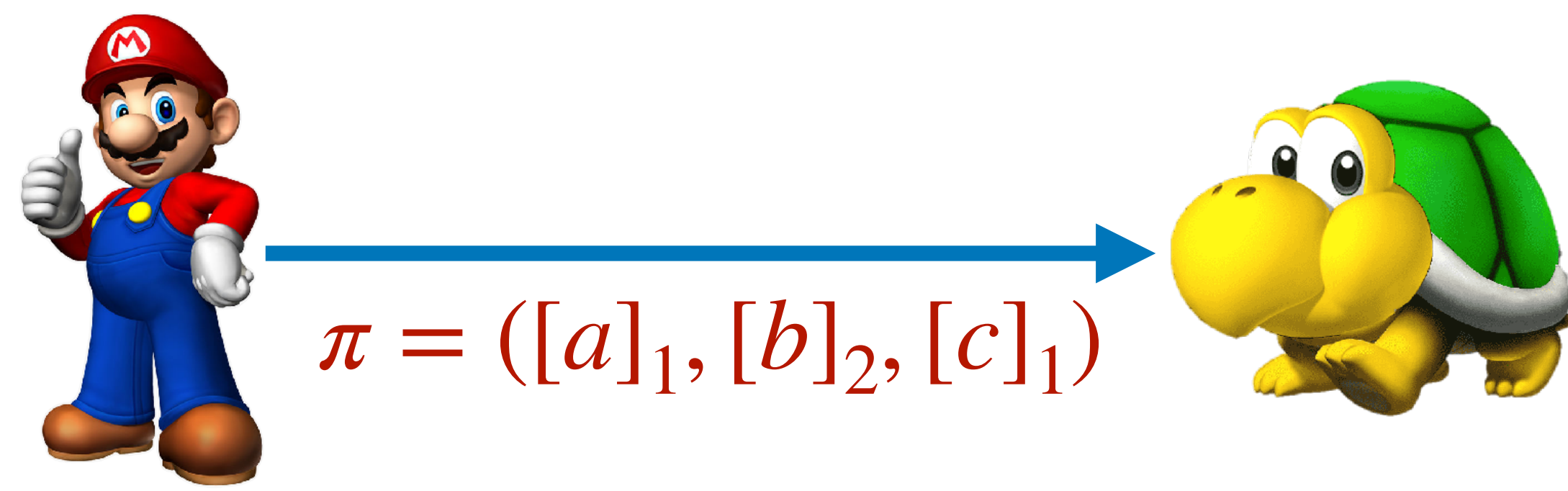


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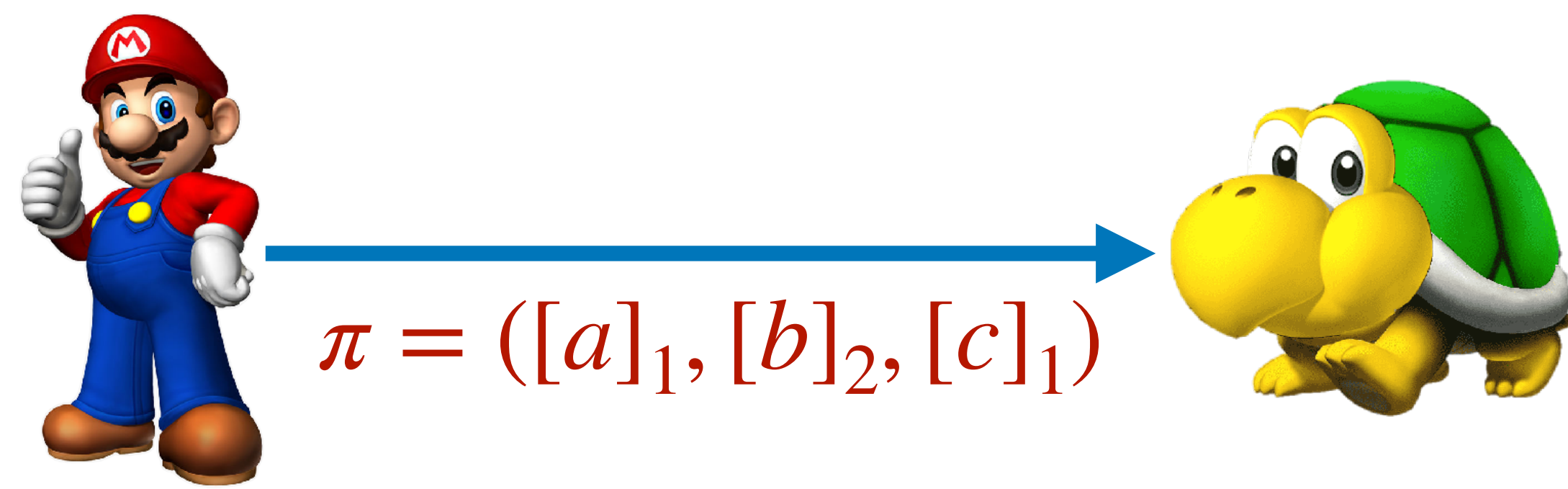
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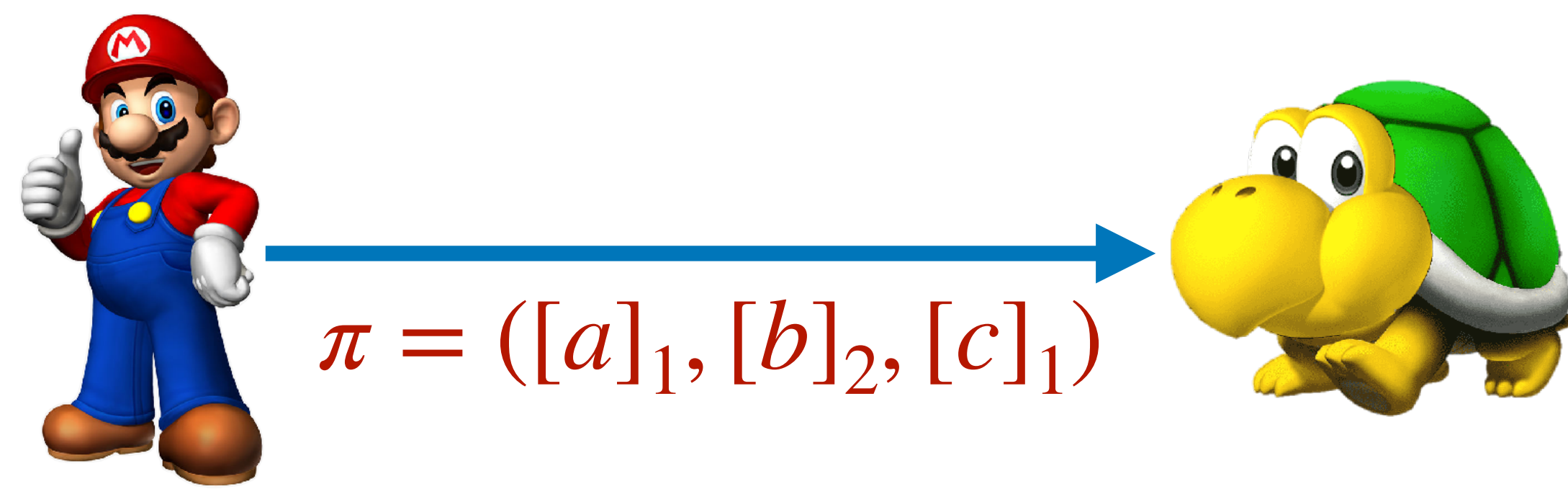
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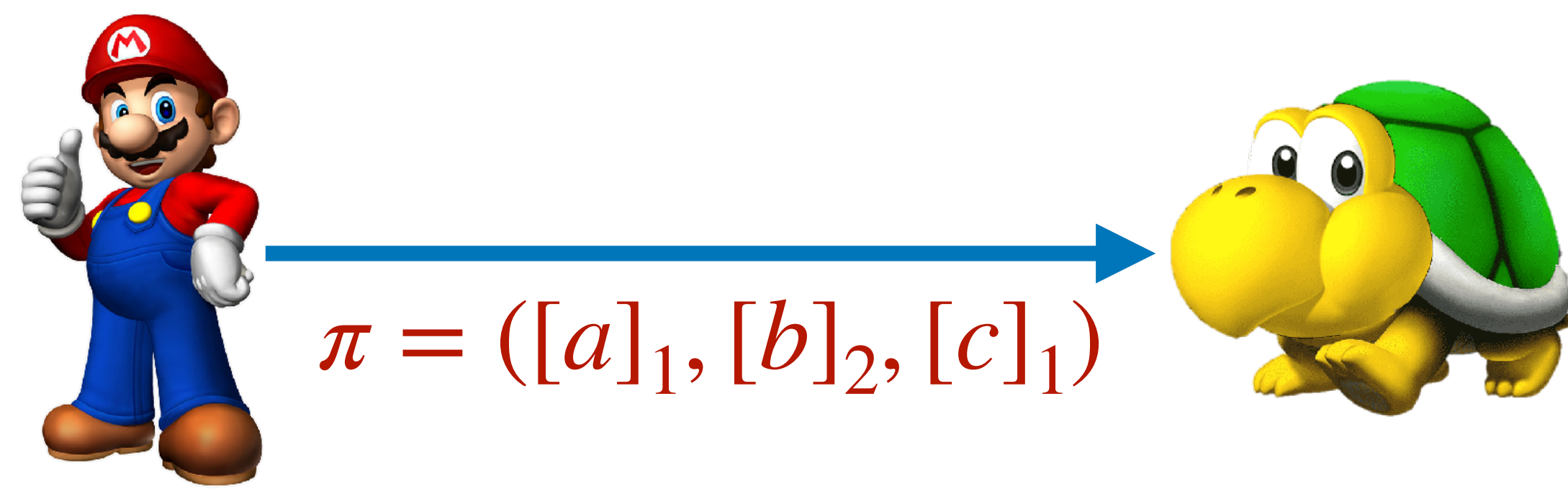
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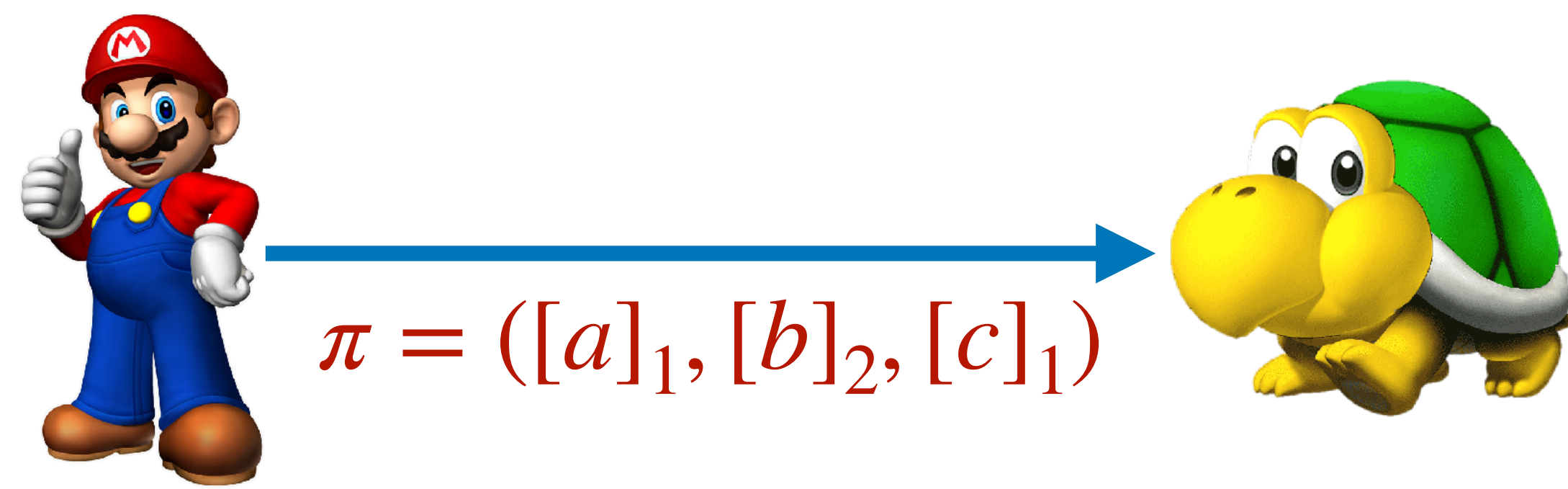
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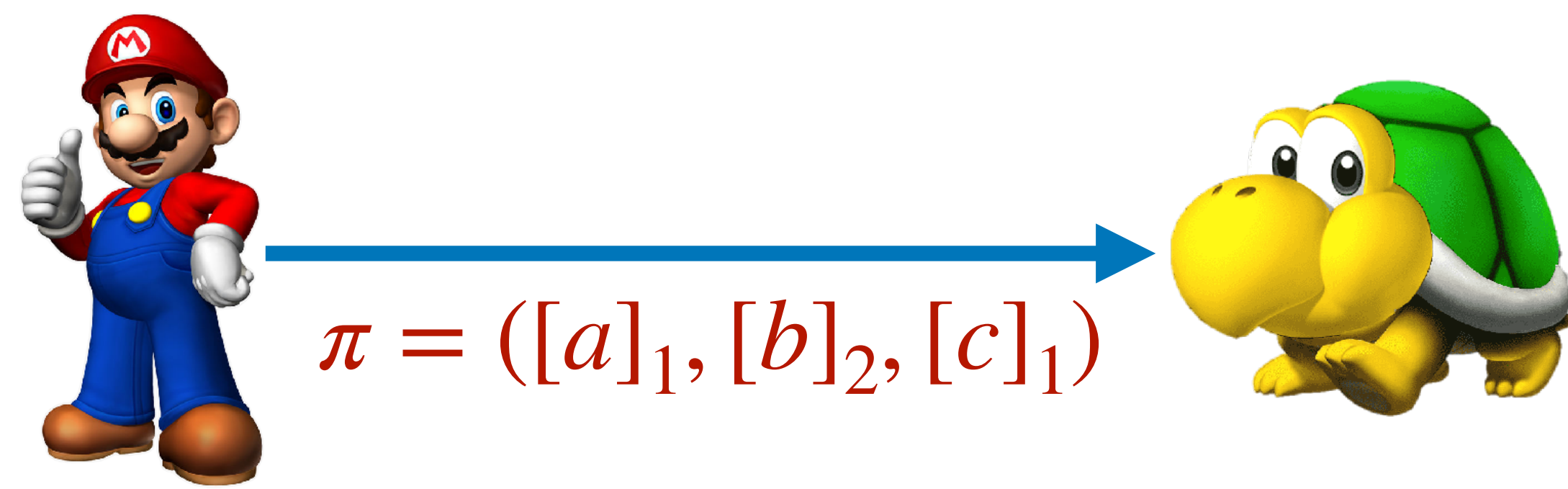
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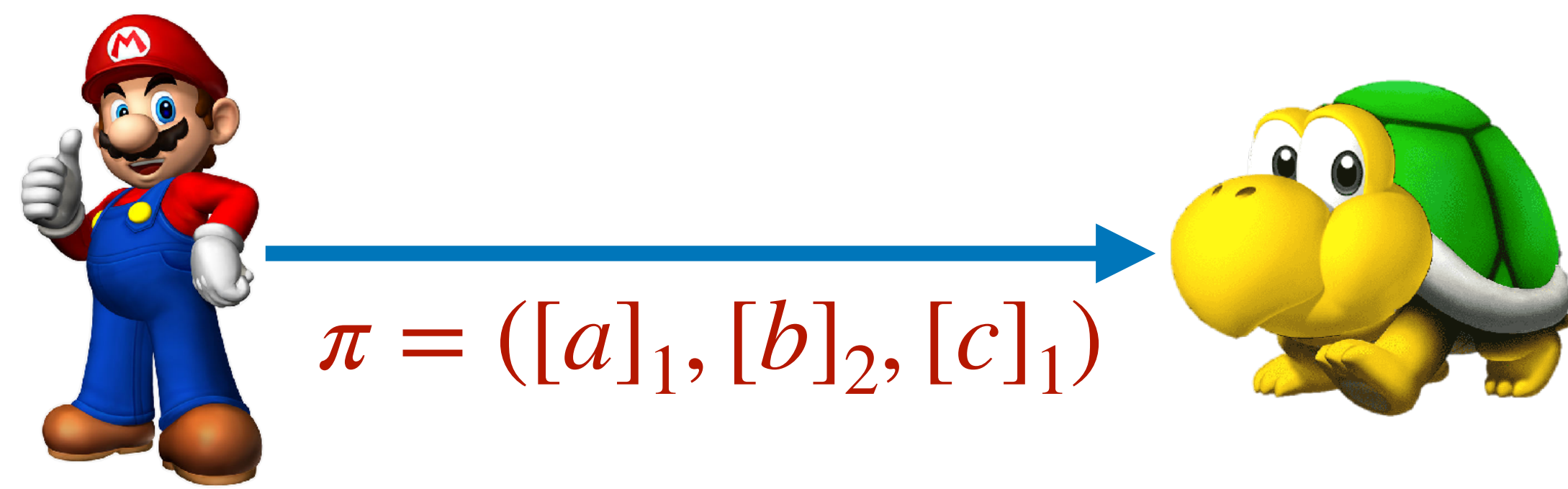
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- It talks about **#group elements**, not **bit-length**

Scenic Route to Polymath

For non-muggles



$$\pi = ([a]_1, [b]_2, [c]_1)$$



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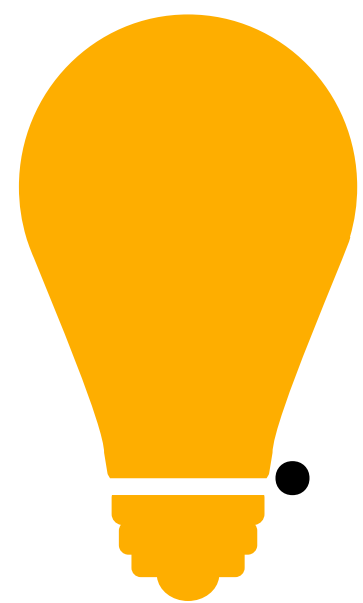
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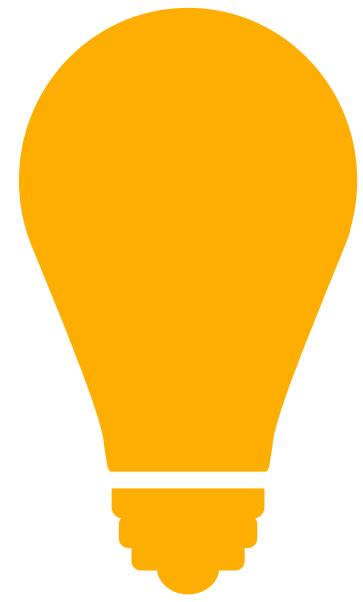
Problem:

- we still have $[b]_1$ in the argument!
- $\ell([b]_2) < \ell([b]_1) + \ell(\bar{b}) + \ell([h]_1)$ in 128-bit level

Scenic Route to Polymath



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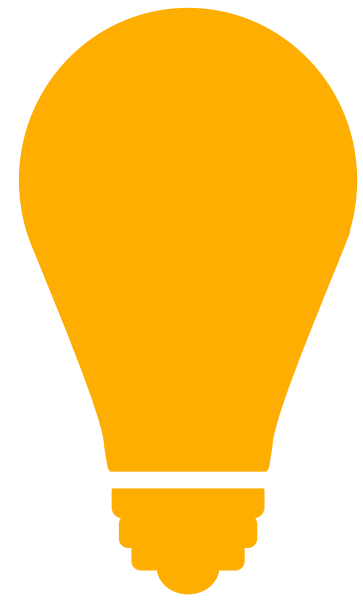


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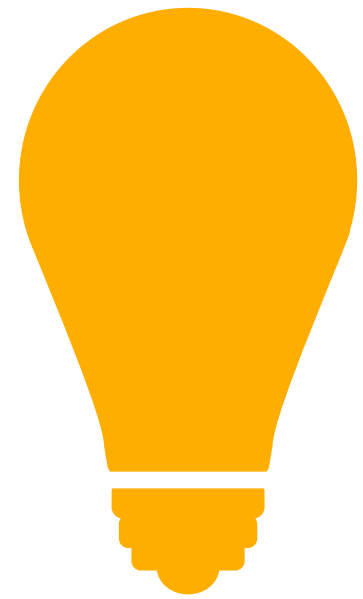
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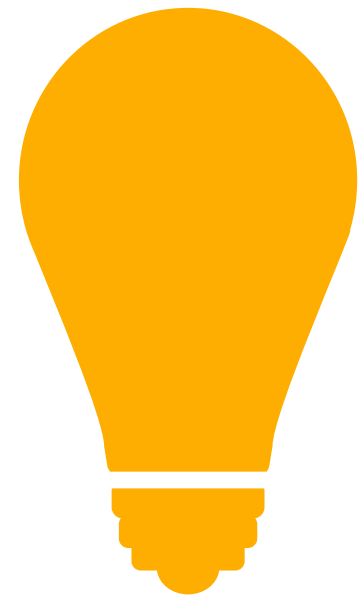
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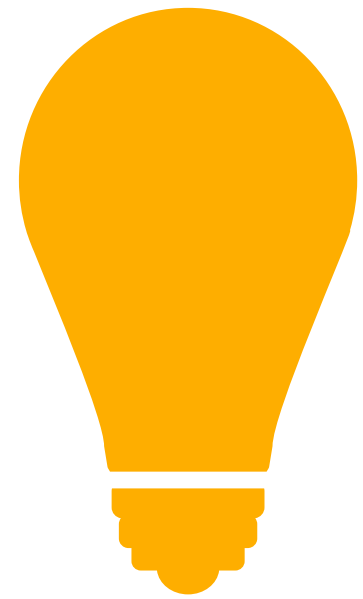
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Problem:

- Groth16 has five trapdoors, KZG is univariate
- Not clear how to use KZG

Scenic Route to Polymath



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- Univariatization:

Scenic Route to Polymath



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 - Replace each trapdoor with x^i for some i and a single trapdoor x

Scenic Route to Polymath



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Problem:

- even after exhaustive search, the exponents i are quite large
- KZG prover time $\Omega(\text{polynomial degree})$
 - \Rightarrow Results in high prover complexity

Scenic Route to Polymath



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- **Observation 1:** Groth16 for SAP has **-1** trapdoor

Scenic Route to Polymath

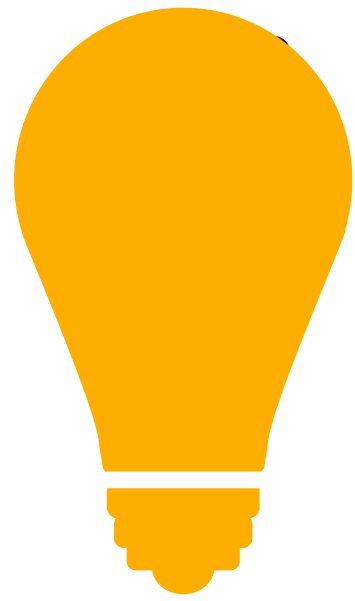


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Scenic Route to Polymath



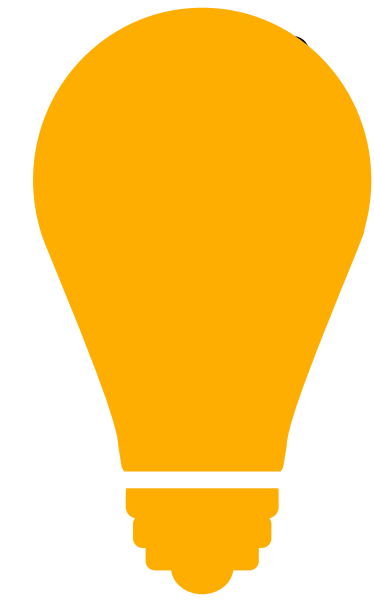
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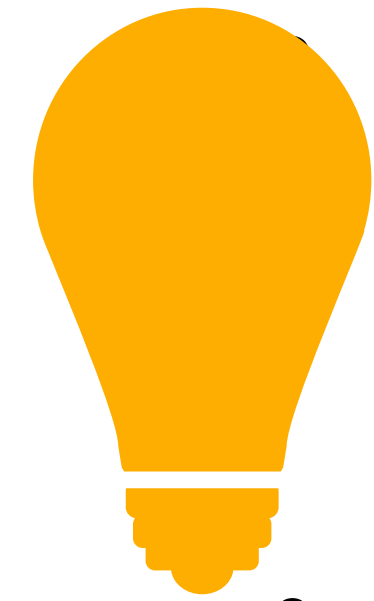
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- We use a different verification algorithm, getting rid of "statement" trapdoor
 - **Verifier becomes faster**
- In Polymath, V interpolates a polynomial in \mathbb{F}_p of degree $|\mathbb{X}|$
 - Instead of doing $|\mathbb{X}|$ -long MSM in Groth16

Scenic Route to Polymath



$$\pi = ([a]_1, [c]_1, \bar{b}, [h]_1)$$



- We only have three trapdoors

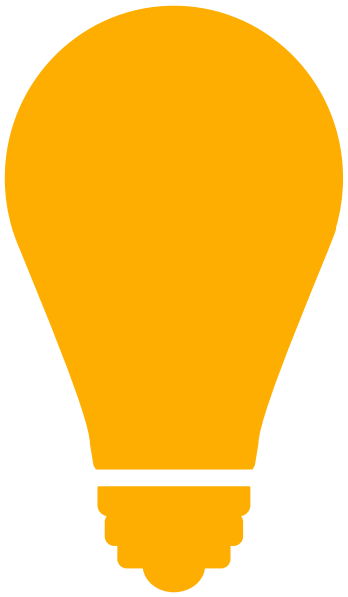
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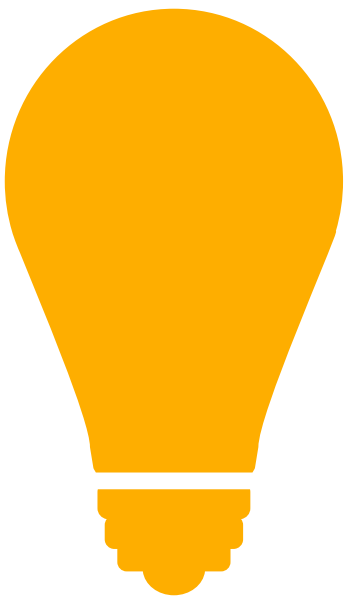
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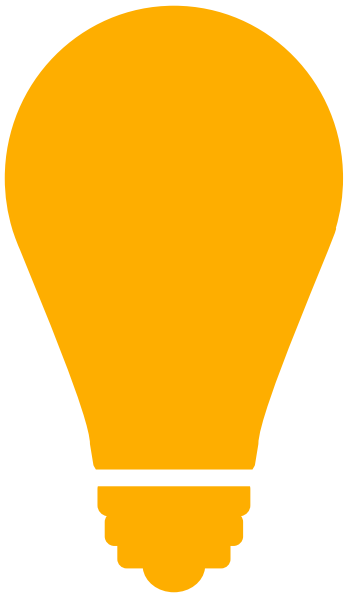
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- x is real trapdoor, α, γ, σ are field elements



Scenic Route to Polymath



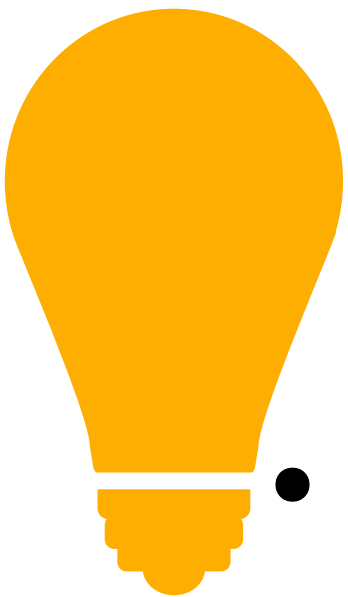
$$\pi = ([a]_1, [c]_1, \bar{b}, [h]_1)$$



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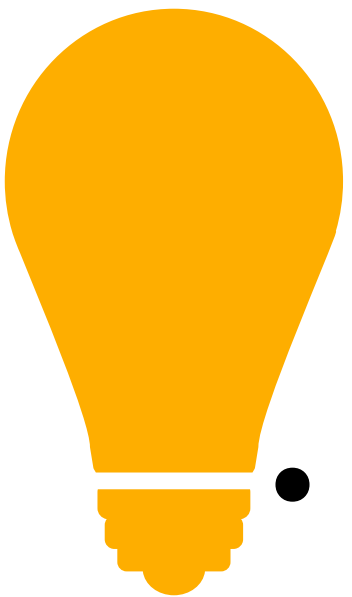
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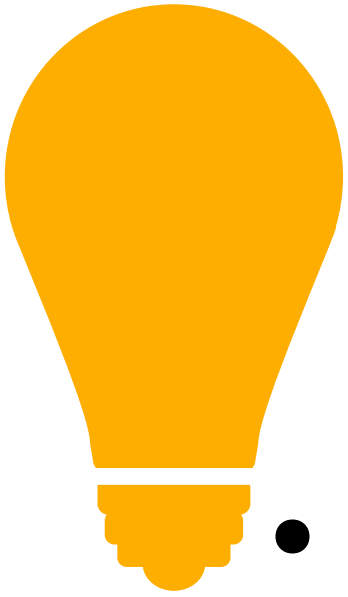
Problem:

- SRS is circuit-dependent
- It does not contain enough elements to compute $[h]_1$

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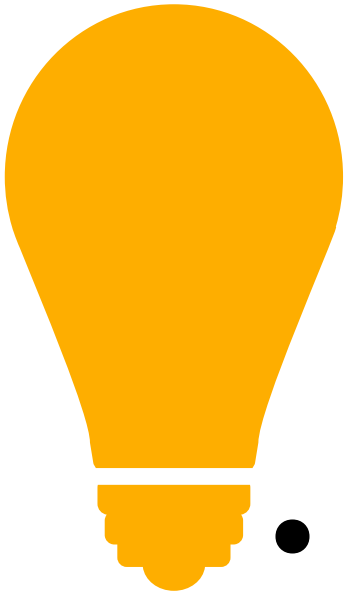
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Part of Polymath's proof is machine-checked

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