

From Random Probing to Noisy Leakages Without Field-Size Dependence

Gianluca Brian
ETH Zurich
Switzerland

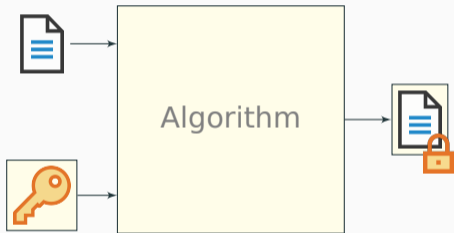
Stefan Dziembowski
University of Warsaw
Poland

Sebastian Faust
TU Darmstadt
Germany

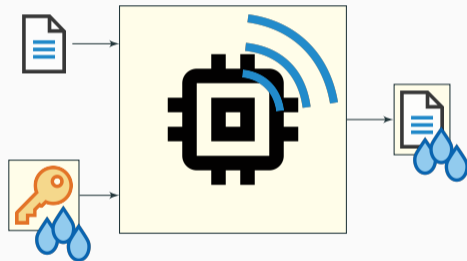
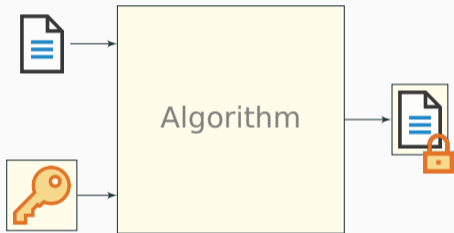
Eurocrypt 2024

Zurich, Switzerland

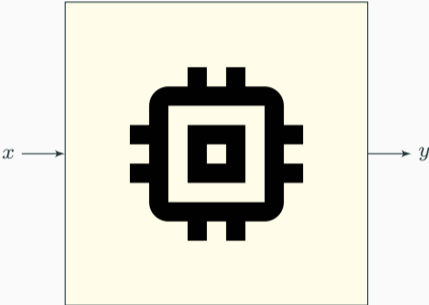
Introduction



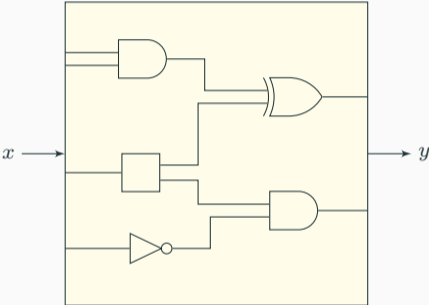
Introduction



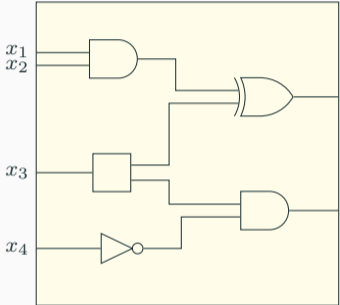
Computation



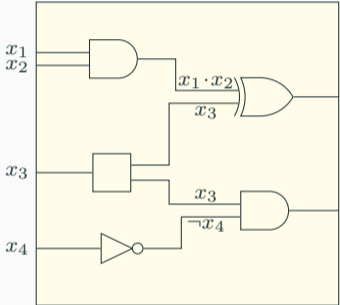
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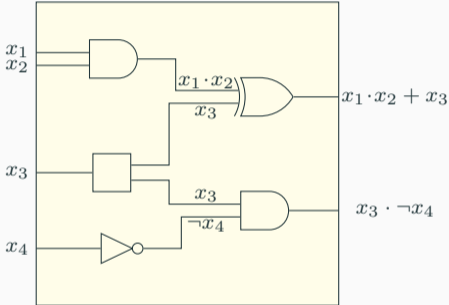
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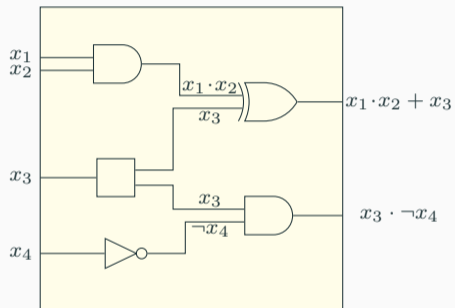
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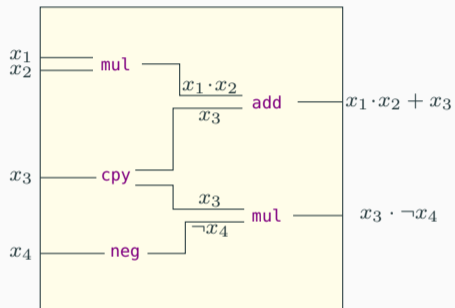


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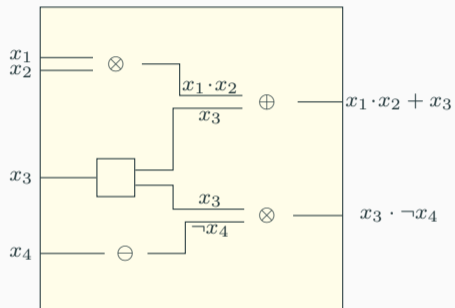
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Computation



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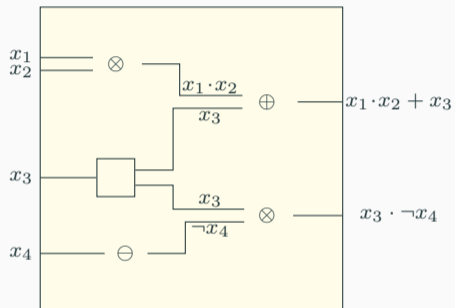
Computation



- Computation in a field \mathbb{F}

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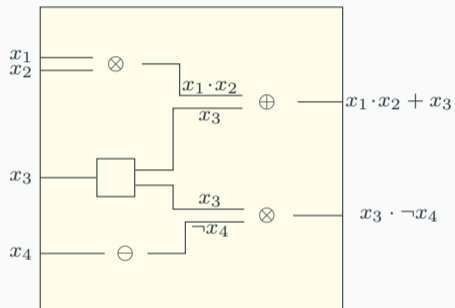
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- Computation in a field \mathbb{F}
- Addition: \oplus
- Subtraction: \ominus
- Multiplication: \otimes

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Computation



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- Copy: \square
- Random: $\$$

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t -probing [ISW03]

- Simple
- Limited amount of probes

Random Probing

Average Probing

Noisy Leakages

- Most realistic
- Difficult to achieve

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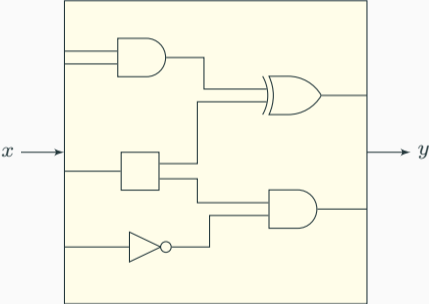
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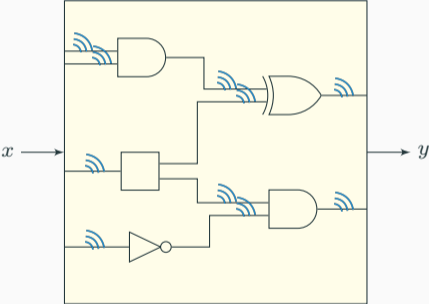
Leakage models

Random probing



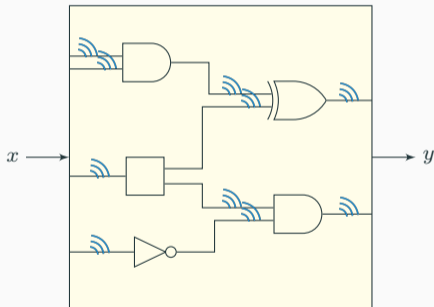
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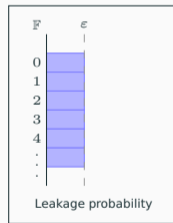
Leakage models

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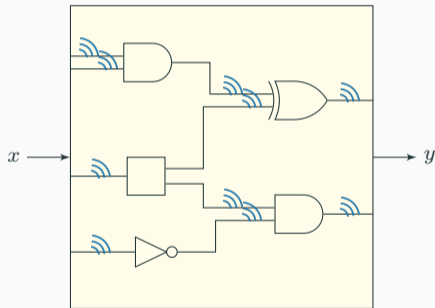
- Probing function $\mathbb{R} = \rho$
- Random probing:

$$\forall w, \Pr[\rho(w) = w] = \epsilon$$



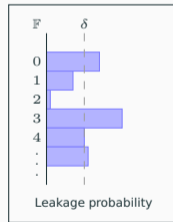
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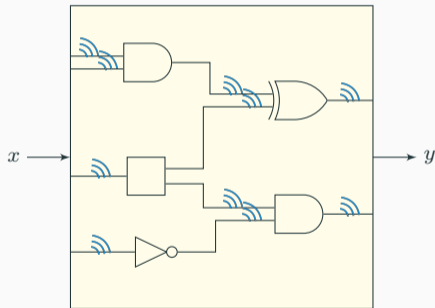
- Probing function $\alpha = \alpha$
- **Average** probing [DFS15]:

For uniform \mathbf{U} , $\Pr[\alpha(\mathbf{U}) = \mathbf{U}] = \delta$



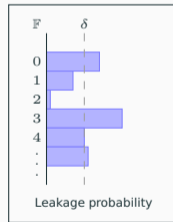
Leakage models

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- **Reveals more information!**

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Random Probing



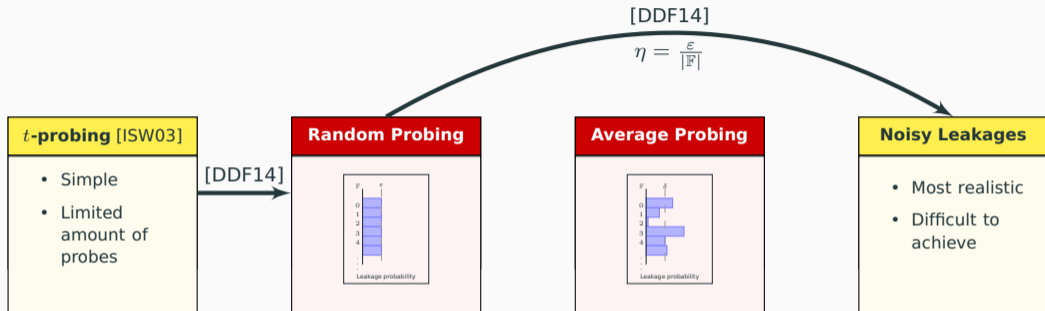
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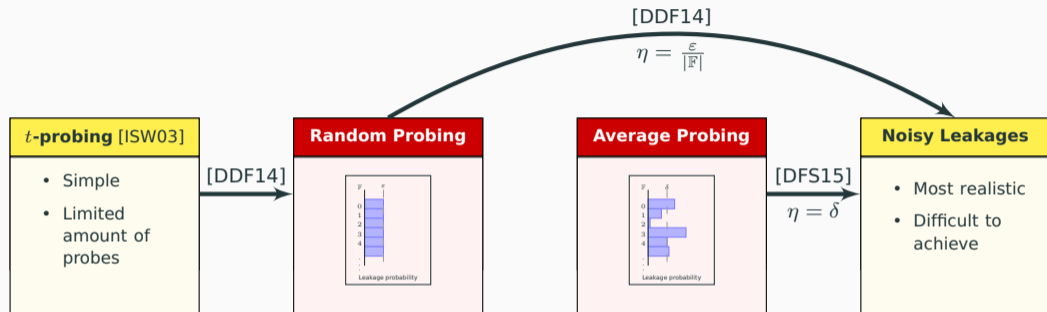
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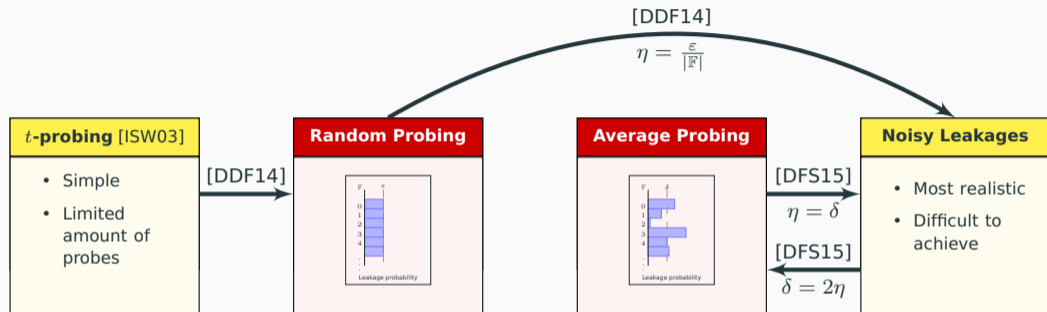


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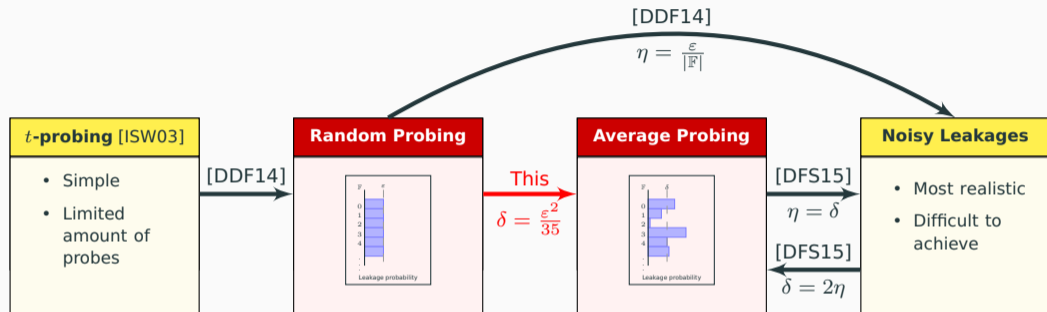


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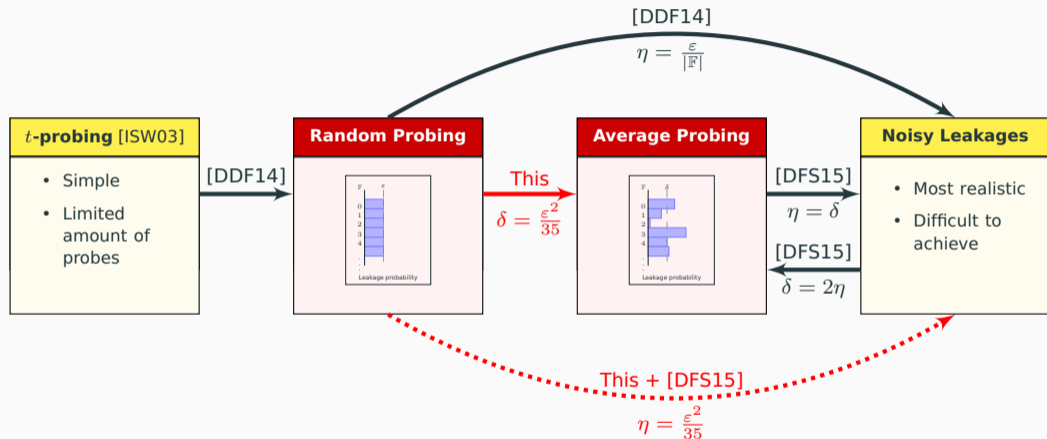


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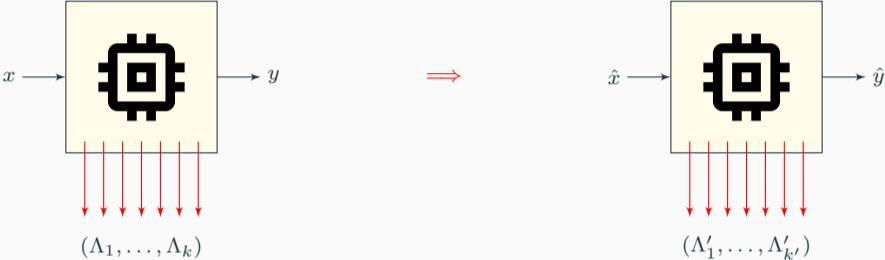
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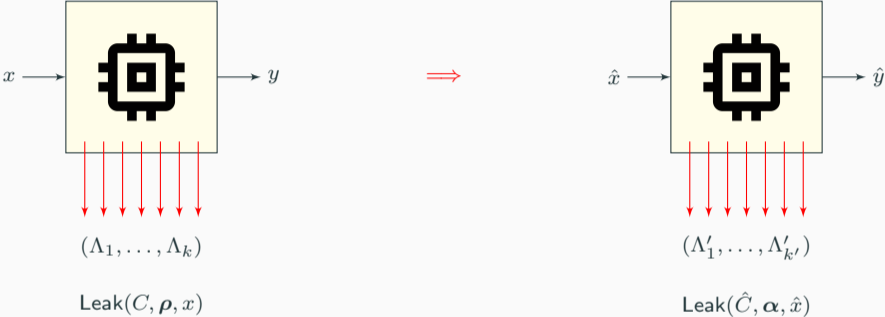
The simulation paradigm



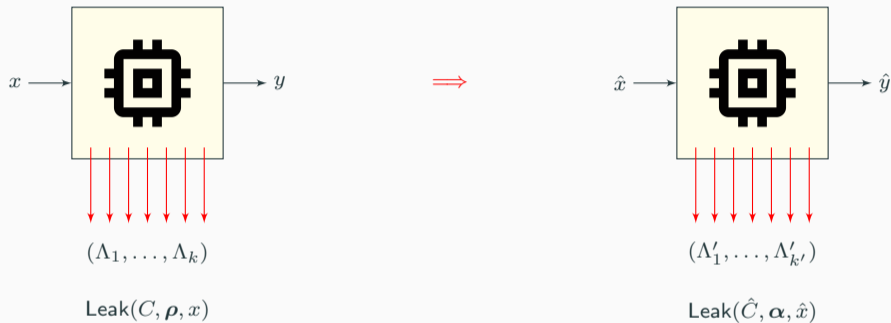
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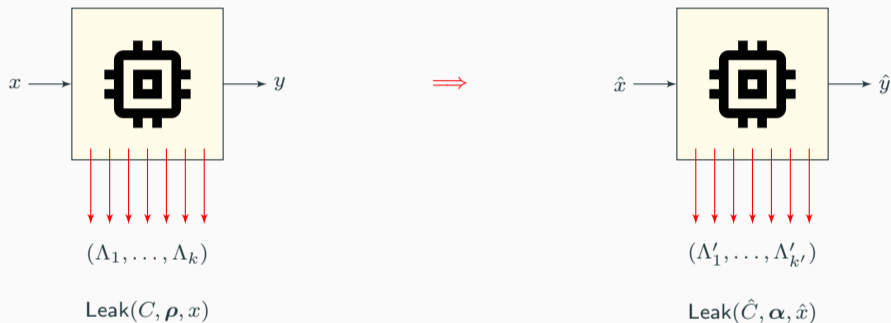


The simulation paradigm



Simulatability: there exists a simulator Sim such that $\text{Sim}(\text{Leak}(C, \rho, x)) \equiv \text{Leak}(\hat{C}, \alpha, \hat{x})$.

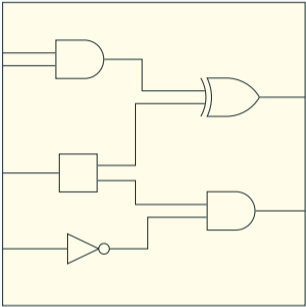
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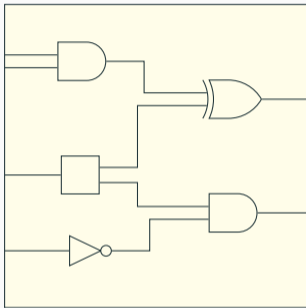


Simulatability: there exists a simulator Sim such that $\text{Sim}(\text{Leak}(C, \rho, x)) \equiv \text{Leak}(\hat{C}, \alpha, \hat{x})$.

Security: if C is ε -**random**-probing secure and leakage from \hat{C} is perfectly simulatable, then \hat{C} is δ -**average**-probing secure.

Our compiler





Compiler
→

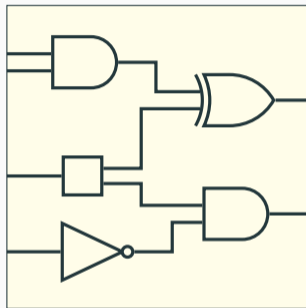
Masking:

$$x \mapsto \hat{x} = (x_1, x_2)$$

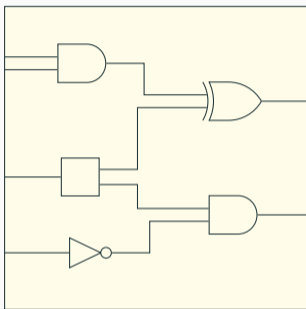
$$x_1 + x_2 = x$$

$$g \mapsto \hat{g}$$

$$g(x) = \text{Dec}(\hat{g}(\hat{x}))$$



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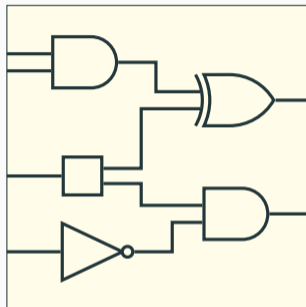
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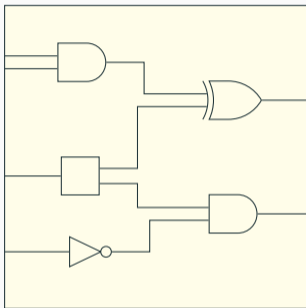
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Security proof: standard hybrid argument.

$$\text{Real}(\hat{C}, \alpha, \hat{x})$$

Our compiler



Compiler
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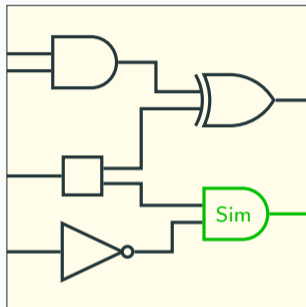
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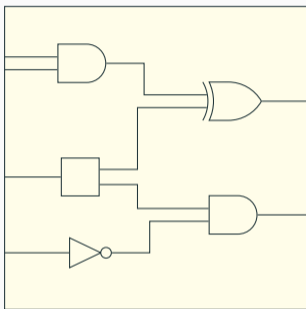
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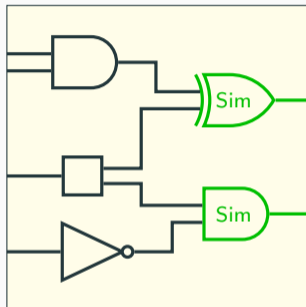
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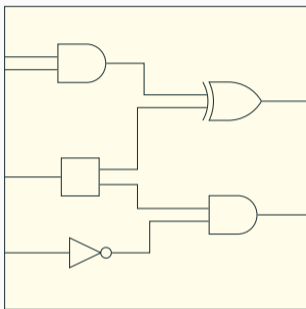
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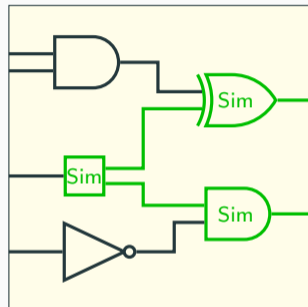
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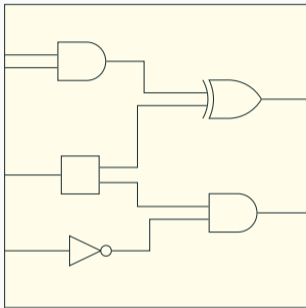
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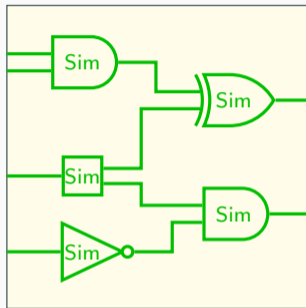
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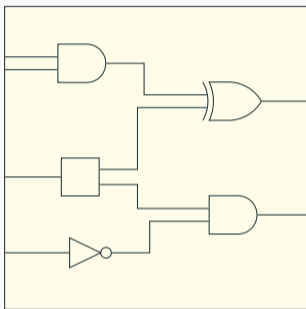
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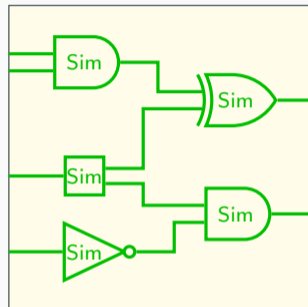
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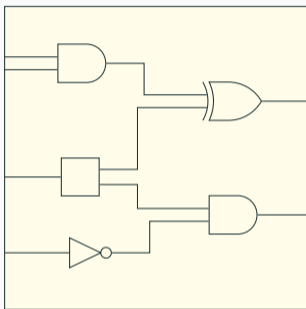
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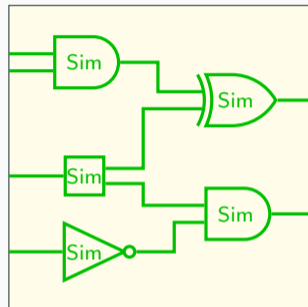
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Security proof: standard hybrid argument.

Problem: we need composable gadgets!

$$\text{Real}(\hat{C}, \alpha, \hat{x}) \equiv \text{Hyb}_1(\hat{C}, \hat{x}) \equiv \text{Hyb}_2(\hat{C}, \hat{x}) \equiv \dots \equiv \text{Hyb}_n(\hat{C}, \hat{x}) = \text{Sim}(\hat{C}, \text{Leak}(C, \rho, x))$$

Example: gate $g : \mathbb{F} \rightarrow \mathbb{F}$ with fan-in 1 and fan-out 1:

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$$\text{Real}_g(\hat{x}) \equiv \text{Sim}_g(\hat{x} \text{ if } x \text{ leaks, } \perp \text{ otherwise})$$

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Solution: give the simulator the full encoded input \hat{x} if the original input x leaks.

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Problem: what about gates with fan-in > 1 ?

Example: gate $g : \mathbb{F}^k \rightarrow \mathbb{F}$ with fan-in k and fan-out 1:

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Problem: what about gates with fan-in > 1 ?

Solution: if $x \in \mathbb{F}^k$, the simulator gets $\hat{x} \in \mathbb{F}^{2k}$ only if random probing was successful on all components of x , and \perp otherwise.

For 1 input wire:

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Gadget simulation

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Case $\Lambda \neq \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \Lambda] = 0$

Case $\Lambda = \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \perp] = 1$

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$$\iff \mathbb{P}[\text{Real}_g(\hat{x}) = \Lambda] = \varepsilon \mathbb{P}[\text{Sim}_g(\hat{x}) = \Lambda] + (1 - \varepsilon) \mathbb{P}[\text{Sim}_g(\perp) = \Lambda] \quad \varepsilon\text{-random-probing}$$

Case $\Lambda \neq \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \Lambda] = 0$

$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \Lambda] = \frac{1}{\varepsilon} \mathbb{P}[\text{Real}_g(\hat{x}) = \Lambda]$$

Case $\Lambda = \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \perp] = 1$

Gadget simulation

For 1 input wire:

$\text{Real}_g(\hat{x}) \equiv \text{Sim}_g(\hat{x} \text{ if } x \text{ leaks, } \perp \text{ otherwise})$

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Case $\Lambda = \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \perp] = 1$

$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \perp] = \frac{\mathbb{P}[\text{Real}_g(\hat{x}) = \perp] - (1 - \varepsilon)}{\varepsilon}$$

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$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \perp] = \frac{\mathbb{P}[\text{Real}_g(\hat{x}) = \perp] - (1 - \varepsilon)}{\varepsilon} \quad \text{must be } \geq 0$$

Gadget simulation

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$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \perp] = \frac{\mathbb{P}[\text{Real}_g(\hat{x}) = \perp] - (1 - \varepsilon)}{\varepsilon}$$

must be ≥ 0

$$\implies \mathbb{P}[\text{Real}_g(\hat{x}) = \perp] \geq 1 - \varepsilon \implies \mathbb{P}[\text{Real}_g(\hat{x}) \neq \perp] \leq \varepsilon$$

Gadget simulation

For 2 input wires:

$\text{Real}_g(\hat{x}) \equiv \text{Sim}_g(\hat{x} \text{ if } x \text{ leaks, } \perp \text{ otherwise})$

$$\iff \mathbb{P}[\text{Real}_g(\hat{x}) = \Lambda] = \mathbb{P}[\text{Sim}_g(\hat{x} \text{ if } x \text{ leaks, } \perp \text{ otherwise}) = \Lambda]$$

$$\iff \mathbb{P}[\text{Real}_g(\hat{x}) = \Lambda] = \varepsilon^2 \mathbb{P}[\text{Sim}_g(\hat{x}) = \Lambda] + (1 - \varepsilon^2) \mathbb{P}[\text{Sim}_g(\perp) = \Lambda] \quad \varepsilon\text{-random-probing}$$

Case $\Lambda \neq \perp$: $\mathbb{P}[\text{Sim}_g(\perp) = \Lambda] = 0$

$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \Lambda] = \frac{1}{\varepsilon^2} \mathbb{P}[\text{Real}_g(\hat{x}) = \Lambda]$$

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$$\implies \mathbb{P}[\text{Sim}_g(\hat{x}) = \perp] = \frac{\mathbb{P}[\text{Real}_g(\hat{x}) = \perp] - (1 - \varepsilon^2)}{\varepsilon^2}$$

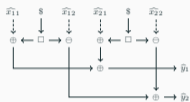
must be ≥ 0

$$\implies \mathbb{P}[\text{Real}_g(\hat{x}) = \perp] \geq 1 - \varepsilon^2 \implies \mathbb{P}[\text{Real}_g(\hat{x}) \neq \perp] \leq \varepsilon^2$$

Our compiler

Our gadgets

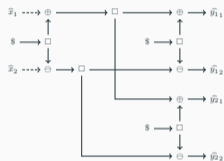
Simulation condition: $\mathbb{P}[\text{Real}_g(\hat{x}) \neq \perp] \leq \epsilon^2$.



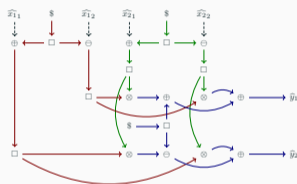
Addition gadget



Subtraction gadget



Copy gadget



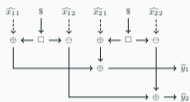
Multiplication gadget

Our compiler

Our gadgets

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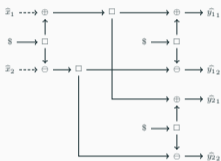
Most complex gadget: multiplication gadget, with $\mathbb{P}[\text{Real}_g(\hat{x}) \neq \perp] \leq 35\delta$.



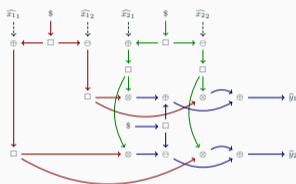
Addition gadget



Subtraction gadget



Copy gadget



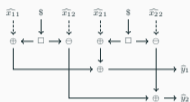
Multiplication gadget

Our compiler

Our gadgets

Simulation condition: $35\delta \leq \epsilon^2$.

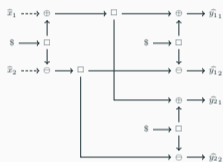
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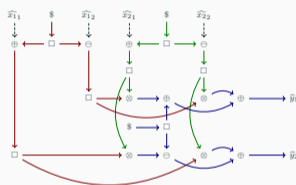
Addition gadget



Subtraction gadget



Copy gadget



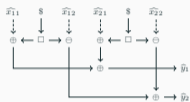
Multiplication gadget

Our compiler

Our gadgets

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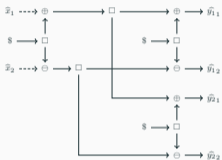
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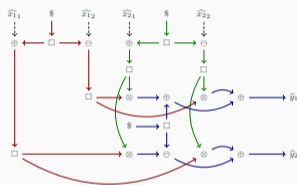
Addition gadget



Subtraction gadget



Copy gadget



Multiplication gadget

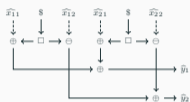
Main theorem: there exists a circuit compiler that compiles ϵ -RP secure circuits into $\frac{\epsilon^2}{35}$ -AP secure circuits.

Our compiler

Our gadgets

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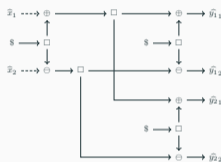
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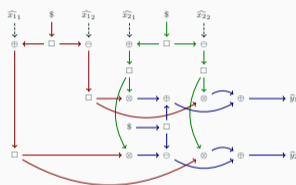
Addition gadget



Subtraction gadget



Copy gadget



Multiplication gadget

Main theorem: there exists a circuit compiler that compiles ϵ -RP secure circuits into $\frac{\epsilon^2}{35}$ -AP secure circuits.

Corollary: by applying [DFS15], there exists a circuit compiler that compiles ϵ -RP secure circuits into $\frac{\epsilon^2}{35}$ -SD-noisy-leakage resilient circuits.

Summary of our results

- We construct a compiler from ε -random probing to $\frac{\varepsilon^2}{35}$ -average probing.
- By applying [DFS15], we get a compiler from ε -random probing to $\frac{\varepsilon^2}{35}$ -noisy leakage.

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- Is it possible to do better (i.e., get rid of the square)?

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Thank you!