



Workshop on Cryptographic Hardware and Embedded Systems (CHES 2008)

www.chesworkshop.org
Washington, D.C., USA
August 10 – 13, 2008

sponsored by IACR



Call for Papers

CHES is the premier forum for presenting new results and scientific advances in all aspects of cryptographic hardware and security for embedded systems. Of special interest are contributions that describe new methods for secure and efficient hardware implementation of cryptography, hardware support for secure and trustworthy software and high-speed or leak-resistant software for embedded systems, e.g. smart cards, microprocessors, DSPs, etc. The workshop helps to bridge the gap between the cryptography research community and the application areas of cryptography. Consequently, we encourage submissions from academia, industry, and other organizations. All submitted papers will be reviewed and the conference proceedings will be published by Springer as part of the Lecture Notes in Computer Science (LNCS) series.

This will be the tenth CHES workshop. CHES '99 and CHES 2000 were held at WPI, CHES 2001 in Paris, CHES 2002 in the San Francisco Bay Area, CHES 2003 in Cologne, CHES 2004 in Boston, CHES 2005 in Edinburgh, CHES 2006 in Yokohama and CHES 2007 in Vienna. The number of participants has grown to more than 250, with attendees coming from industry, academia, and government organizations. The topics of CHES 2008 include but are not limited to:

- * Architectures for public-key & secret-key cryptosystems
- * Reconfigurable hardware & FPGAs for cryptography
- * Cryptography for ubiquitous computing and wireless applications
- * Efficient arithmetic algorithms
- * Special-purpose hardware for cryptanalysis
- * Architectures for trusted computing
- * Device identification
- * Smart card architectures and attacks
- * True and pseudorandom number generators
- * Security for embedded software and systems
- * Efficient software algorithms for embedded processors
- * Formal methods and tools for secure hardware design
- * Cryptographic processors and co-processors
- * Security in commercial consumer applications (pay-TV, automotive, etc)
- * Hardware tamper resistance
- * Technologies and hardware for content protection
- * Side channel attacks and countermeasures
- * Non-classical cryptographic technologies

Instructions for CHES Authors

Authors are invited to submit original papers via electronic submission. Details of the electronic submission procedure will be posted on the CHES webpage (<http://www.chesworkshop.org>) when the system is activated, a month before the submission deadline.

The submission must be **anonymous**, with no author names, affiliations, acknowledgments, or obvious references. It should begin with a title, a short abstract, and a list of keywords. The paper should be at most 12 pages (excluding the bibliography and clearly marked appendices), and at most 15 pages in total, using at least 11-point font and reasonable margins. Submissions not meeting these guidelines risk rejection without consideration of their merits. All submissions will be blind-refereed.

Policy against double submissions

Only original research contributions will be considered. Submissions that substantially duplicate work that any of the authors have published elsewhere, or have submitted in parallel to any other conferences or workshops that have proceedings, *will be instantly rejected*. Authors should be aware that we will co-operate with other conference committees to identify potential double-submissions and strictly enforce the IACR Policy on Irregular Submissions (<http://www.iacr.org/irregular.html>).

Important Dates

Submission deadline: **Feb 29th, 2008, 23:59 EST.**
Final version due: May 16th, 2008.

Acceptance notification: April 25th, 2008.
Workshop presentations: August 11th – 13th, 2008.

Mailing List

If you wish to receive subsequent Call for Papers and registration information, please send a brief mail to mailinglist@chesworkshop.org. Your details will only be used for sending CHES related information.

Program Committee

- Daniel V. Bailey, RSA Laboratories, USA
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Workshop Proceedings

The proceedings will be published in the Springer Lecture Notes in Computer Science (LNCS) series in time for distribution at the Workshop. Accepted papers should be formatted according to the LNCS default author instructions at URL <http://www.springer.de/comp/lncs/authors.html> (see file “typeinst.pdf”). Note that in order to be included in the proceedings, the authors of an accepted paper must guarantee to present their contribution at the workshop.