Workshop on Cryptographic Hardware and Embedded Systems 2010 (CHES 2010)

Santa Barbara, California, USA Tuesday August 17th - Friday August 20th, 2010

Program

Tuesday, August 17						
Time		Event				
18:00 - 20:00	Registration					
18:00 - 23:00	University Center Corw in Pavilion Lobby Joint Rump Session with Crypto University Center Lagoon Plaza & Corw in Pavilion (A full dinner menu w ill be served w ith the Rump Session) Possible submission at http://rump2010.cr.yp.to/submission.html					
Wednesday, August 18						
Time		Event				
	Session	Authors	Title			
		Registration				
07:30 - 08:45		Breakfast				
08:45 - 09:00	Opening Remarks University Center Corw in Pavilion					
	Session 1: Low Cost	Jean-Philippe Aumasson and Luca Henzen and Willi Meier and Maria Naya-Plasencia	Quark: a lightweight hash			
09:00 - 10:15	Cryptography Chair: Anne Canteaut	Lars Knudsen and Gregor Leander and Axel Poschmann and Matthew J.B. Robshaw	PRINTcipher: A Block Cipher for IC-Printing			
	University Center Corw in Pavilion	Guido Bertoni and Joan Daemen and Michaël Peeters and Gilles Van Assche	Sponge-based pseudo-random number generators			
10:15 - 10:45	Morning Break University Center Lagoon Plaza					
	Session 2: Efficient Implementations I	Nicolas Guillermin	A high speed coprocessor for elliptic curve scalar multiplications over Fp			
10:45 - 12:00	Chair: Tanja Lange	Raveen R. Goundar and Marc Joye and Atsuko Miyaji	Co-Z Addition Formulae and Binary Ladders on Elliptic Curves			
	University Center Corw in Pavilion	Patrick Longa and Catherine Gebotys	Efficient Techniques for High-Speed Elliptic Curve Cryptography			
12:00 - 14:00		Lunch Storke Plaza				
	Session 3: Side-Channel Attacks	Jean-Sebastien Coron and Ilya Kizhvatov	Analysis and Improvement of the Random Delay Countermeasure of CHES 2009			
14:00 - 15:40	& Countermeasures I Chair: Emmanuel Prouff University Center Corw in	Onur Aciicmez and Billy Bob Brumley and Philipp Grabher	New Results on Instruction Cache Attacks			
		Amir Moradi and Oliver Mischke and Thomas Eisenbarth	Correlation-Enhanced Power Analysis Collision Attack			
	Pavilion	Olivier Benoît and Thomas Peyrin	Side-channel Analysis of Six SHA-3 Candidates			
15:40 - 16:10	Crypto/CHES Joint Afternoon Break Outside Camobell Hall					
16:10 - 17:10	Invited Talk I: (joint session with Crypto) Chair: Bart Preneel	Ivan Damgård (Aarhus University) and David Naccache	Is Theoretical Cryptography Any Good in Practice?			
17:15 - 18:00	IACR Membership Meeting Campbell Hall					
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18:00 - 20:15	Beach Barbecue (joint with Crypto) Goleta Beach
20:00 - 22:30	Crypto Café (joint with Crypto) Anacapa Formal Lounge

20:00 - 22:30	(<mark>joint with Crypto)</mark> Anacapa Formal Lounge						
Thursday, August 19							
Time		Event					
	Session	Authors	Title				
		Registration					
07:30 - 08:45	Breakfast						
	Session 4: Tamper Resistance	Sergei Skorobogatov	Flash Memory 'Bumping' Attacks				
9:00 - 10:15	& HW Trojans Chair: Ingrid	Dongdong Du and Seetharam Narasimhan and Rajat Subhra Chakraborty and Swarup Bhunia	Self-Referencing: A Scalable Side-Channel Approach for Hardware Trojan Detection				
9.00 - 10.13	Verbauwhede University Center Corw in Pavilion	Jérôme Di-Battista and Jean-Christophe Courrège and Bruno Rouzeyre and Lionel Torres and Philippe Perdu	When Failure Analysis Meets Side-Channel Attacks				
10:15 - 10:45	Morning Break University Center Lagoon Plaza						
	Session 5: Efficient Implementations II	Charles Bouillaguet and Hsieh-Chung Chen and Chen-Mou Cheng and Tony Tung Chou and Ruben Niederhagen and Adi Shamir and Bo-Yin Yang	Fast Exhaustive Search for Polynomial Systems in F_2				
10:45 - 12:00	Chair: Kris Gaj	Axel Poschmann and Huaxiong Wang and San Ling	256 bit Standardized Crypto for 650 GE - GOST Revisited				
	University Center Corw in Pavilion	Yasuyuki Nogami and Kenta Nekado and Tetsumi Toyota and Naoto Hongo and Yoshitaka Morikawa	Mixed Bases for Efficient Inversion in F_{((2^2)^2)^2} and Conversion Matrices of SubBytes of AES				
12:00 - 14:00	Lunch Storke Plaza						
	Session 6:	Luca Henzen and Pietro Gendotti and Patrice Guillet and Enrico Pargaetzi and Martin Zoller and Frank K. Gürkaynak	Developing a Hardware Evaluation Method for SHA-3 Candidates				
14:00 - 15:40	SHA 3 Chair: Akashi Satoh	Kris Gaj, Ekawat Homsirikamol, Marcin Rogawski	Fair and Comprehensive Methodology for Comparing Hardware Performance of Fourteen Round Two SHA-3 Candidates using FPGAs				
	University Center Corw in Pavilion	Joppe W. Bos and Deian Stefan	Performance Analysis of the SHA-3 Candidates on Exotic Multi-Core Architectures				
		Christian Wenzel-Benner and Jens Graef	XBX: eXternal Benchmarking eXtension for the SUPERCOP crypto benchmarking framework				
15:40 - 16:10		Afternoon Break University Center Lagoon Plaz	a				
	Session 7: Fault Attacks & Countermeasures	Alexandre Berzati and Cécile Canovas-Dumas and Louis Goubin	Public Key Perturbation of Randomized RSA Implementations				
16:10 - 17:00	Chair: Marc Joye University Center Corw in Pavilion	Yang Li and Kazuo Sakiyama and Shigeto Gomisawa and Toshinori Fukunaga and Junko Takahashi and Kazuo Ohta	Fault Sensitivity Analysis				
17:45 starting	I aviiion	Shuttle Service UCSB - Four Season					
	Buses Depart in front of Anacapa Residence Hall						
18:15 - 18:45	Reception Four Seasons Biltmore						
18:45 - 20:00	Awards Dinner Four Seasons Biltmore						
20:00 - 22:00	Rump Session Four Seasons Biltmore						
Friday, August 20							
Time		Event					
	Session	Authors	Title				

Registration

07:30 - 08:45	Breakfast		
09:00 - 10:15	Session 8: PUFs and RNGs	Maximilian Hofer and Christoph Boehm	An Alternative to Error Correction for SRAM-Like PUFs
	Chair: Guido Bertoni	Michal Varchola and Milos Drutarovsky	New High Entropy Element for FPGA Based True Random Number Generators
	University Center Corw in Pavilion	Daisuke Suzuki and Koichi Shimizu	The Glitch PUF: A New Delay-PUF Architecture Exploiting Glitch Shapes
10:15 - 10:45	Morning Break University Center Lagoon Plaza		
	Invited Talk II		
10:45 - 11:45	Chair: François- Xavier Standaert	Hovav Shacham (University of California San Diego)	Cars and Voting Machines: Embedded Systems in the Field
	University Center Corw in Pavilion		
11:45 - 13:30	Lunch Storke Plaza		
	Session 9: New Designs	Kimmo Jäarvinen and Vladimir Kolesnikov and Ahmad-Reza Sadeghi and Thomas Schneider	Garbled Circuits for Leakage-Resilience: Hardware Implementation and Evaluation of One-Time Programs
13:30 - 14:20	Chair: Lejla Batina	Stéphane Badel and Nilay Dağtekin and Jorge Nakahara Jr	ADMADII I O a Multi Durmaca Cromtagraphia
	University Center Corw in Pavilion	and Khaled Ouafi and Nicolas Reffé and Pouyan Sepehrdad and Petr Sušil and Serge Vaudenay	ARMADILLO: a Multi-Purpose Cryptographic Primitive Dedicated to Hardware
14:20 - 14:50	Afternoon Break University Center Lagoon Plaza		
	Session 10: Side-Channel Attacks & Countermeasures II	Matthieu Rivain and Emmanuel Prouff	Provably Secure Higher-Order Masking of AES
14:50 - 16:05	Chair: Wieland Fischer	Yossef Oren and Mario Kirschbaum and Thomas Popp and Avishai Wool	Algebraic Side-Channel Analysis in the Presence of Errors
	University Center Corw in Pavilion	Michael Tunstall and Marc Joye	Coordinate Blinding over Large Prime Fields
16:05 - 16:15	Concluding Remarks University Center Corw in Pavilion		

Financial Support













Jens-Peter Kaps Last update: August 16th, 2010.